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Section:

Group ID:

CMPE 240 Experiment 4 Preliminary Work

Truth Table:

#	CLK	X	Y	Z
0	0	0	0	
1	1	0	0	0000
2	0	0	1	0000
3	1	0	1	0011
4	0	1	0	0011
5	1	1	0	0110
6	0	1	1	0110
7	1	1	1	1001

List of components used in the design:

1. D-Flip Flop (2) -> 74HC74 (1)

2. AND (2) -> 74LS11 (1)

3. NAND (4) -> 74HC132 (1)

...

Circuit:

