Student Names: Esad Yusuf Atik – Orkun Mahir Kılıç
Section: 12
Group ID: 9

CMPE 240 Experiment 3 Preliminary Work

Truth Table:

#	A	B	C	D	E	Alarm
0	0	0	0	0	0	LOW
1	0	0	0	0	1	LOW
2	0	0	0	1	0	HIGH
3	0	0	0	1	1	HIGH
4	0	0	1	0	0	LOW
5	0	0	1	0	1	LOW
6	0	0	1	1	0	HIGH
7	0	0	1	1	1	LOW
8	0	1	0	0	0	LOW
9	0	1	0	0	1	HIGH
10	0	1	0	1	0	LOW
11	0	1	0	1	1	LOW
12	0	1	1	0	0	LOW
13	0	1	1	0	1	HIGH
14	0	1	1	1	0	LOW
15	0	1	1	1	1	LOW
16	1	0	0	0	0	HIGH
17	1	0	0	0	1	LOW
18	1	0	0	1	0	HIGH
19	1	0	0	1	1	HIGH
20	1	0	1	0	0	LOW
21	1	0	1	0	1	HIGH
22	1	0	1	1	0	LOW
23	1	0	1	1	1	LOW
24	1	1	0	0	0	HIGH
25	1	1	0	0	1	HIGH
26	1	1	0	1	0	LOW
27	1	1	0	1	1	LOW
28	1	1	1	0	0	HIGH

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29	1	1	1	0	1	HIGH
30	1	1	1	1	0	LOW
31	1	1	1	1	1	HIGH

List of components used in the design:

1. MUX (8 input)

2. AND gate x 2

3. NOT gate x 4

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Circuit:

ABC