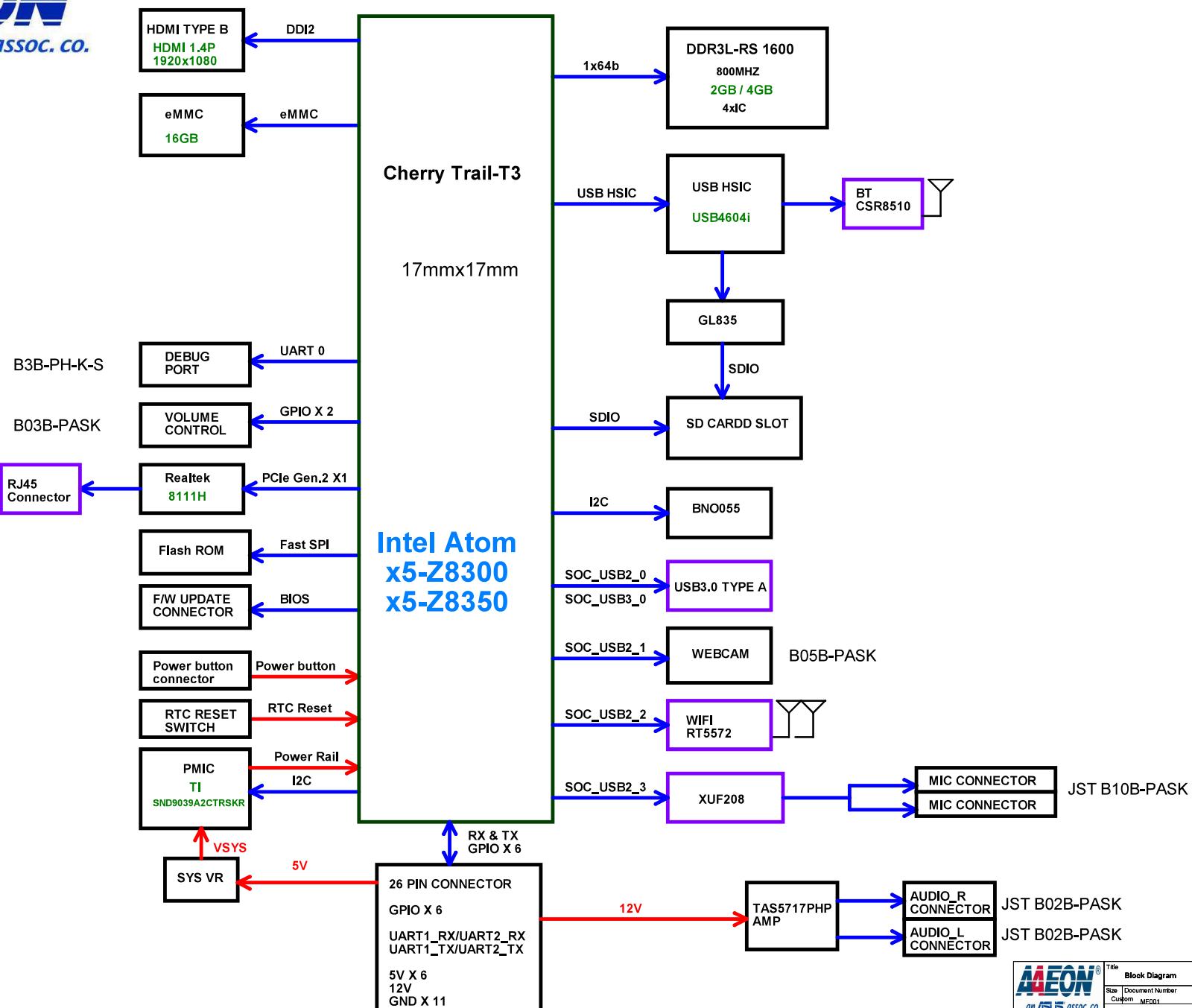


01	INDEX	21	LEVEL SHIFT CIRCUITRY	41	HISTORY	
02	BLOCK DIAGRAM	22	SPI FLASH ROM			
03	POWER TREE	23	EMMC			d
04	POWER SEQUENCE	24	BNO055			
05	SOC MEMORY	25	WIFI_RT5572N			
06	SOC POWER1	26	BT_CSR CRS8510			
07	SOC DISPLAY/CAMERA	27	USB3.0_TYPE-A			
08	SOC PCIE/SDMMC/I2S AUDIO	28	USB HSIC			
09	SOC USB/HSIC/I2C/ISH	29	RTL8111G-CG			c
10	SOC CLK/SPI/GPIO/HSUART	30	EDP TO HDMI			
11	SOC GND	31	AUDIO1			
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13	SOC DECOUPLING CAPS	33	FUNCTION CONN			
14	SOC POWER TRIBUTARIES	34	3V VOLTAGE REGULATOR			
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16	HARDWARE STRAP	36	PMIC 2of3/SWITCHING POWER			
17	POWER & RESET PIN HEADER	37	PMIC 1of3/CLDO			
18	MEMORY 1/2	38	VTT VOLTAGE REGULATOR			
19	MEMORY 3/4	39	DC IN 26 PIN CONNECTOR			
20	DDR3L-RS VREF&Termination	40	MECHANICAL COMPONENT			

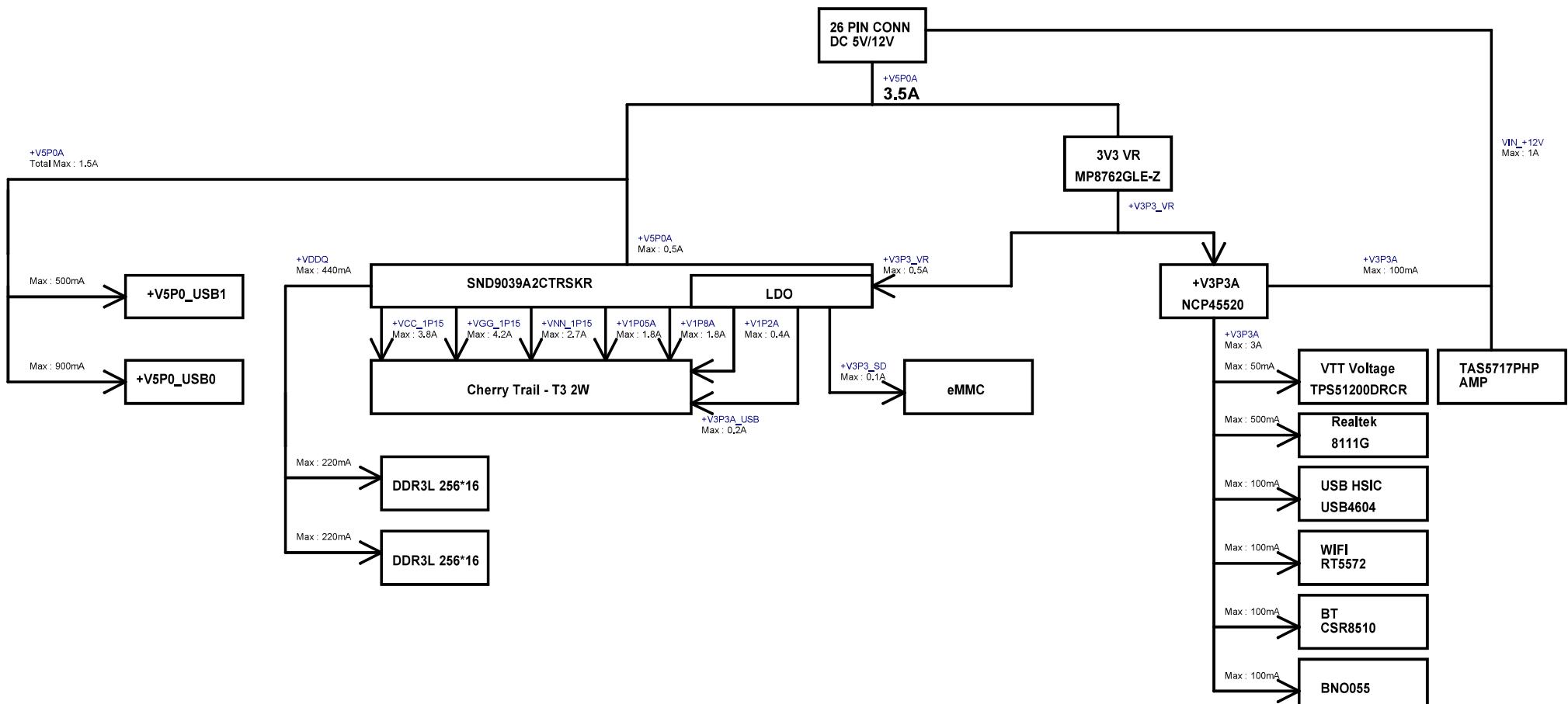


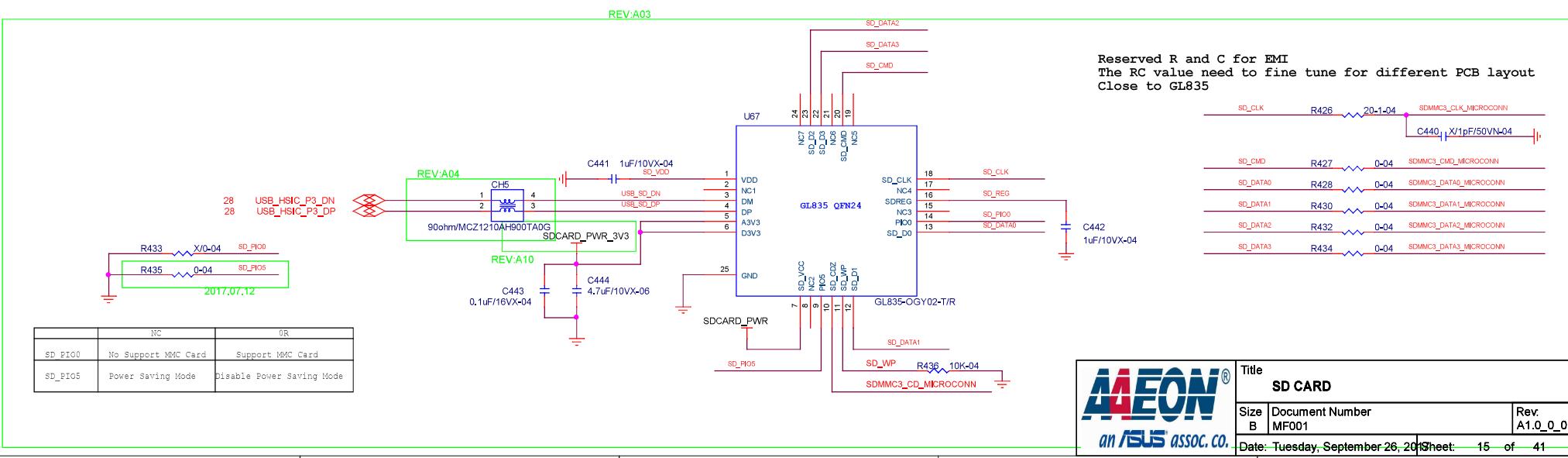
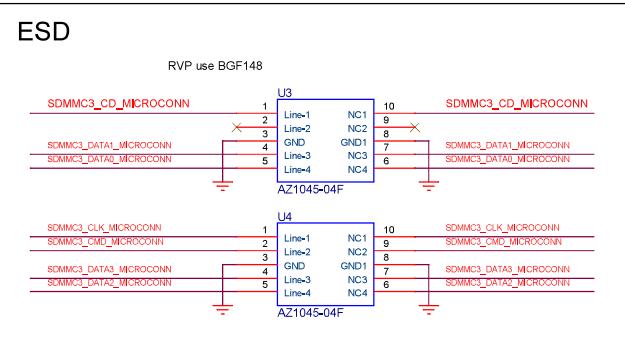
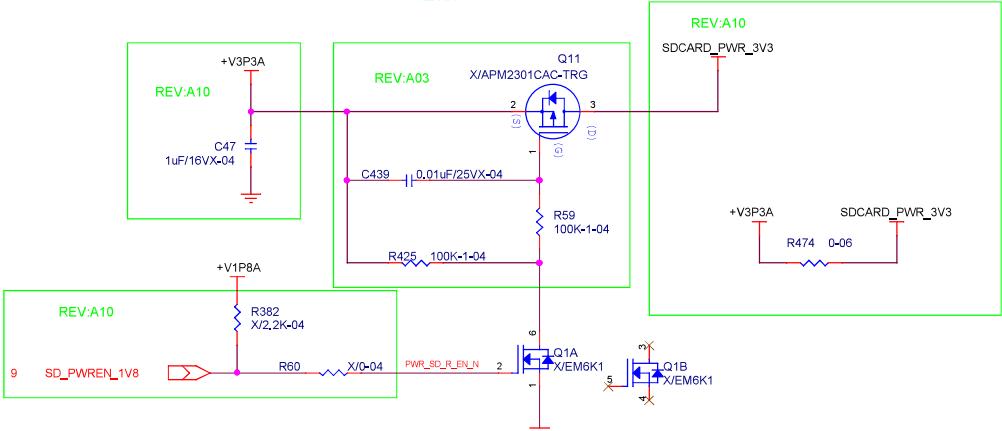
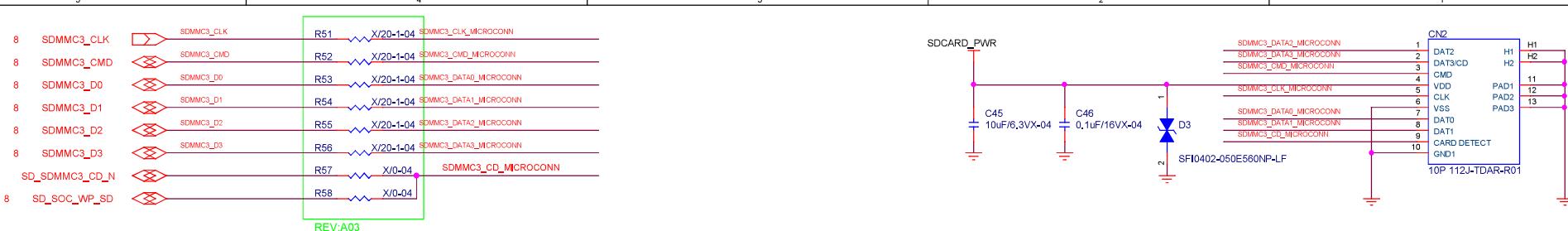
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Size	Document Number	Rev:
A	MF001	A1.0_0_0
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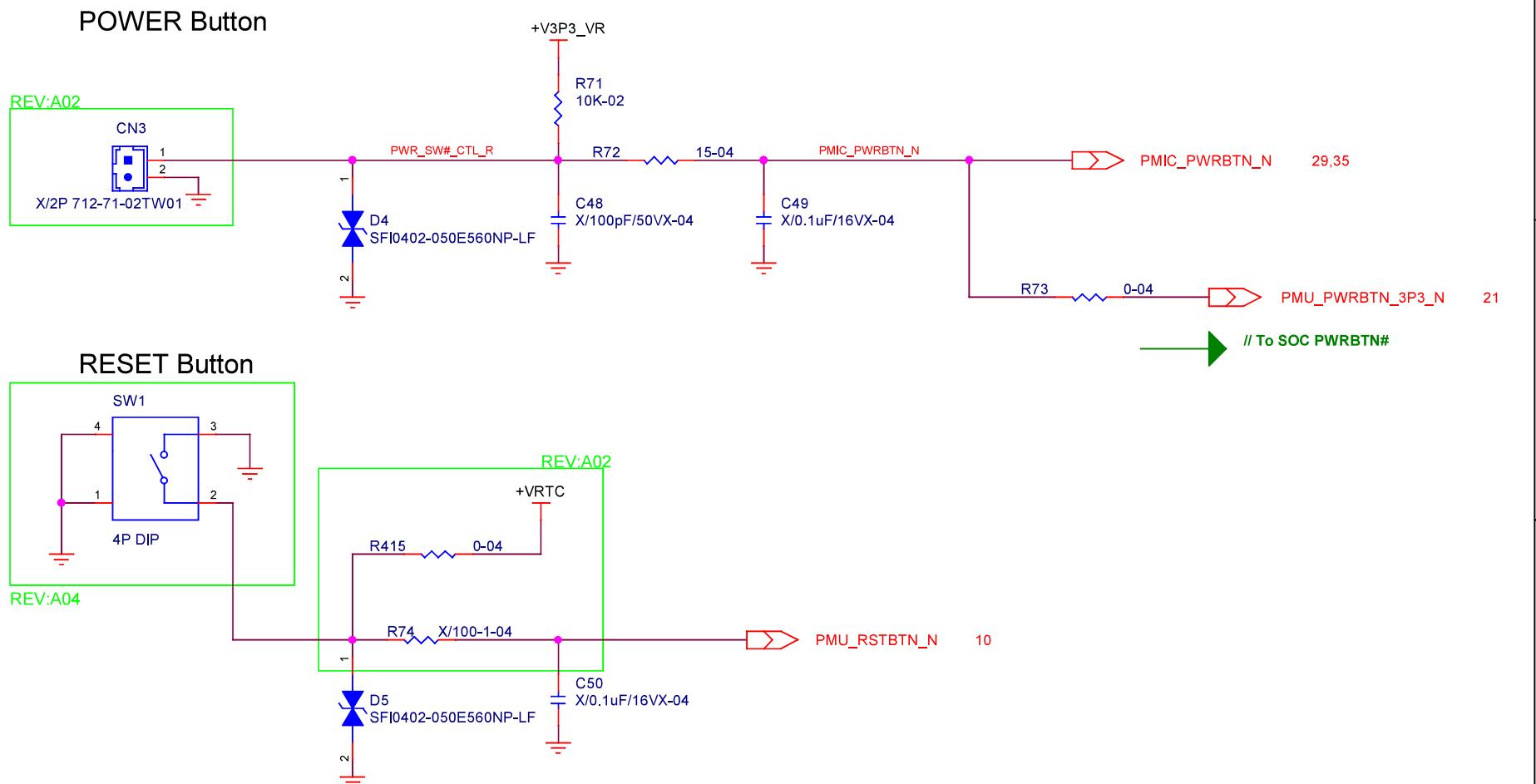
Block Diagram



Power Tree



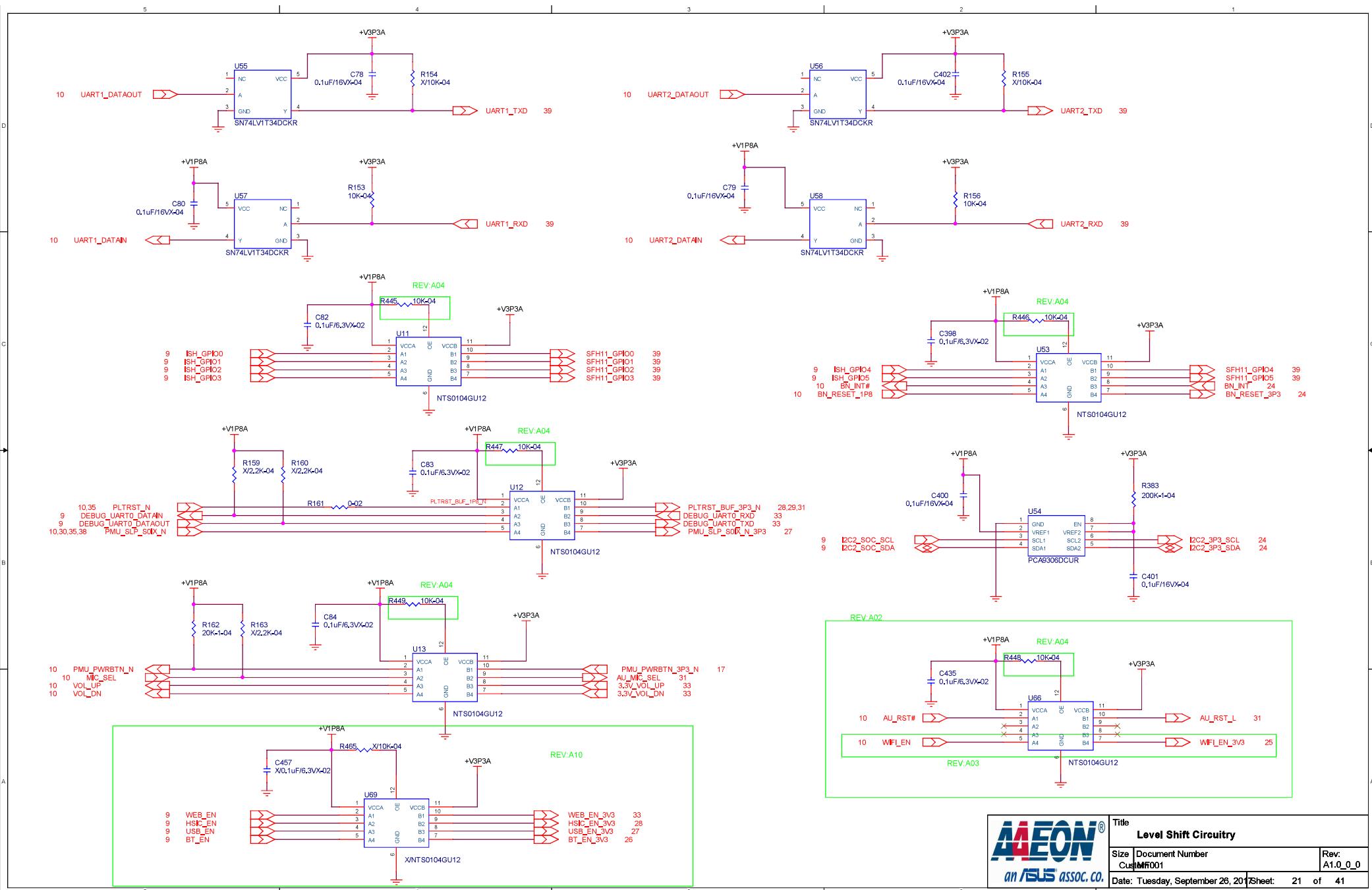


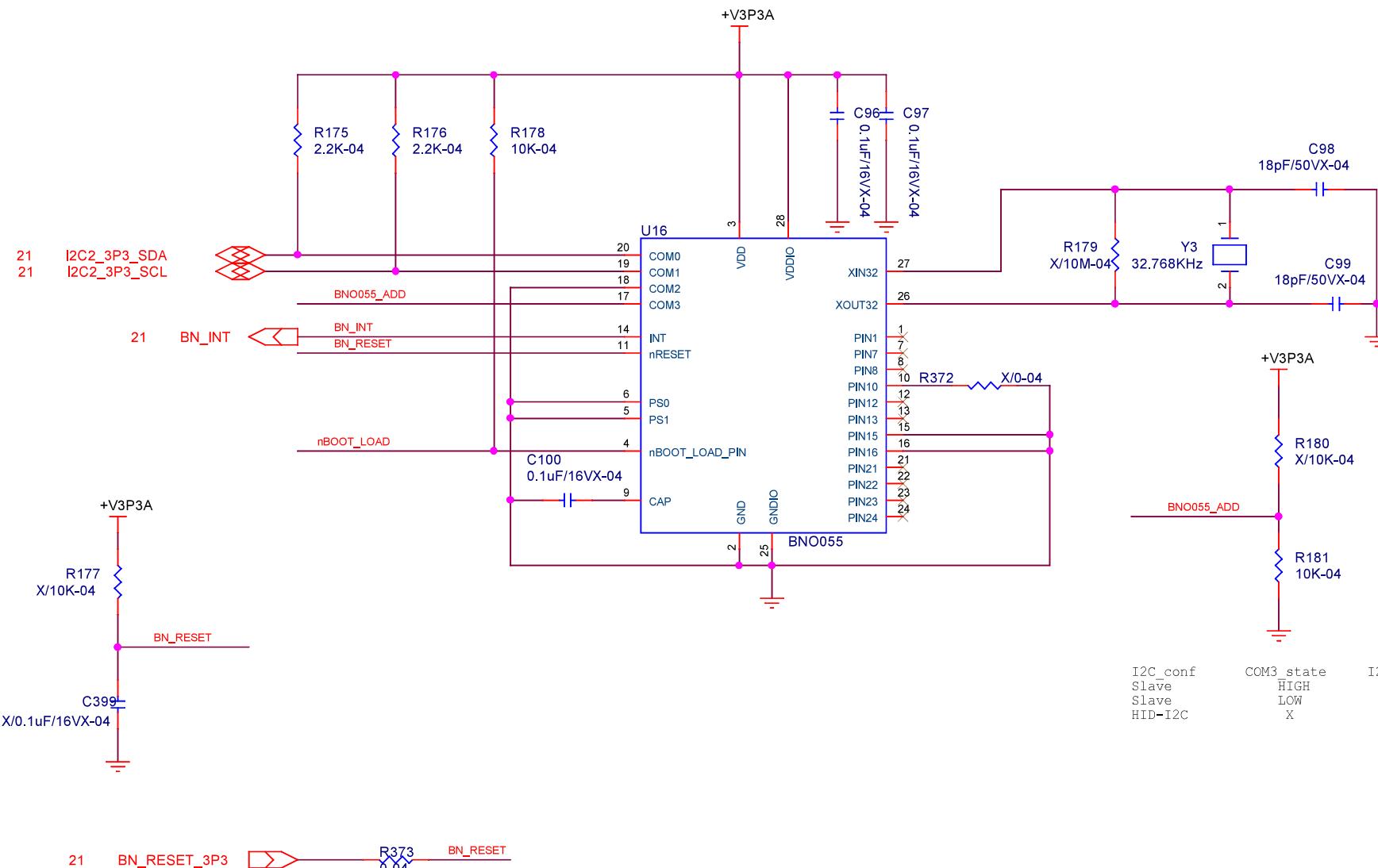


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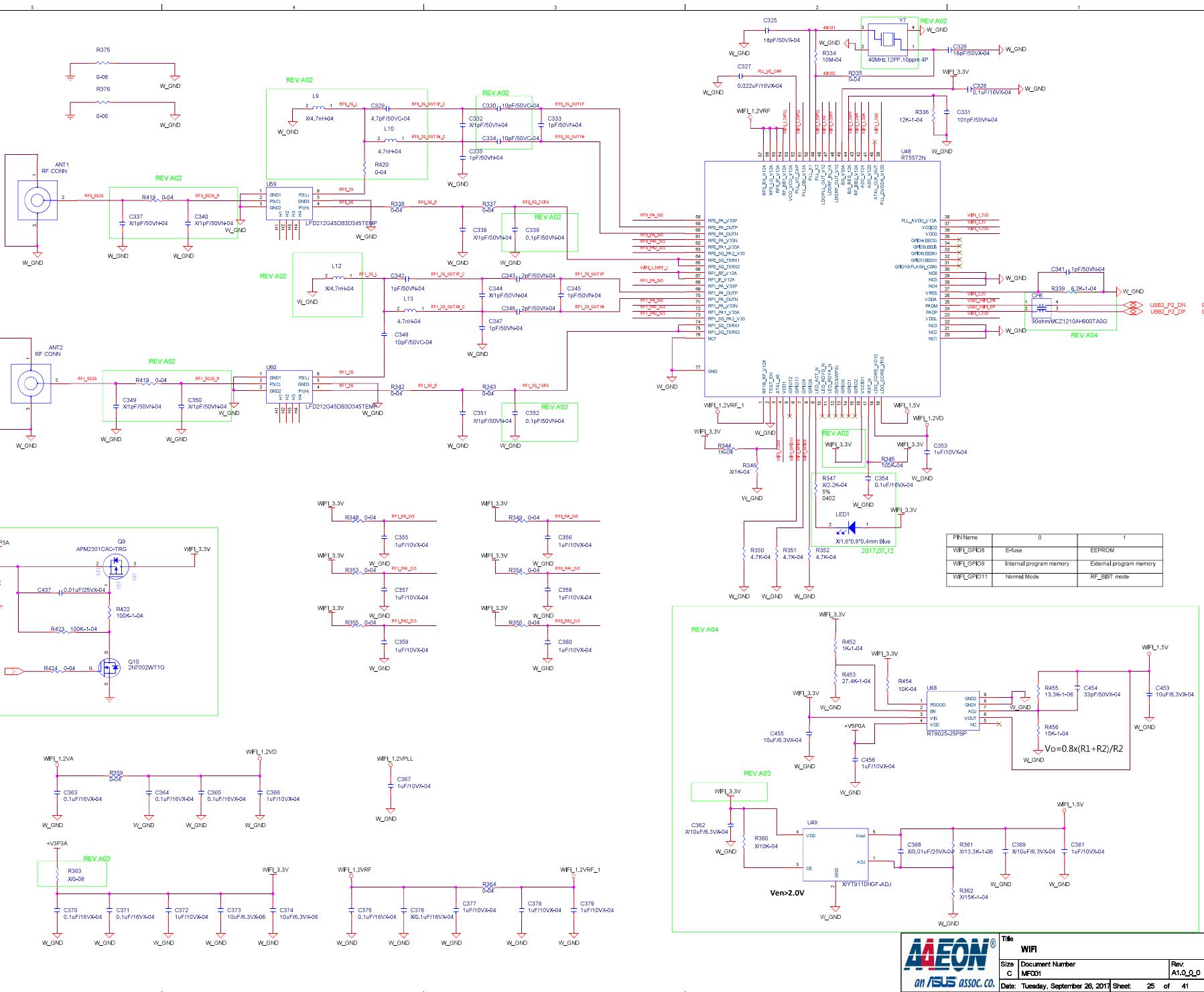
I₂C_conf
Slave
Slave
HID-I₂C

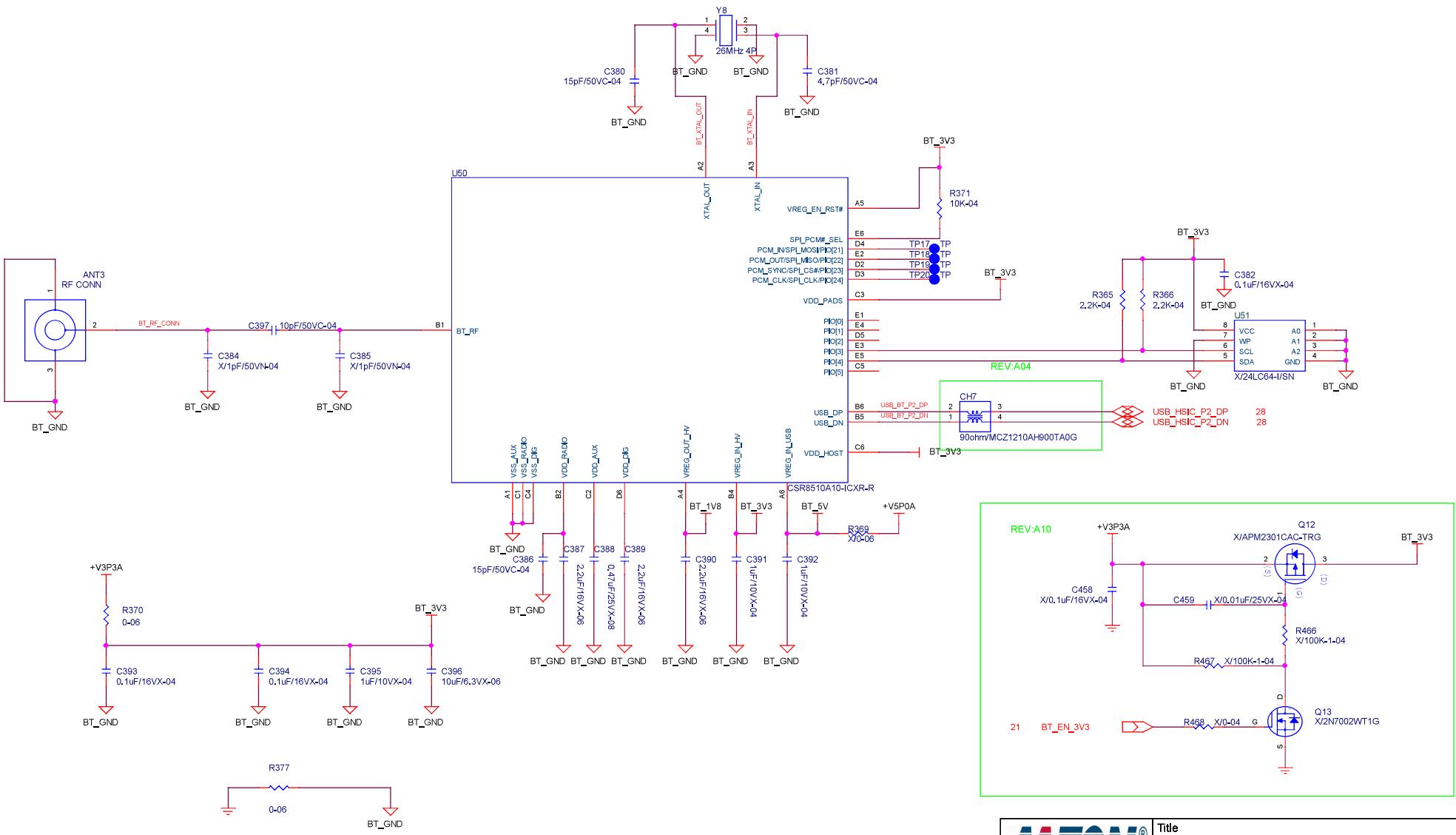
COM3_state
HIGH
LOW
X

I₂C_address
0x29
0x28
0x40

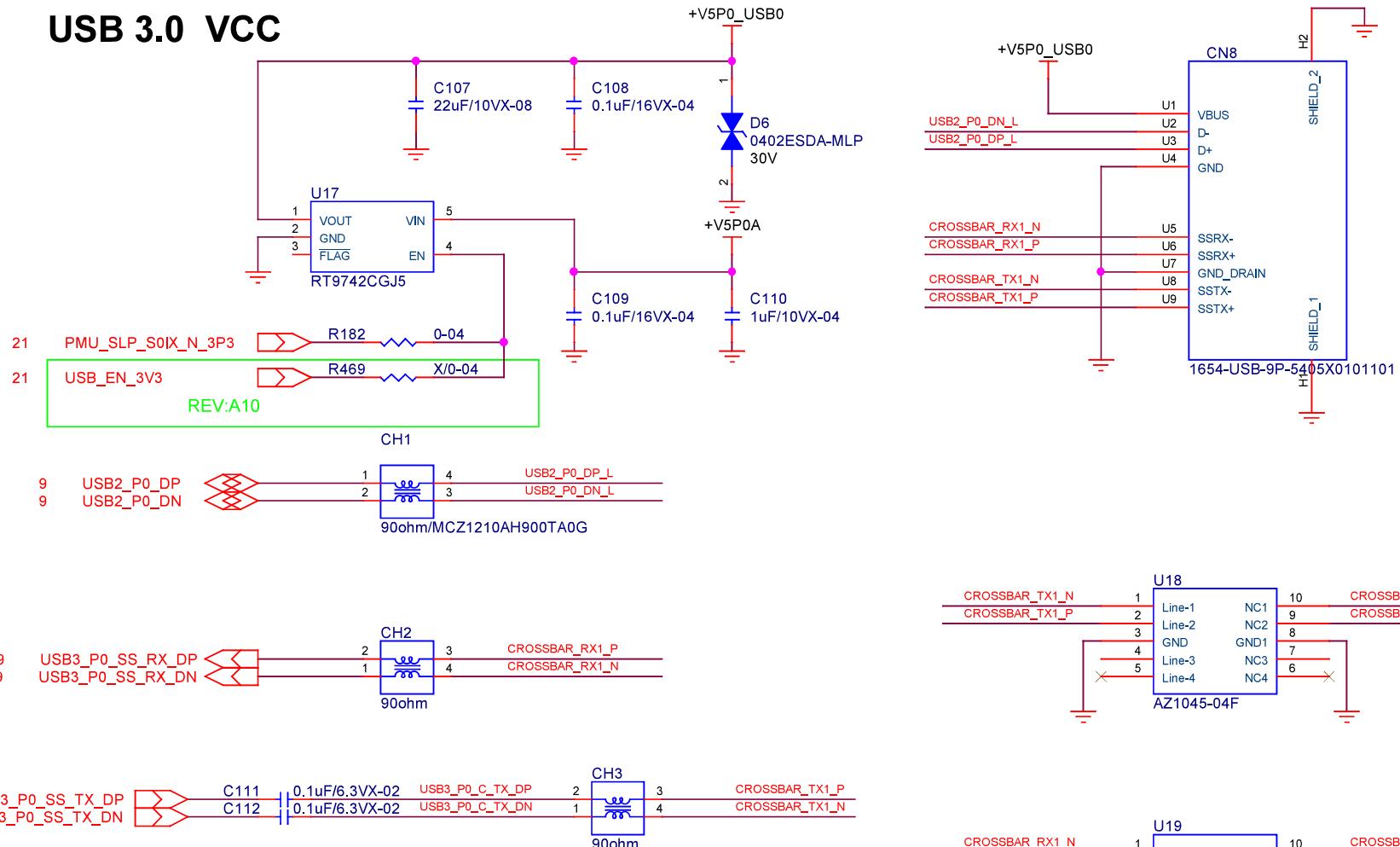


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USB 3.0 VCC



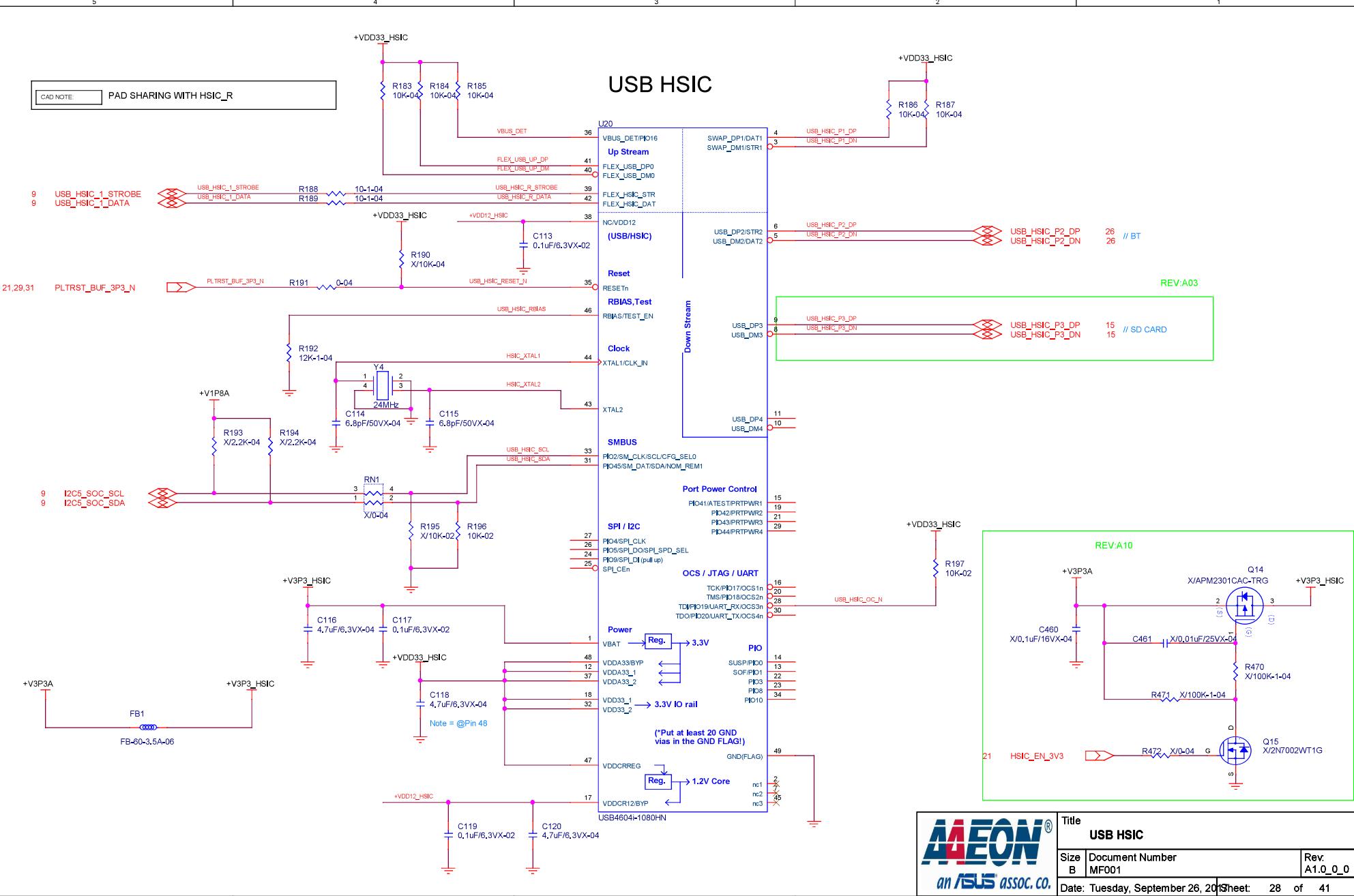
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Size: **A** Document Number: **MF001** Rev: **A1.0_0_0**

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USB HSIC



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