# UltraFast Design Methodology Checklist Demo Script

### Introduction

In this demonstration, you will be introduced to the UltraFast™ Design Methodology Checklist, which is designed to answer common questions and recommend good FPGA design and debugging techniques.

### **Preparation:**

Required files: None

Required hardware: None

 Required tools: Document Navigator (DocNav), which is installed along with the Vivado® Design Suite

• Supporting materials: None

# **UltraFast Design Methodology Checklist**

	Action with Description	Point of Emphasis and Key Takeaway
•	Launch the Vivado Design Suite.	
•	Select <b>Help</b> > <b>Documentation and tutorials</b> to open the Documentation Navigator.	The Documentation Navigator is an intuitive and powerful tool for navigating, viewing, and searching
•	If the Documentation Navigator update pop-up appears, click <b>OK.</b>	<ul><li>our technical documentation</li><li>The Documentation Navigator is the</li></ul>
•	Click the <b>Update Catalog</b> option from the Documentation Navigator.	place where you can access all documentation and videos.

Action with Description	Point of Emphasis and Key Takeaway
Explore the Catalog View tab and document filters.	The Catalog View tab allows you to access documents of interest.
<ul> <li>To access the UltraFast Design         Methodology Guide for the Vivado         Design Suite:         <ul> <li>Select Vivado under the Design</li></ul></li></ul>	<ul> <li>It is important to understand that at least one device, design tool, or IP must be selected. Likewise, at least one document type and one functional category must be selected to find a document.</li> <li>All accessed documents will be opened within the Documentation Navigator.</li> <li>The Documentation Navigator is also allowed to download the documents.</li> <li>Update the Documentation Navigator if needed.</li> </ul>
<ul> <li>Right-click any document and notice the options.</li> </ul>	Navigator if needed.
<ul> <li>Explore the Design Hub View tab:</li> <li>Select System-Level Design         Flow under the Vivado Design         Hubs category.</li> <li>Select Logic Synthesis in the</li> </ul>	<ul> <li>The Design Hub View tab provides a set of commonly used documentation and videos for quick access.</li> <li>The System-Level Design Flow</li> </ul>
Design Flow Overview flow chart.	provides access to documentation for various design stages in one

place.

one place.

 When you click on any design stage in the Design Flows, the respective design stage page opens and gives access to all the documentation in

### **Action with Description**

- **Point of Emphasis and Key Takeaway**
- Create a design checklist:
  - Click Create Design Checklist under the Vivado Design Checklists category in the Design Hub View tab.
  - Leave the Display Label as "My Methodology Checklist" in the New Design Checklist dialog box and fill in the rest of the details.
  - Click OK to create My Methodology Checklist.

- You can have multiple design checklists with the Documentation Navigator.
- Each checklist corresponds to a single project.
- You can also delete checklists for completed projects.



- Access the recently created checklist and review the information in the Title Page tab.
- This page includes the Design Checklist Instructions, Project Overview, and Project Notes sections.
- The Project Overview and Project Notes sections will include user information when the project is created.

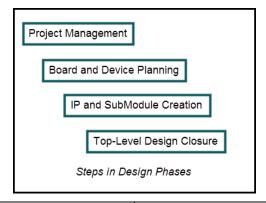
### **Action with Description**

 Review the organization of the UltraFast Design Methodology Guide from the hyperlink in the Title Page.

The UltraFast design methodology checklist is organized by each of the design phases.

### Point of Emphasis and Key Takeaway

- As you enter each design phase, we recommend that you review the corresponding tab in the checklist and the chapter in the methodology guide.
- Note that these steps are usually overlapping in time. Sometimes the process might also return to a previous step resulting in iterations.



- Review the Project Management tab.
- Go to 1.2 Xilinx Training.
- Mark the Vivado Training group as completed.
- Hover the mouse over the grayed tick mark icon on the left side of the "1.2.1 Vivado Training" group and click.
- In the Checklist Item Dialog box, select the Change the Status of All the Checklist Questions in this Group option, select the Item is Complete option, and click OK.

Once you are done with reviewing each questions and actions in the group, you can mark it as completed.

# Action with Description Point of Emphasis and Key Takeaway 1.2. - Xlinx Training 1.2.1 - Vivado Training Checklist Item Dialog - Edit Items The Vivado Training Checklist Item Dialog - Edit Items Checklist Question Group - Use this dialog to modify the status for all the items in this question group. Status for a question group is derived by rolling up the status of Individual questions in this group. Status determines icon displayed in column 1, while verboaty controls how much text is displayed in column 2. Checklist Item: Vivado Training Checklist Item: Status of all the Checklist Questions in this Group Question Status of all the Checklist Questions in this Group Questions in this questions in this Group Questions

- Review the questions and actions in the rest of the tabs.
- While reviewing, click the hyperlinks in the Reference column to visit the corresponding information in the user guides related to the corresponding items.
- This section focus on the design creation, coding guidelines, and the timing closure parts of the UltraFast design methodology. For more information about the rest of the UltraFast design methodology, review the UltraFast Design Methodology Guide for the Vivado Design Suite (UG949)

## **Summary**

The UltraFast design methodology checklist is designed as a guide for designers to assist them in getting their design to operate reliably and at the desired system speed.

### References:

- Supporting materials
  - Chapter 1 of UltraFast Design Methodology Guide for the Vivado Design Suite (UG949)