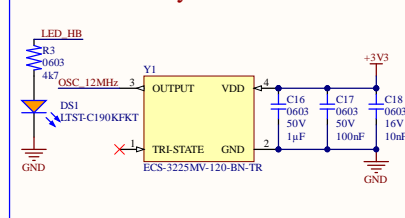
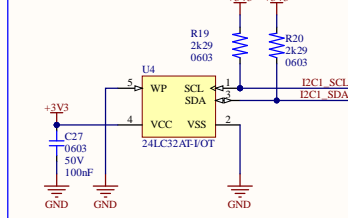
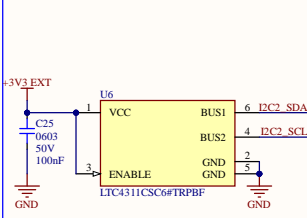
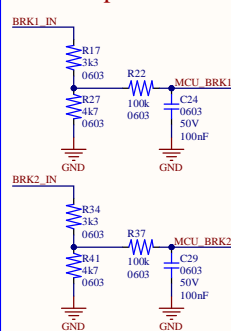
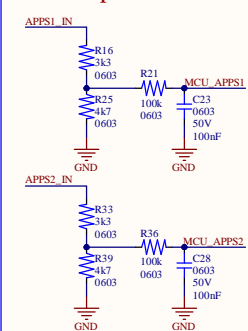
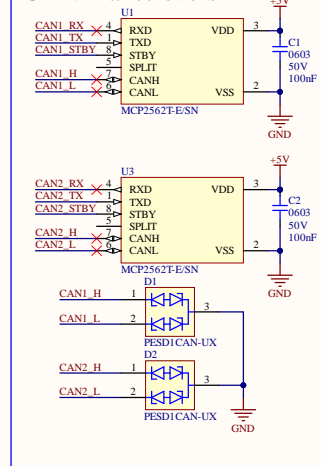
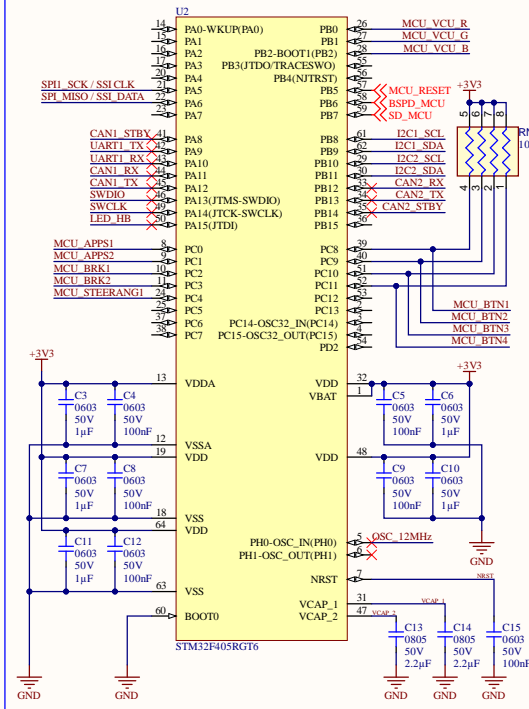
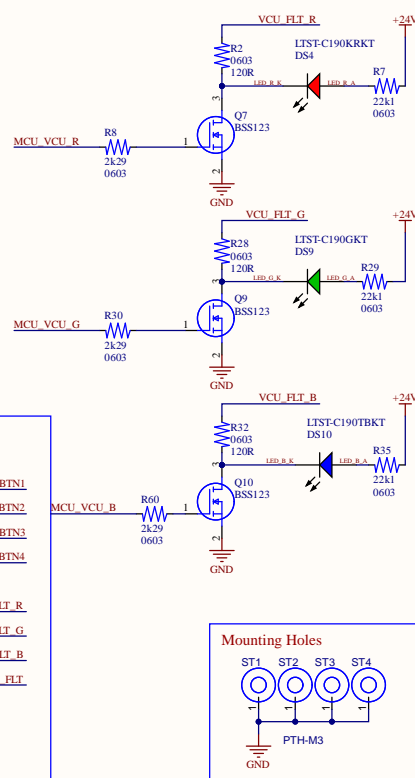
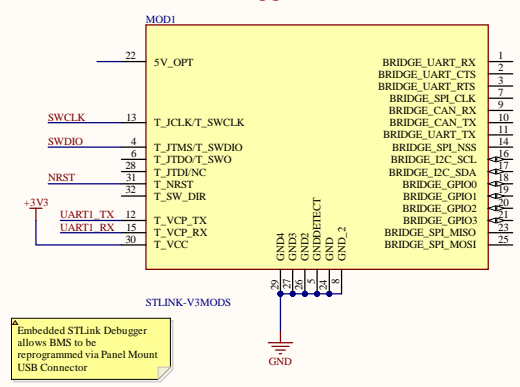
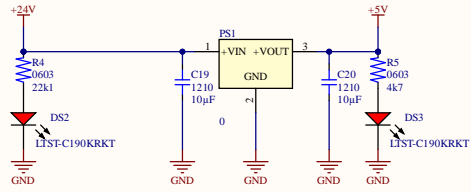


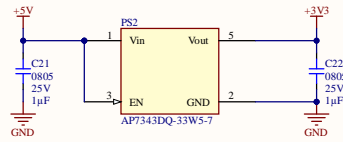
A	1	2	3	4	5		6	
					REVISION	DATE	DRAWN BY	COMMENT
					AA	5/17/2024	Ethan Zifzal	Initial Release of Schematic
					B			



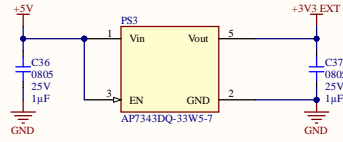
### +5V Regulation (8V - 28V) @ 1A



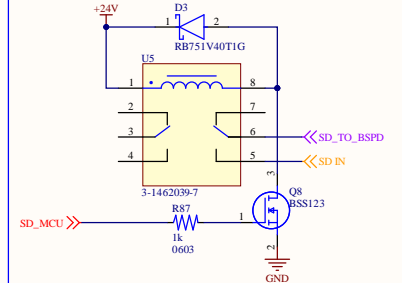
### +3V3 Regulation




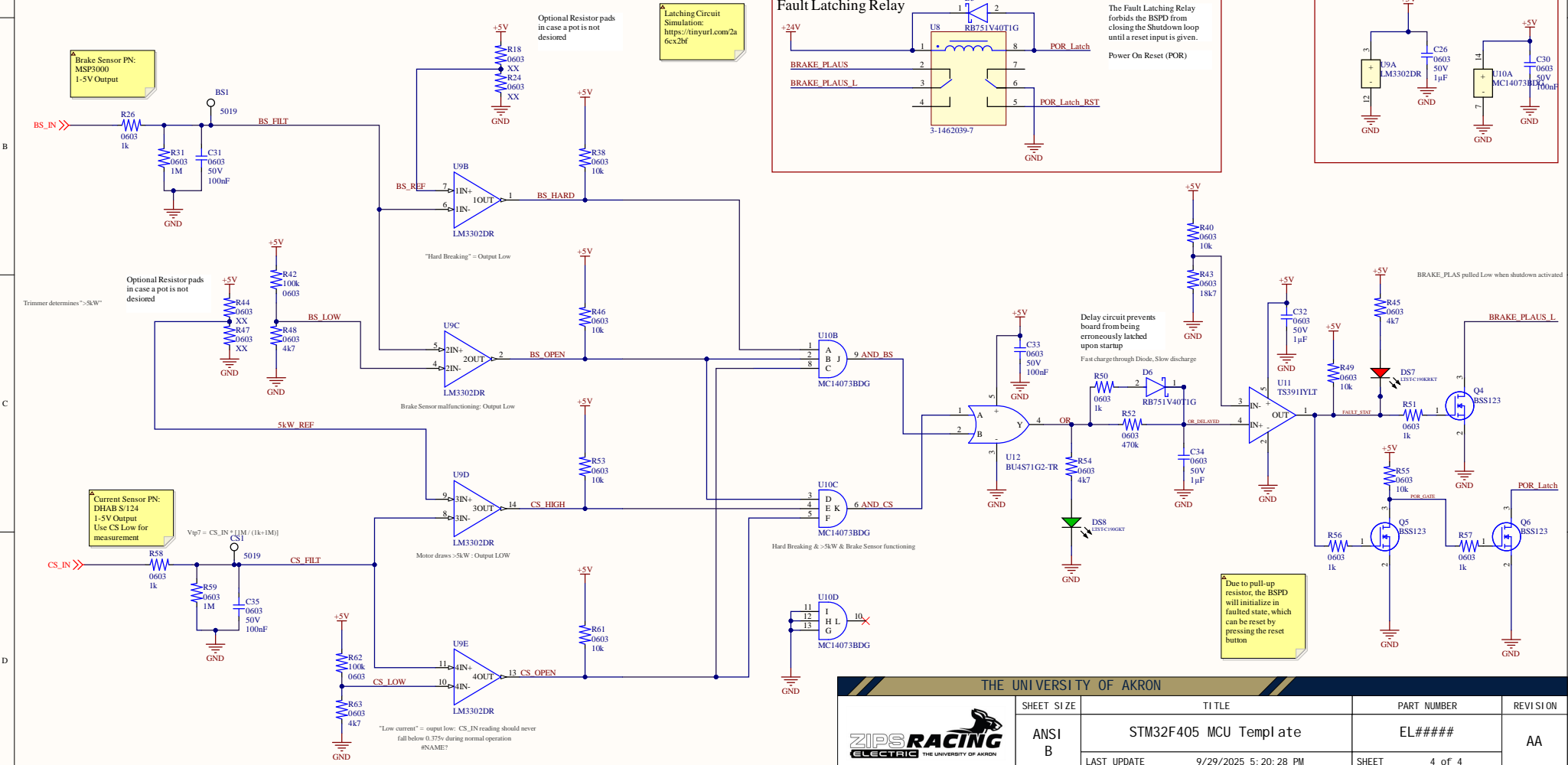
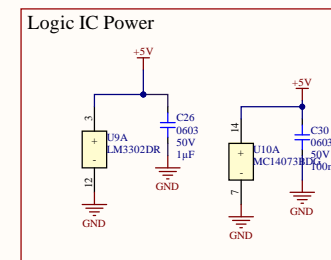
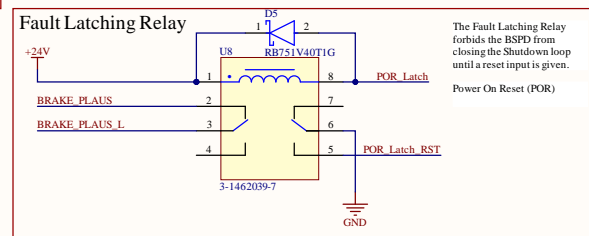
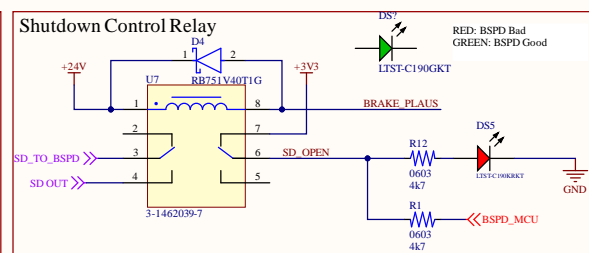
### +3V3 External




### Shutdown Loop Control



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	SHEET SIZE	TITLE	PART NUMBER	REVISION
	ANSI B	STM32F405 MCU Template	EL####	AA
	LAST UPDATE	9/29/2025 5:20:28 PM	SHEET 3 of 4	



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	SHEET SIZE	TITLE	PART NUMBER	REVISION
	ANSI B	STM32F405 MCU Template	EL####	AA
		LAST UPDATE	9/29/2025 5:20:28 PM	