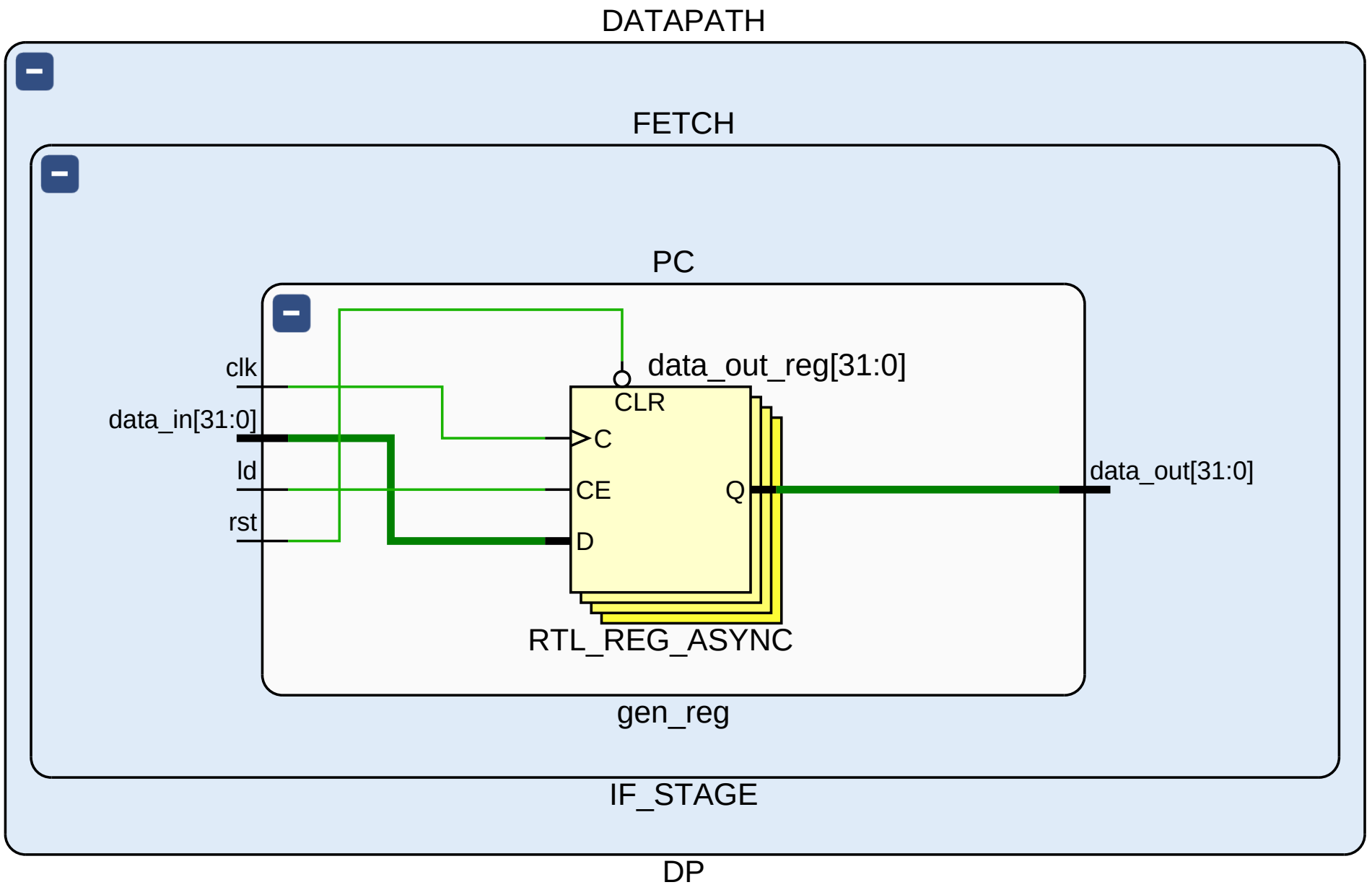
DATAPATH
FETCH
PC
clk
data_in[31:0]
ld
rst
data_out_reg[31:0]
CLR
C
CE
D
Q
data_out[31:0]
RTL_REG_ASYNC
gen_reg
IF_STAGE
DP