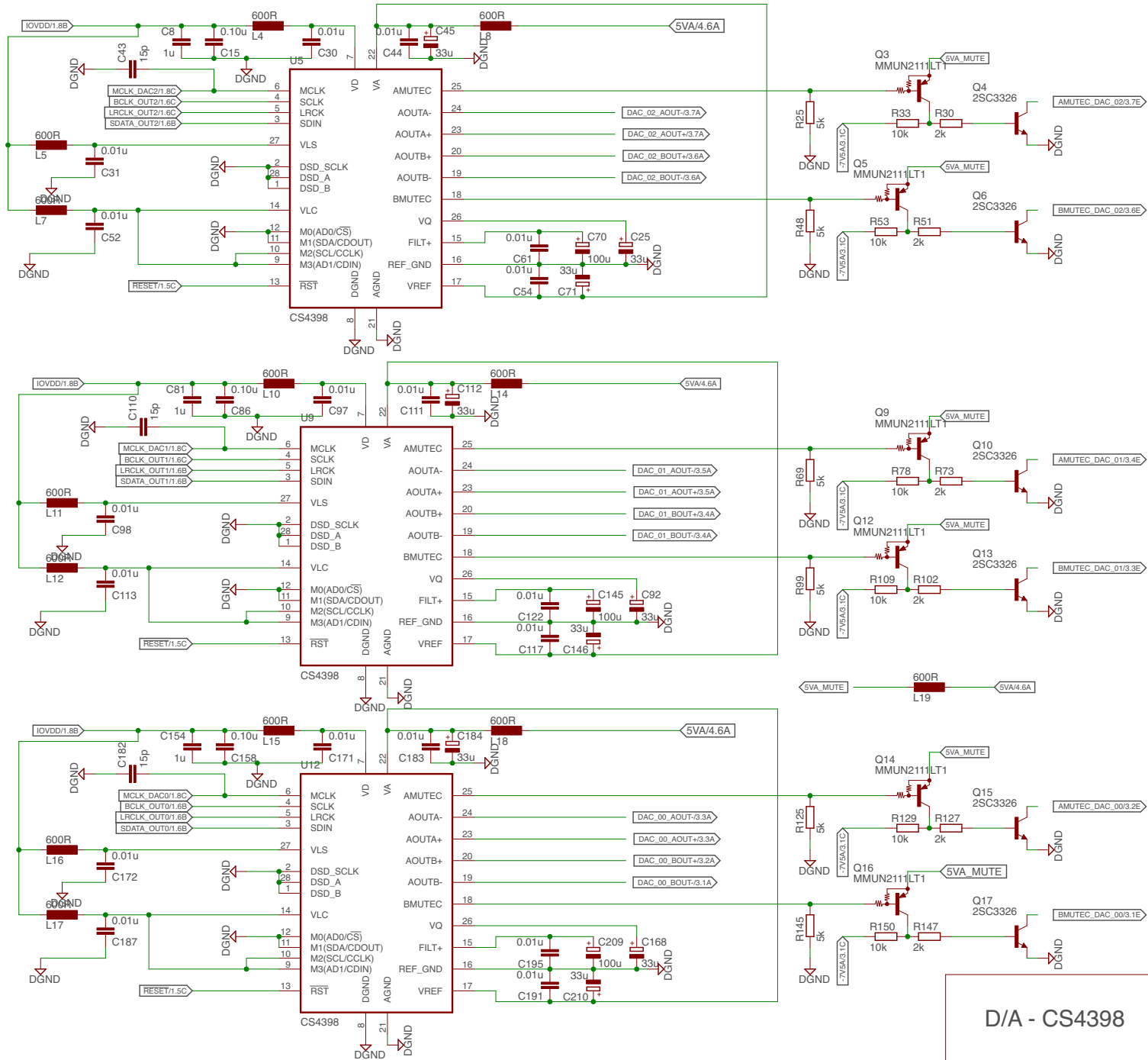


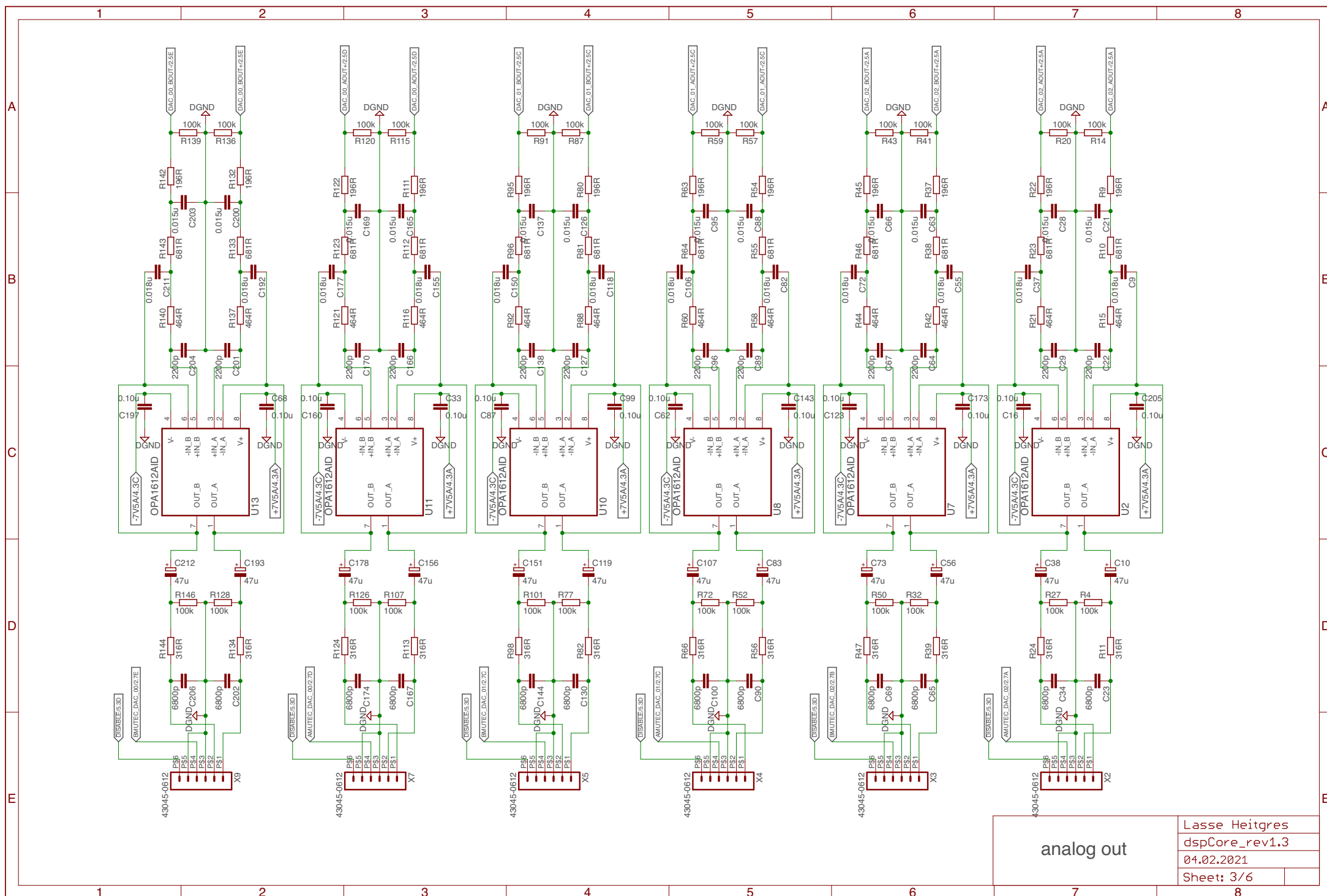


CS4398 configuration  
M0=0, M1=0 = LJ up to 24-bit  
M2=0, M3=1 = Double Speed (50 to 100 kHz)  
M2=1, M3=1 = Quad Speed (100 to 200 kHz)

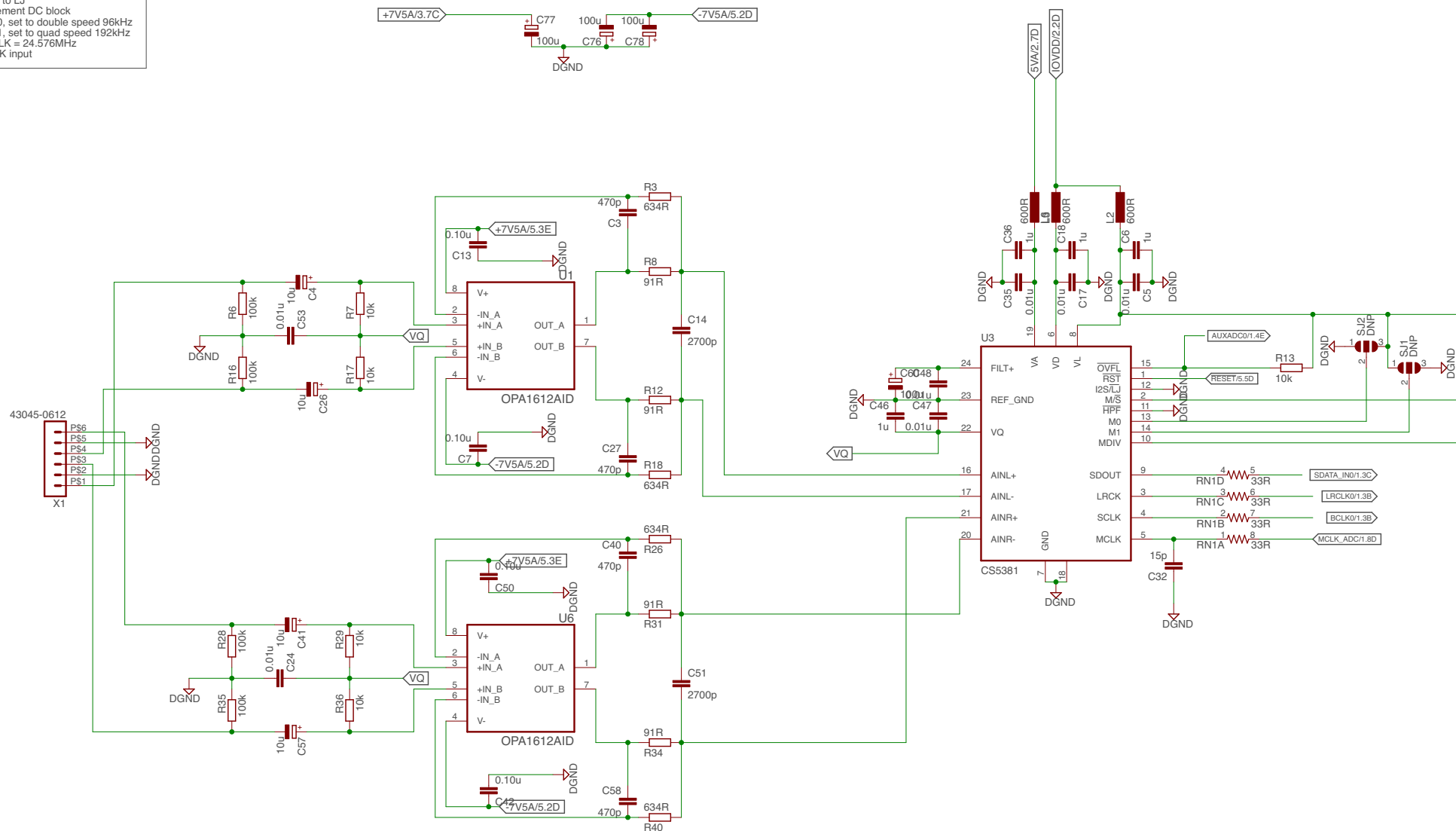


D/A - CS4398

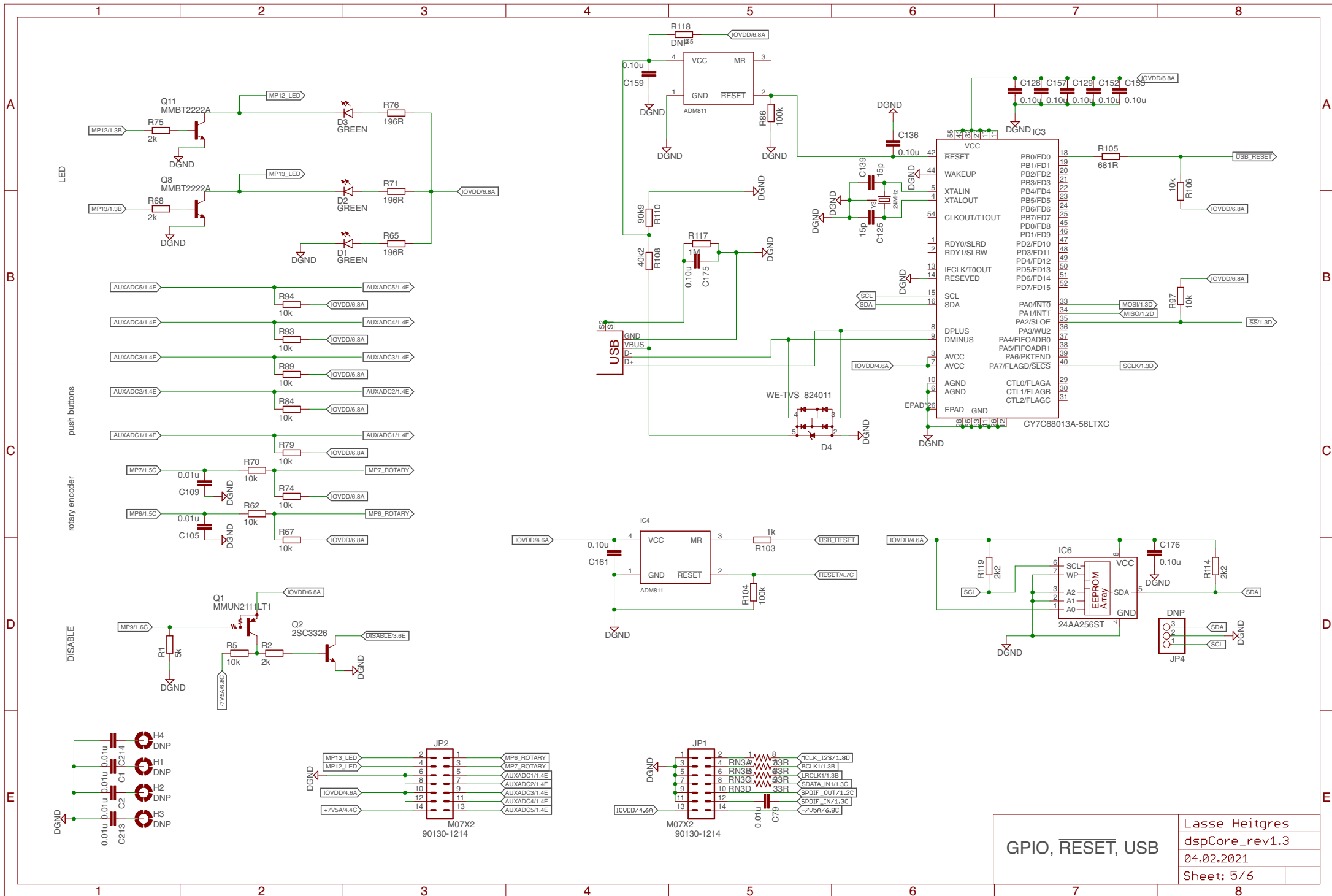
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Konfiguration  
 I2S/LJ = 0, set to LJ  
 HPF = 0, implement DC block  
 M0 = 1, M1 = 0, set to double speed 96kHz  
 M0 = 0, M1 = 1, set to quad speed 192kHz  
 MDIV = 1, MCLK = 24.576MHz  
 M/S = 1, MCLK input



analog input





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