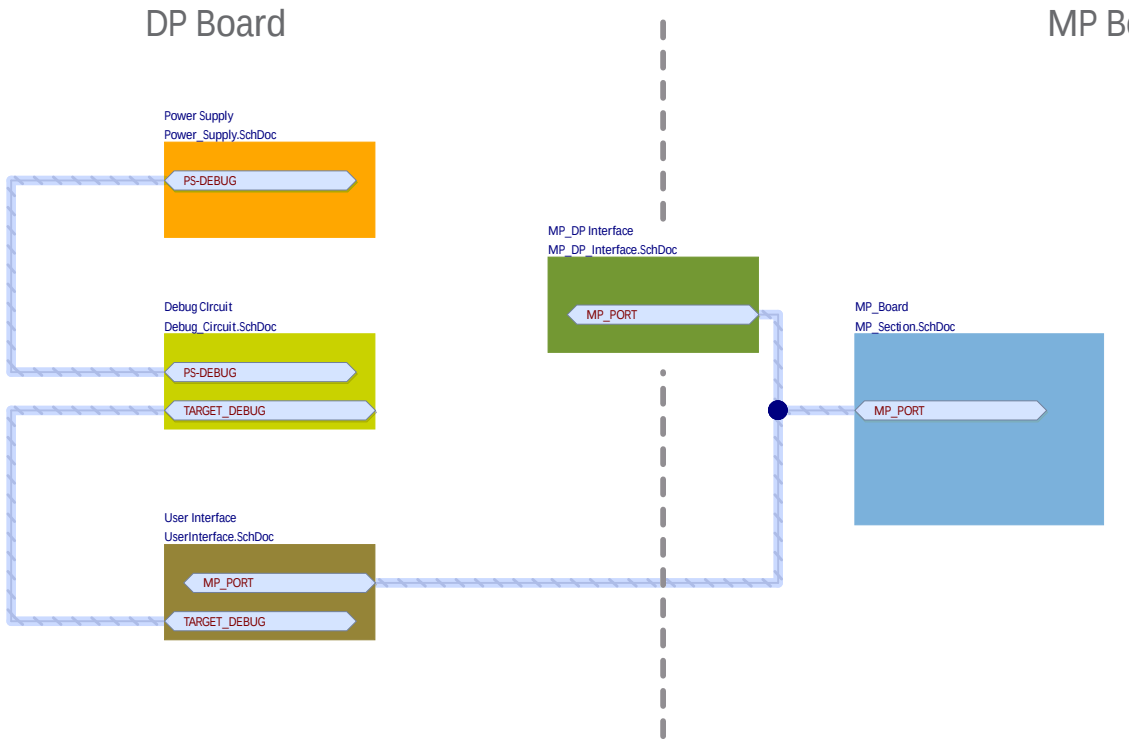
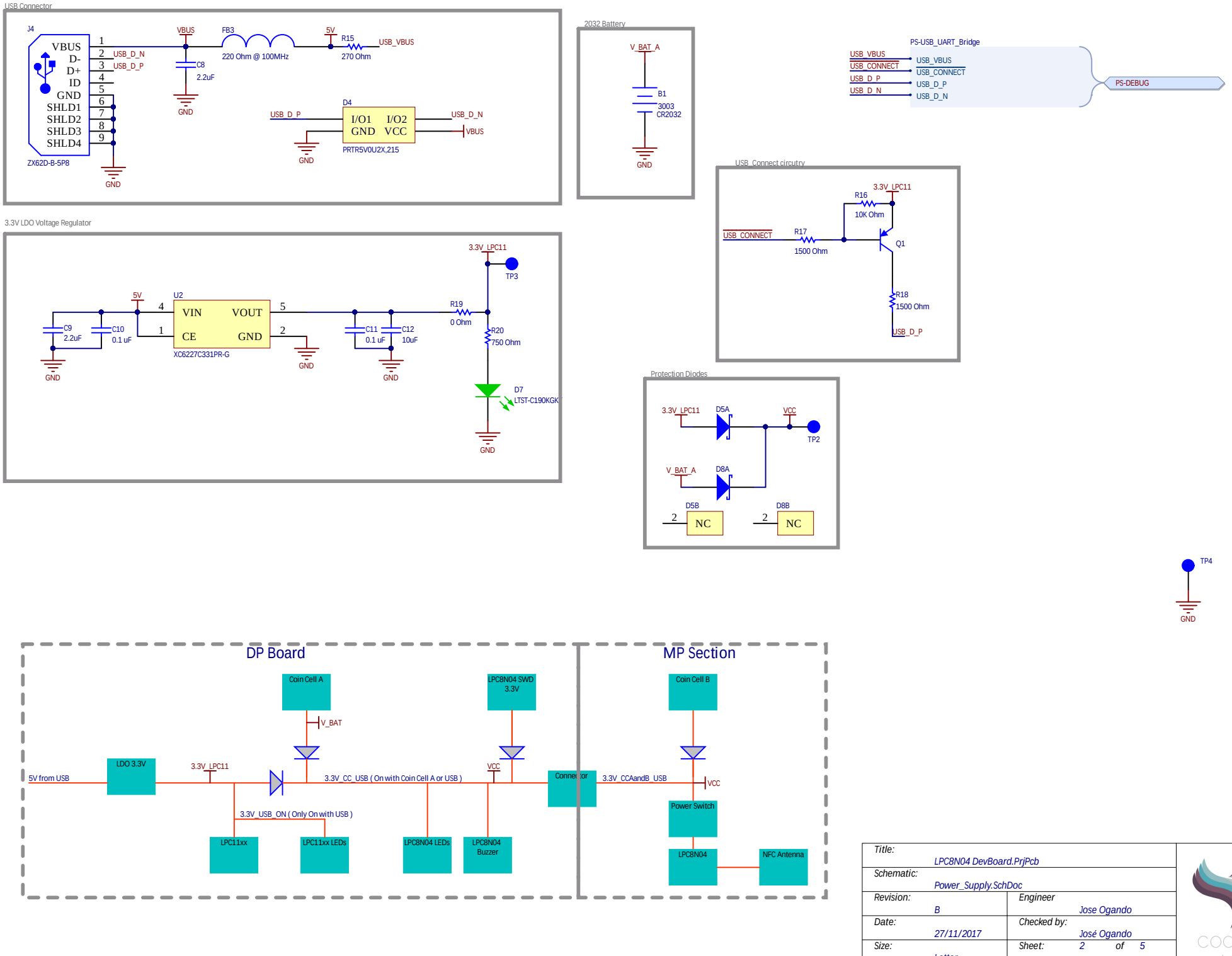
Date	Comment
5-Sept-2017	Initial Release.
7-Sept-2017	Changed Jumpers to specify 0.050" pitch. Removed PIO0_8 and PIO0_9 connections from debug section. Modified diode part numbers. Modified Piezo connection. Added 6 port analog connector to MP_Section.
8-Sept-2017	Removed DP SWD Programming Header. Added SWD DP Testpoints.
9-Sept-2017	Removed LEDs from Debug Circuit. Removed Wake-Up signals from DP.
11-Sept-2017	Modified R10 Size (In series with DP XTAL). Added NFC Inductor net class. Corrected Interface Signal Naming.
12-Sept-2017	Changed Speaker Model and list. Added Prototype Assembly Variant. Updated Harness to remove nWAKE and nSLEEP signals. Added Page Size Changed revision to A
3-Oct-2017	Removed 3 and 4 Turn Variant Removed P11. Analog Signals from MP Not connected Header P10 as a DNP. Bypassed with a 0 ohm resistor. Added 3x0.05" Jumpers to the BOM. Added 2x0.1" Jumpers to the BOM.
4-Oct-2017	Changed LEDs to Green Part # Changed Silk to revision to A Rotated P3, P4, P5 and P6 Added NFC_RESET Bypass capacitor. Added RESET on the Silkscreen.
20-Oct-2017	Moved RESET signal close to S3 on PCB Changed J1 BOM line item as it was a PTH component. Increased TP, Jumpers and Ports font size to 1.5mm
02-Nov-2017	Increased breakaway cut out area by using the vias as signal path.
27-Nov-2017	P6-3,5 connected to GND

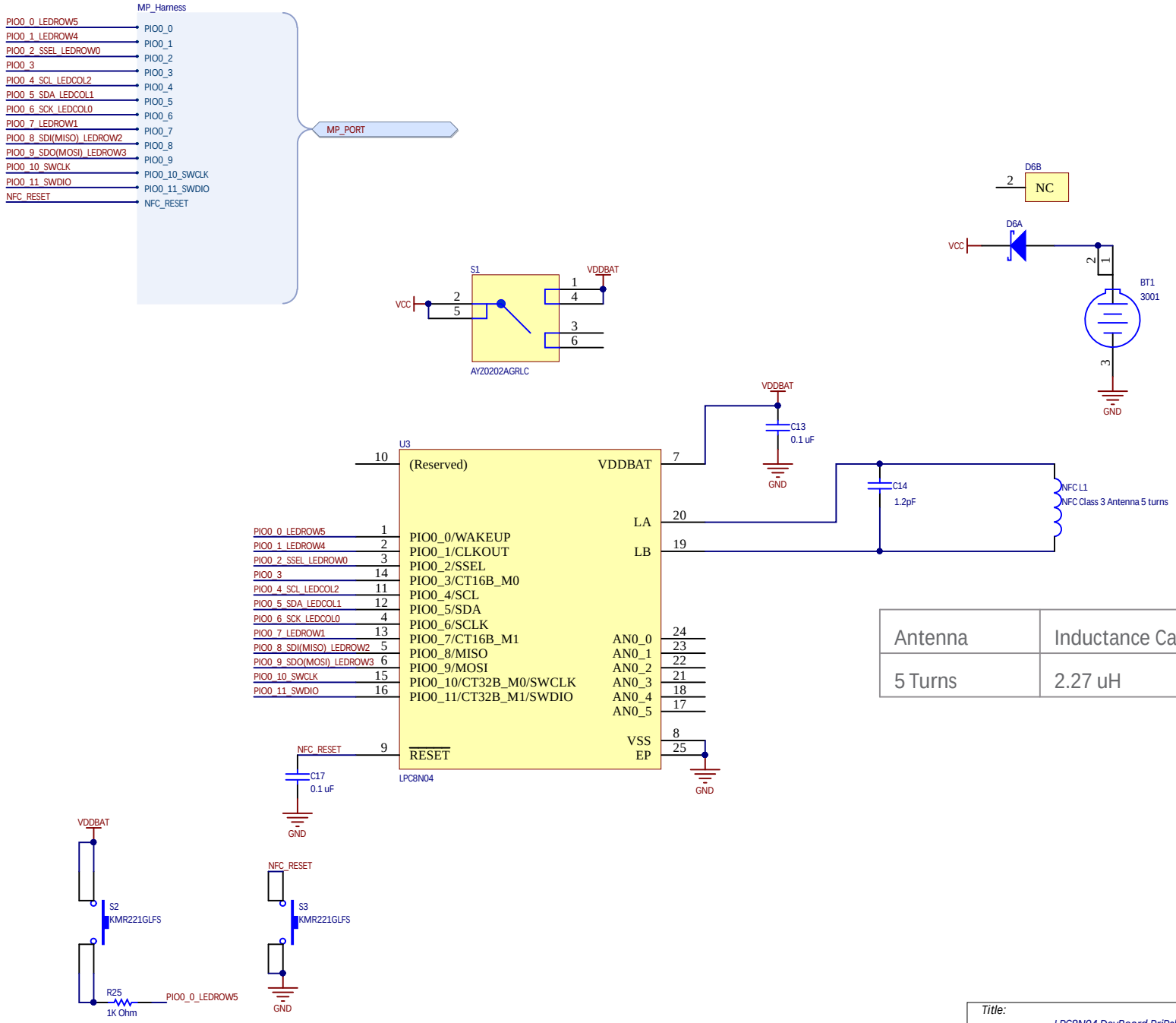


Title: LPC8N04 DevBoard.PrjPcb	
Schematic: LPC8N04 Devboard.SchDoc	
Revision: B	Engineer: Leonel Sanchez
Date: 27/11/2017	Checked by: Jose Ogando
Size: Letter	Sheet: 1 of 5

code
www.code-ing.com

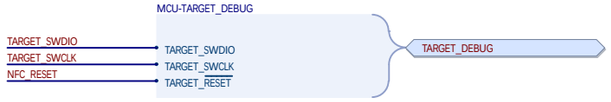


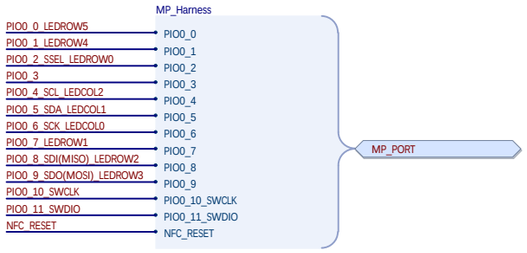
Title: LPC8N04 DevBoard.PrjPcb	
Schematic: Power_Supply.SchDoc	
Revision: B	Engineer: Jose Ogando
Date: 27/11/2017	Checked by: José Ogando
Size: Letter	Sheet: 2 of 5

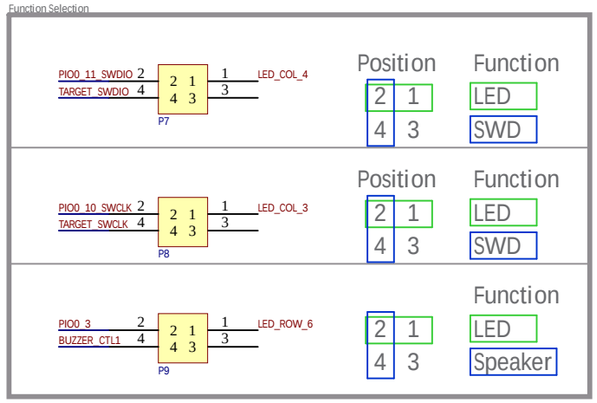


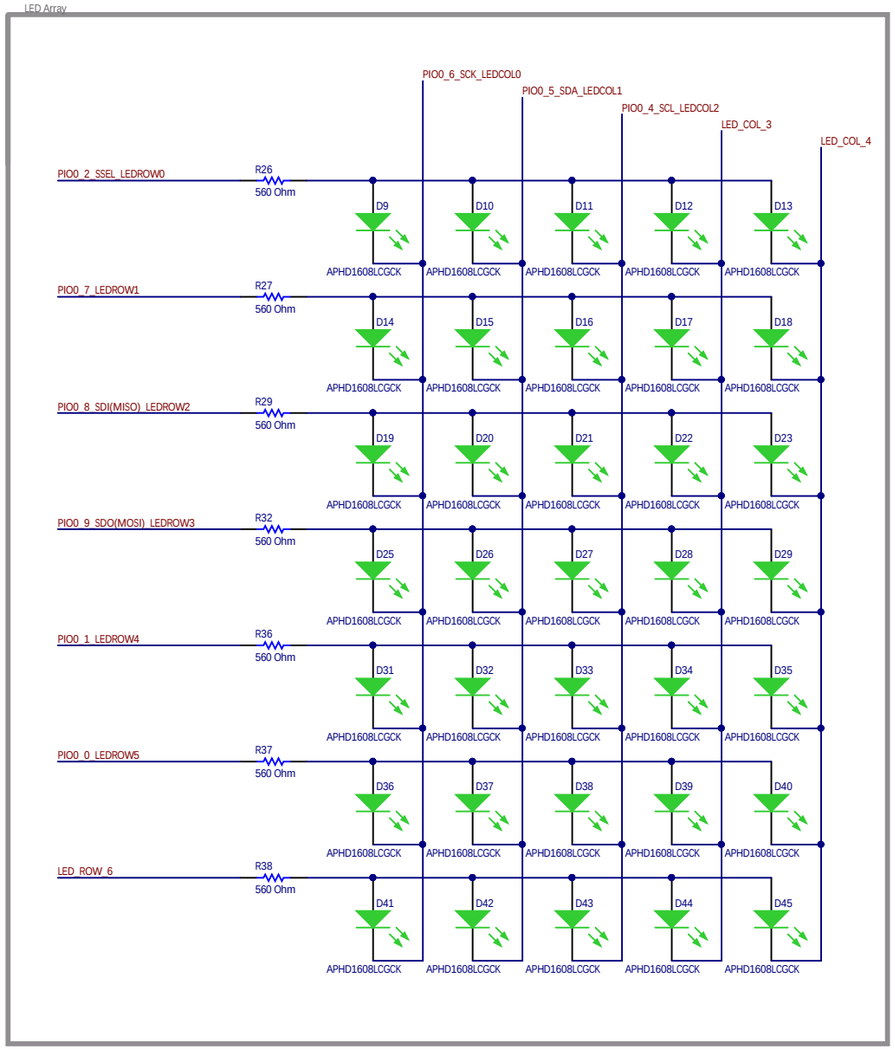
Antenna	Inductance Calc.
5 Turns	2.27 uH

Title: LPC8N04 DevBoard.PrjPcb	
Schematic: MP_Section.SchDoc	
Revision: B	Engineer: Leonel Sanchez
Date: 27/11/2017	Checked by: Jose Ogando
Size: Letter	Sheet: 3 of 5



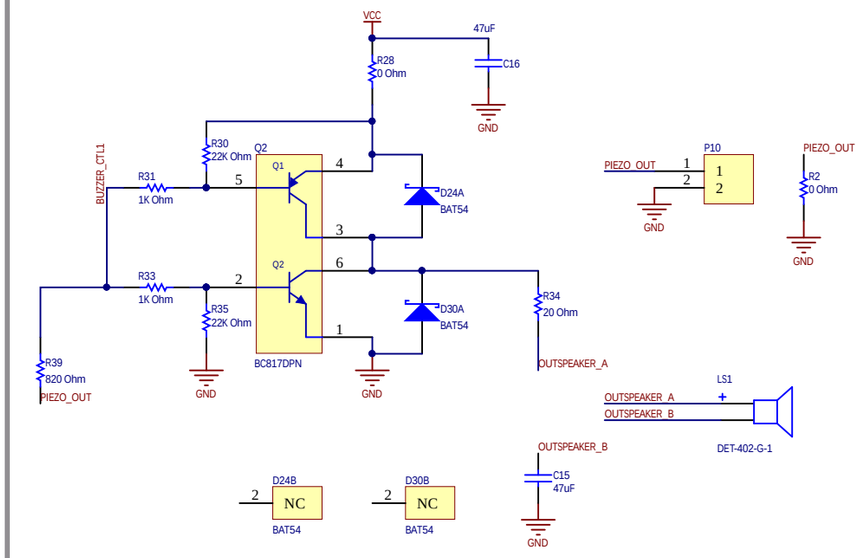




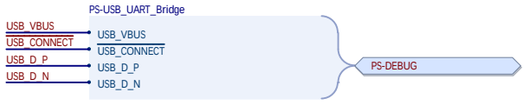


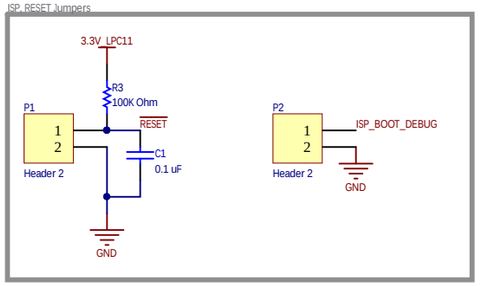
Speaker / Piezo Interface

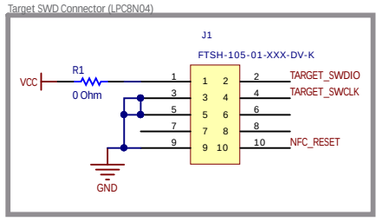
Speaker	R28 Value	R34 Value	C15 Value	Power (Aprox)
DET-402-G-1	0 Ohm	20 Ohm	47 uF	30 mW
PIEZO	Use Header			

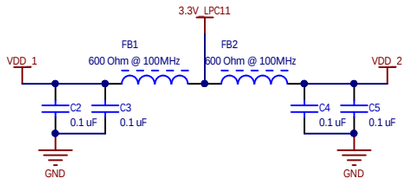


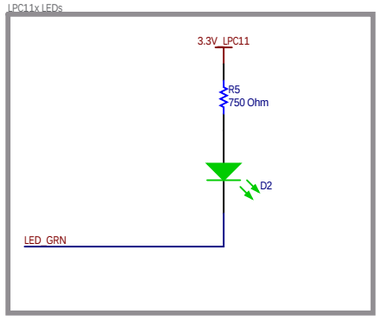
Title:	LPC8N04 DevBoard.PrjPcb
Schematic:	UserInterface.SchDoc
Revision: B	Engineer: Leonel Sanchez
Date: 27/11/2017	Checked by: Jose Ogando
Size: A3	Sheet: 4 of 6

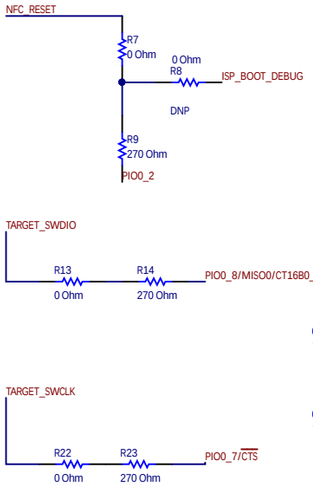


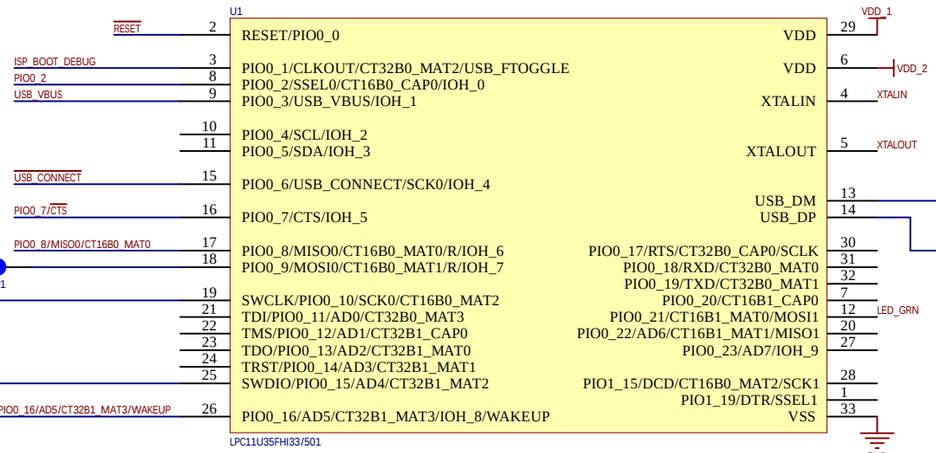


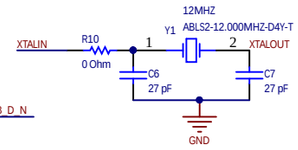


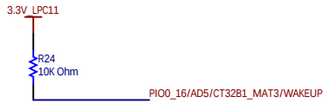




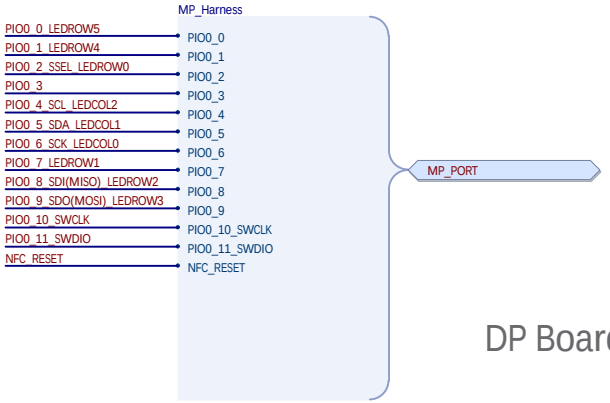








Title: LPC8N04 DevBoard.PrjPcb	
Schematic: Debug_Circuit.SchDoc	
Revision: B	Engineer: Leonel Sanchez
Date: 27/11/2017	Checked by: Jose Ogando
Size: A3	Sheet: 5 of 6

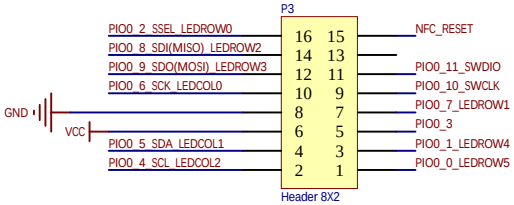


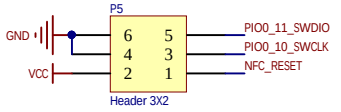
Grove I2C Connector
PMod SPI Connector

P4

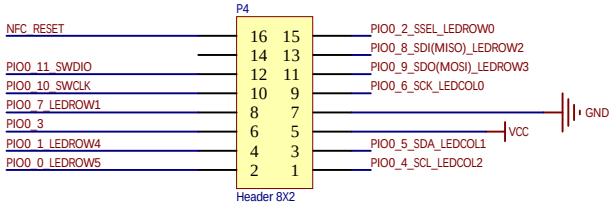
16	15 SSEL
14	13 SDI
12	11 SDO
10	9 SCK
8	7 GND GND
6	5 VDD VDD
4	3 SDA
2	1 SCL

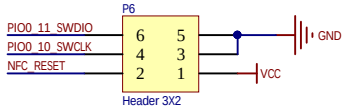
DP Board





MP Board





Routing Should connect P3 to P4 with a breakaway configuration

Title: LPC8N04 DevBoard.PrjPcb	
Schematic: MP_DP_Interface.SchDoc	
Revision: B	Engineer: Leonel Sanchez
Date: 27/11/2017	Checked by: Jose Ogando
Size: Letter	Sheet: 6 of 6