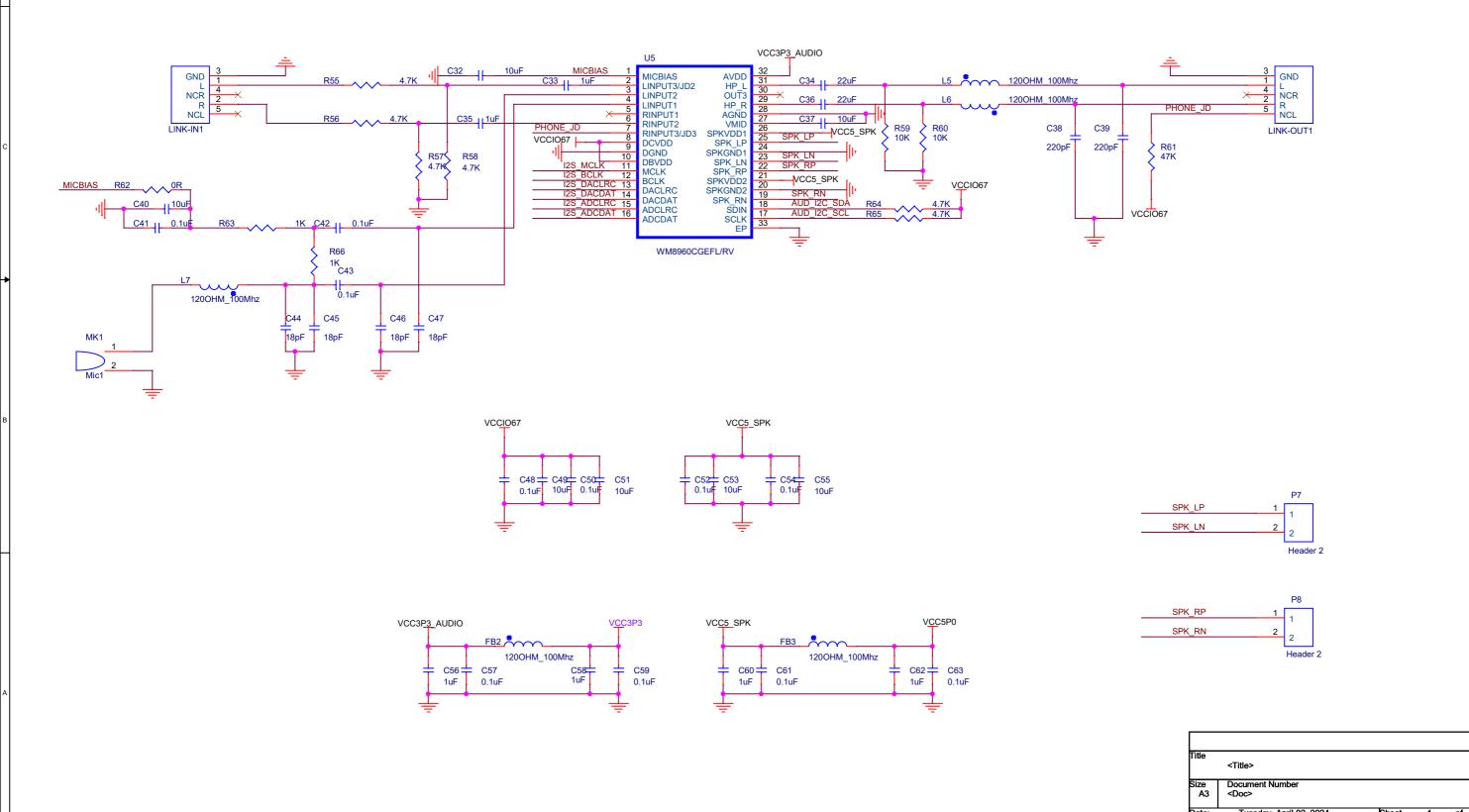
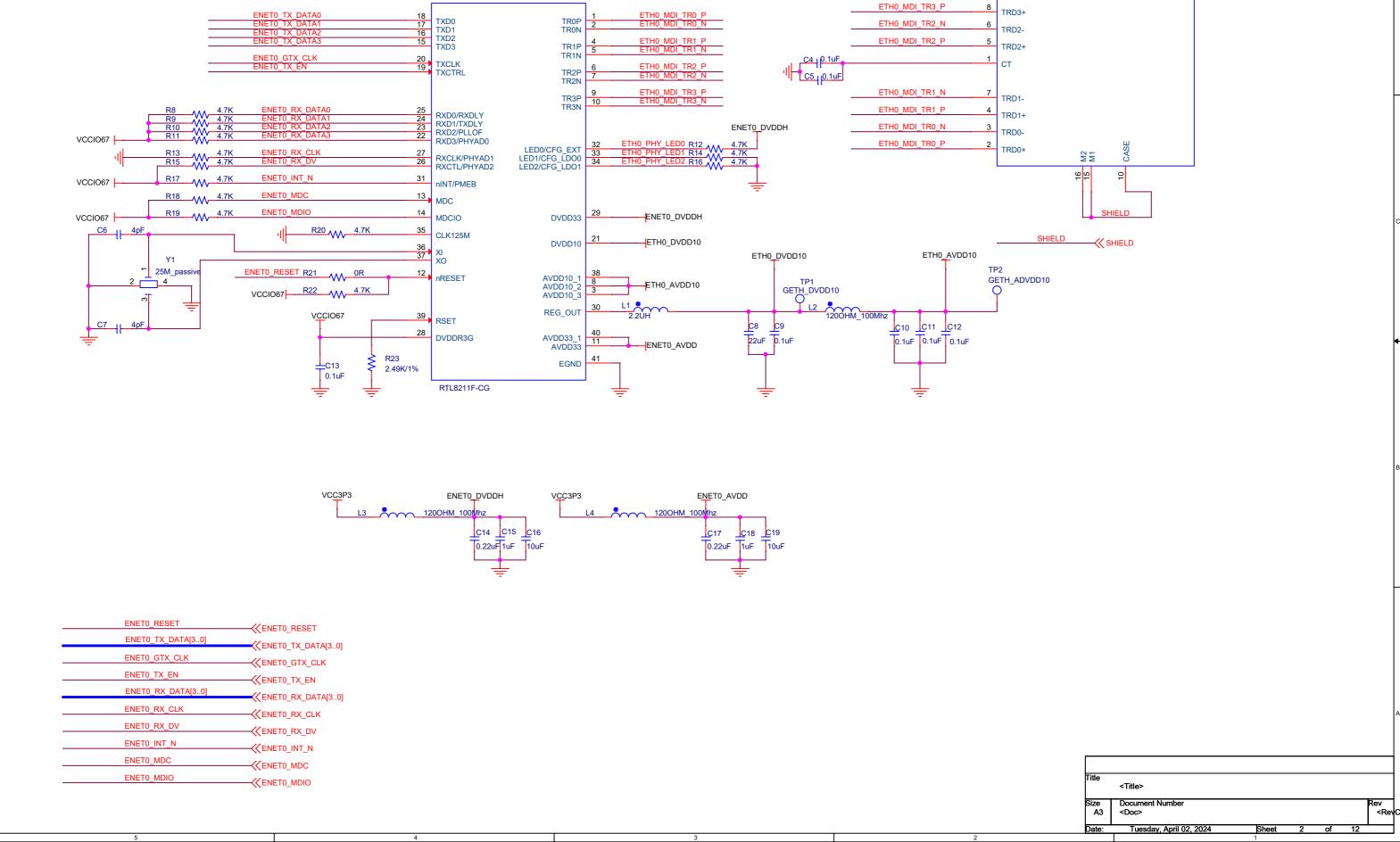
I2S MCLK -<<li>I2S\_MCLK I2S\_BCLK I2S\_BCLK I2S\_DACLRC I2S\_DACLRC I2S\_DACDAT -<:\(\)I2S\_DACDAT I2S\_ADCLRC -<<\I2S\_ADCLRC I2S\_ADCDAT -≪12S\_ADCDAT AUD\_I2C\_SDA -≪AUD\_I2C\_SDA AUD I2C SCL -</AUD\_I2C\_SCL

> Rev <RevCode>

## 音频编解码器





U2

J2 RJ45\_HR911130A

13 14 12 12 11 LD2\_A LD1\_K LD1\_A

TRD3-

ETH0\_MDI\_TR3\_N

