

2SD717

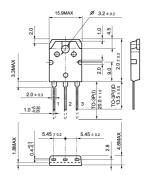
Silicon Epitaxial Planar Transistor

GENERAL DESCRIPTION

Silicon NPN high frequency, high power transistors in

a plastic envelope, primarily for use in audio and general purpose





QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	TYP	MAX	UNIT
V _{CBO}	Collector-emitter voltage peak value	$V_{BE} = 0V$	-	70	V
V_{CEO}	Collector-emitter voltage (open base)		-	70	V
Ic	Collector current (DC)		-	10	Α
I _{CM}	Collector current peak value		-		Α
P _{tot}	Total power dissipation	T _{mb} ≤25°C	-	80	W
V_{CEsat}	Collector-emitter saturation voltage	$I_{C} = 4.0A$; $I_{B}=0.4A$	-	2	V
V_{F}	Diode forward voltage	$I_F = 3.5A$	1.5	2.0	V
t_f	Fall time	$I_{C}=4A, I_{B1}=-I_{B2}=0.4A, V_{CC}=30V$	0.4	1.0-	μs

LIMITING VALUES

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
V_{CESM}	Collector-emitter voltage peak value	$V_{BE} = 0V$	-	70	V
V _{CEO}	Collector-emitter voltage (open base)		-	70	V
V _{EBO}	Emitter-base oltage (open colloctor)			5	V
Ic	Collector current (DC)		-	10	A
I _B	Base current (DC)		-	2.5	A
P _{tot}	Total power dissipation	Tmb≤25℃	-	80	W
T _{stq}	Storage temperature		-55	150	℃
T _i	Junction temperature		-	150	\mathbb{C}

ELECTRICAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	TYP	MAX	UNIT
I _{CBO}	Collector-base cut-off current	V _{CB} =70V	-	0.2	mA
I _{EBO}	Emitter-base cut-off current	V _{EB} =5V	-	0.2	mA
$V_{(BR)CEO}$	Collector-emitter breakdown voltage	Ic=1mA	70		V
V_{CEsat}	Collector-emitter saturation voltages	$I_C = 4.0A$; $I_B = 0.4A$	-	3	V
h _{FE}	DC current gain	$I_{C} = 1A; V_{CE} = 5V$	50	240	
f⊤	Transition frequency at f = 5MHz	I _C = 1A; V _{CE} = 12V	10	-	MHz
C_c	Collector capacitance at f = 1MHz	$V_{CB} = 10V$	350	-	pF
t _{on}	On times	I _C =4A,I _{B1} =-I _{B2} =0.4A,V _{CC} =30V	0.3		us
ts	Tum-off storage time	I _C =4A,I _{B1} =-I _{B2} =0.4A,V _{CC} =30V	2.5		us
t _f	Fall time	I _C =4A,I _{B1} =-I _{B2} =0.4A,V _{CC} =30V	0.4		us

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