

Experiment No. 2 USE OF TRANSMISSION LINE AS A CIRCUIT ELEMENT

Aim:

To understand and verify the use of transmission line as a circuit element.

Software requirements:

Software- QUCS (Quite Universal Circuit Simulator)

Theory:

The input impedance of a transmission line is the impedance offered by it at the input terminals. As the source is connected at the input terminals, this quantity has some special significance while selecting the source. During computations, input impedance is quite useful parameter to find the power flowing into the line when a generator is connected to it. To push maximum power over to the line, the source impedance and input impedance of line must have a complex conjugate relationship.

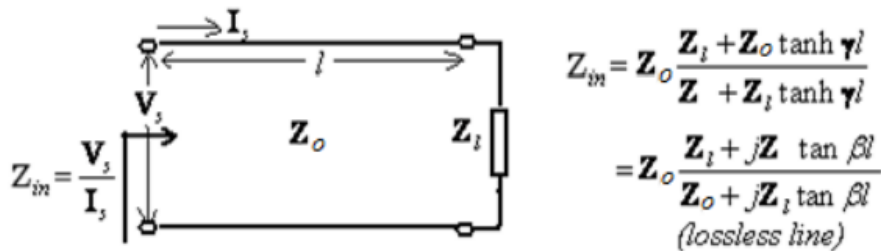


Figure Input impedance of a line terminated over an impedance.

Formally, the input impedance of a line can be defined as the ratio of complex phasor voltage to complex phasor current at its input terminals. Mathematically,

$$Z_{in} = \frac{V_{in}}{I_{in}} = \frac{\text{Input voltage}}{\text{Input current}}$$

It is complex quantity, value being dependent upon the configuration, length and termination of the line.

For a general (lossy) line:

$$Z_{in} = Z_o \frac{Z_L + Z_o \tanh \gamma l}{Z_o + Z_L \tanh \gamma l}$$

For an ideal (lossless) line:

$$Z_{in} = Z_o \frac{Z_l + jZ_o \tan \beta l}{Z_o + jZ_l \tan \beta l}$$

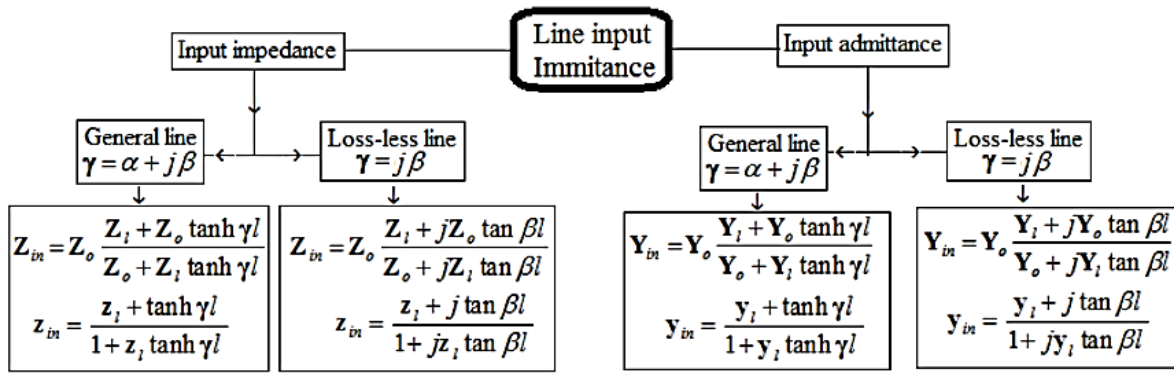
So, for a short-circuited transmission line, an input impedance is given by –

$$Z_{in}|_{Z_l=0} = Z_{sc} = Z_o \frac{Z_l + Z_o \tanh \gamma l}{Z_o + Z_l \tanh \gamma l} \Big|_{Z_l=0} = Z_o \tanh \gamma l$$

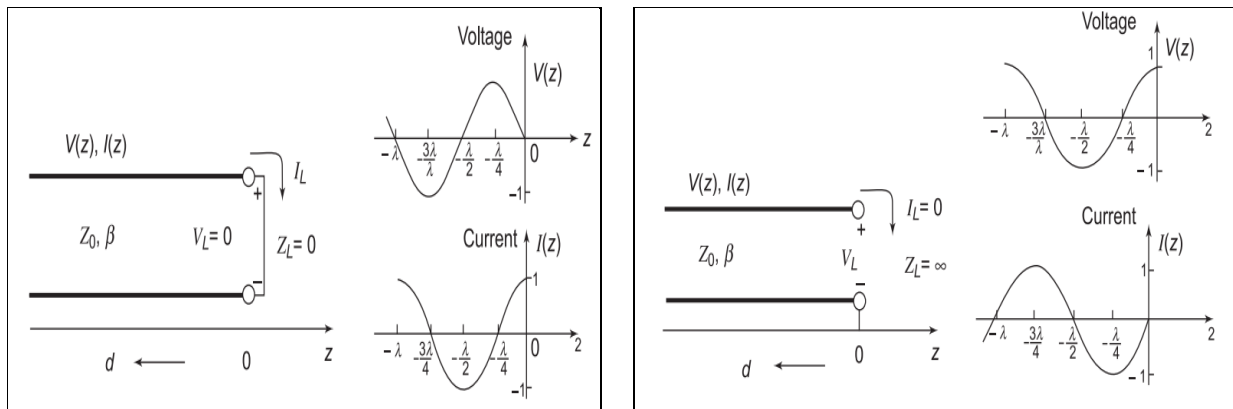
With open circuit termination, its input impedance becomes,

$$Z_{in}|_{Z_l \rightarrow \infty} = Z_{oc} = Z_o \frac{Z_l + Z_o \tanh \gamma l}{Z_o + Z_l \tanh \gamma l} \Big|_{Z_l \rightarrow \infty} = Z_o \coth \gamma l$$

In case of loss-less line, $\gamma = j\beta$ resulting in $Z_{sc} = jZ_o \tan(\beta l)$ and $Z_{oc} = -jZ_o \cot(\beta l)$. So, overall equations for an input impedance and input admittance are as summarized below:



The voltage and current waveforms for short-circuited and open-circuited transmission lines are as given below:



In case of short circuit termination, it is a current anti-node and a voltage node that exists right over the load. In this case, as the total voltage is required to be zero over the load, the

voltage must get reflected with 180° phase shift whereas the current need not under go any phase shift. It results in voltage node and current anti-node over the short circuit termination.

When the termination is open circuit, the current gets reflected with 180° phase shift, since the total current has to be zero on an open circuit and the reflected current has to cancel the incident current which can happen only if they are out of phase. However, the voltage gets reflected without any phase shift, as the direction of travel of the wave and phase of current being reversed, reflected voltage cannot have a phase shift. Thus, it is a current node and consequently, a voltage anti-node that exists right over the open circuit load.

So, an input impedance of short-circuited and open-circuited transmission lines is purely reactive in nature. Hence, these can behave like an inductor, capacitor and also as a resonant circuit as summarized below:

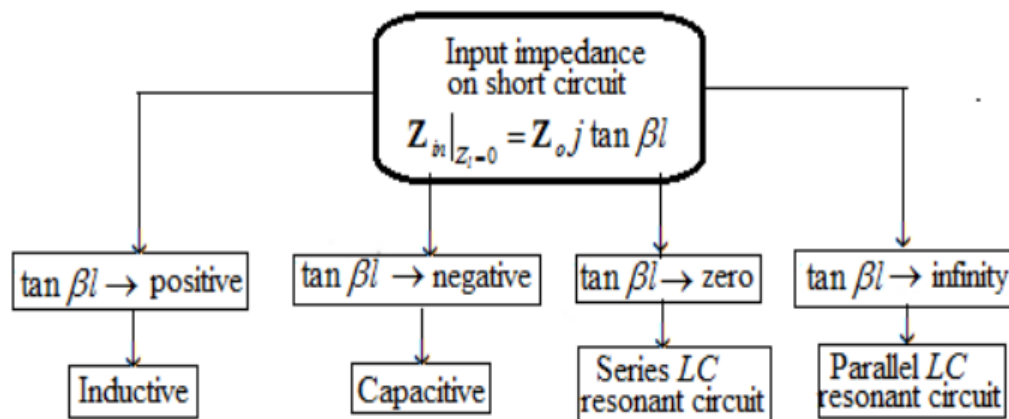


Fig.: Use of Short-circuit T.L. as a Circuit Element

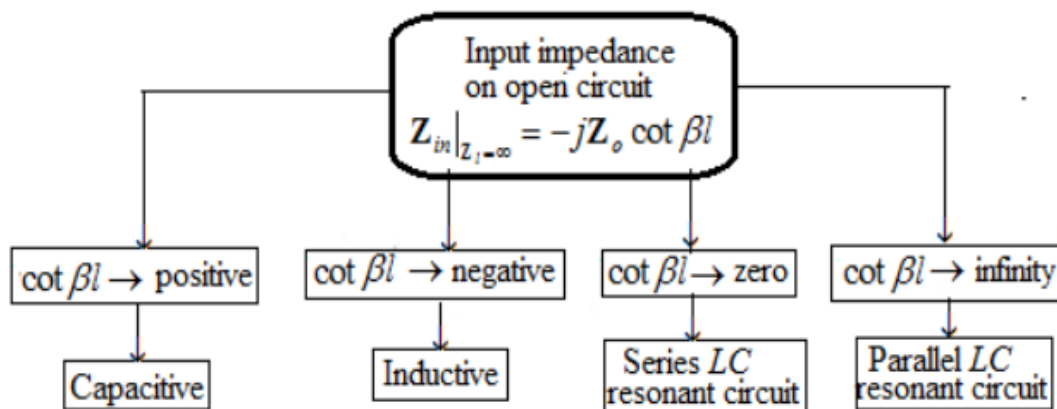


Fig.: Use of Open-circuit T.L. as a Circuit Element

The variation of an input impedance of a lossless line with line length is given below:

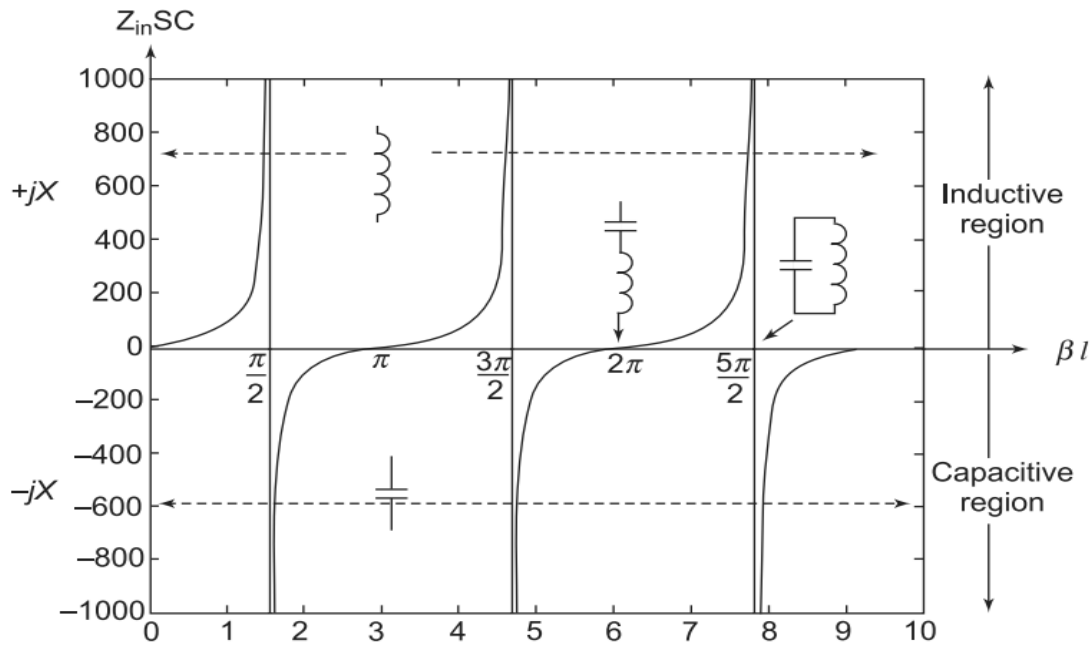


Fig.: Variation of input impedance of lossless short-circuited line with the line length

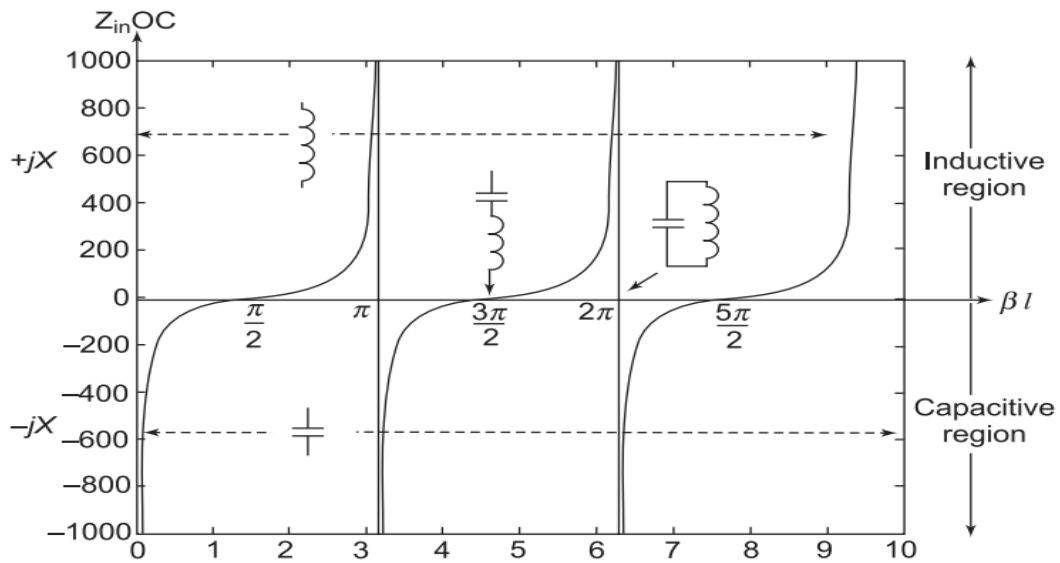
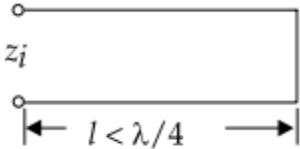
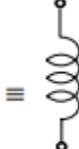
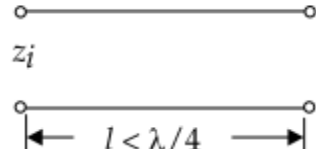

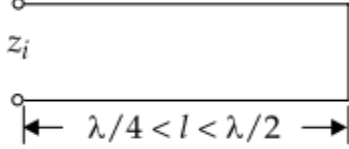
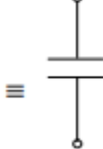
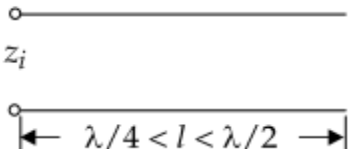
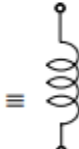
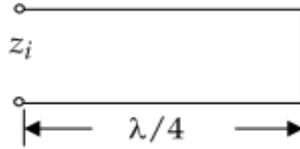
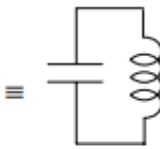
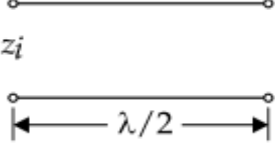
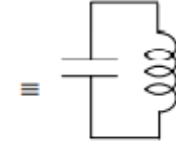


Fig.: Variation of input impedance of lossless open-circuited line with the line length

lines of different length with short and open ends along with their equivalent circuits are as given below:

| Transmission line | Equivalent circuit | Input impedance |
|---|---|--|
|  |  | $z_i = +jz_0 \tan \beta l$ |
|  |  | $z_i = -jz_0 \cot \beta l$ |
|  |  | $z_i = +jz_0 \tan \beta l$ |
|  |  | $z_i = -jz_0 \cot \beta l$ |
|  |  | $z_i \approx \frac{2z_0^2}{Rl} = \frac{z_0^2}{\tanh \alpha l}$ |
|  |  | $z_i \approx \frac{2z_0^2}{Rl} = \frac{z_0^2}{\tanh \alpha l}$ |

Conclusion:

After performing this experiment, we can conclude that by selecting a terminated line of suitable length, it is possible to produce the equivalent of a pure inductance and capacitance or any desired combination thereof.