

Part 1: sizing chart

- First get value of $R_D = \frac{V_{RD}}{I_D}$ $V_{RD} = V_{DD}/2 = 0.9V$ $I_D = 150\mu A$

Then $R_D = 6k\Omega$

- we can get gain is given by:

$$|A_v| \approx gmR_D = \frac{2 * I_D * R_D}{V_{OV}} = \frac{2 * V_{RD}}{V_{OV}}$$

But for real mosfet $V_{OV} \neq \frac{2 * I_D}{gm}$ we define new expression $V^* = \frac{2 * I_D}{gm}$

- For a square-law device, $V^* = V_{OV}$, however for a real MOSFET they are not equal. The actual gain is now given by $|A_v| \approx \frac{2 * V_{RD}}{V^*}$

- we have $|A_v| = 6$ then $V^* = \frac{2 * V_{RD}}{A_v} = \frac{2 * 0.9}{6} = 0.3V$, which we make the operating point voltage of real MOSFET overdrive voltage $V_Q^* = 0.3V$.

- And we assume that channel length value will be $L = 2\mu m$, to avoid short channel effects.

- from LAB01 we can get value of V_{TH} for NMOS

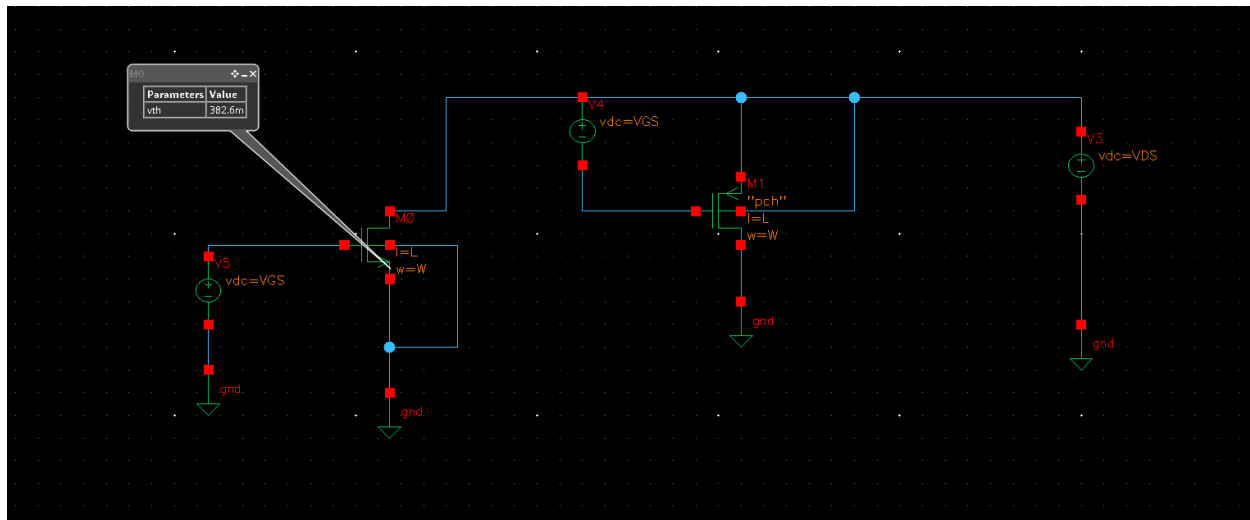


Figure 1 get value of V_{TH}

By making dc analysis simulation we get $V_{TH} = 382.6mV$.

- Sweep V_{GS} from 0 to $\approx V_{TH} + 0.4V$ $0 \rightarrow 782.6mV$ with 10mV step. Set $V_{DS} = \frac{V_{DD}}{2} = 0.9V$.
- firstly, assume the width of MOSFET to be $W = 10\mu m$, $L = 2\mu m$.

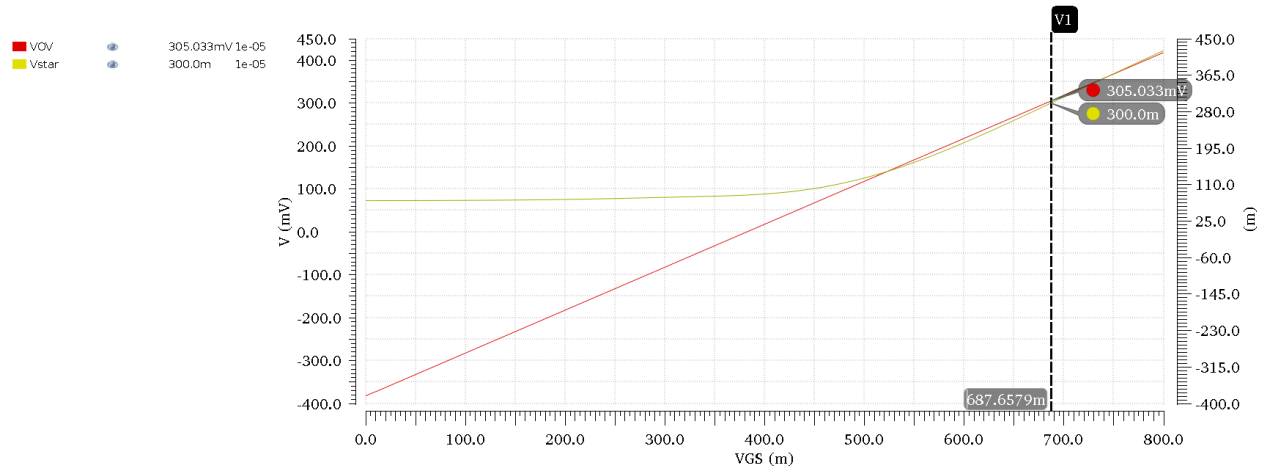


Figure 2 simulation V^* and V_{ov} vs V_{GS}

- from simulation we found at $V_{GSQ} = 687.6579mV$, $V_{OVQ} = 305.033mV$, $V_Q^* = 300mV$

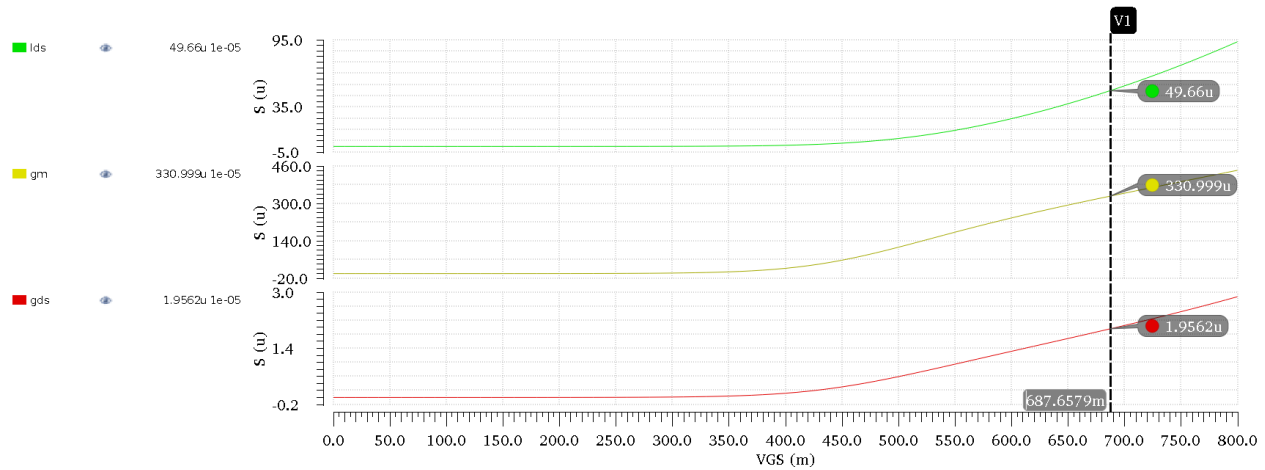


Figure 3 simulation I_D & g_m and g_{ds} VS V_{GS}

- from simulation we found the values in this table at $V_{GS} =$:

Parameter	Simulator value
V_{OVQ}	305.033mV
V_Q^*	300mV
I_{DQ}	49.66 μA
g_{mQ}	330.999 μS
g_{dsQ}	1.9562 μS

- I want current equal 150 μA but at $W=10\mu m$ $I_{DQ} = 49.66\mu A$

W	I_D
10 μm	49.66 μA
?	150 μA

Then $W = \frac{150\mu * 10\mu}{49.66\mu} = 30.205\mu m$

- get parameters at this W:

$$\frac{gm}{gm_Q} = \frac{W}{W_Q} \quad \frac{gm}{330.999\mu} = \frac{30.205}{10} \quad gm = 999.782\mu S$$

$$\frac{gds}{gds_Q} = \frac{W}{W_Q} \quad \frac{gds}{1.9562\mu} = \frac{30.205}{10} \quad gds = 5.9087\mu S$$

$$I_D = 150\mu A$$

$$r_o = \frac{1}{gds} = 169.241K\Omega$$

So common source parameters is:

Parameter	Value
L	2 μm
W	30.205 μm
V_{GSQ}	687.6579mV
I_D	150 μA
RD	6K Ω
gm	999.782 μS
gds	5.9087 μS
r_o	169.241K Ω

- Gain = gm*(RD || r_o)=5.8.

Part 2: CS Amplifier

1. OP and AC Analysis

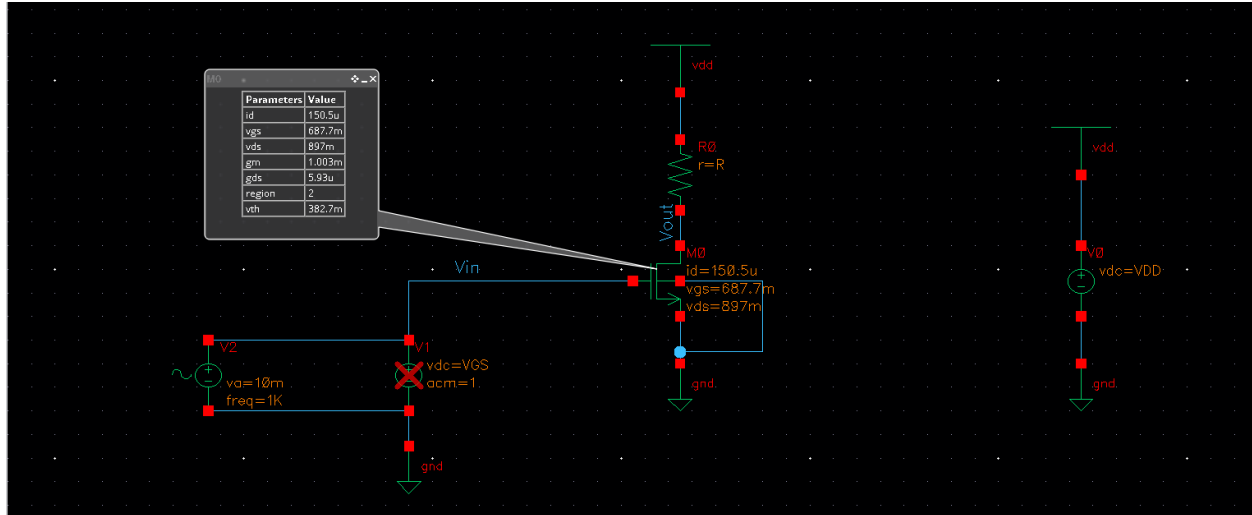


Figure 4 operating point in cs amplifier

Parameter	Part 1	CS amplifier
I_D	150 μA	150.5 μA
g_m	999.782 μS	1.003mS
g_{ds}	5.9087 μS	5.93 μS
r_o	169.241K Ω	168.634K Ω
V_{GSQ}	687.6579mV	687.7mV

• From these results we found that there is a great agreement between our results in part 1 and CS amplifier simulation part and region of MOSFET equal 2 which mean that MOSFET is in saturation region.

3) $r_o = 168.634\text{K}\Omega \gg RD = 6\text{K}\Omega$

$R_{OUT} = r_o || RD = 5.8\text{K}\Omega$ which is very close to RD we can neglect value of r_o .

• if we minimum channel length the value of r_o will decrease following the relation $r_o = \frac{1}{\lambda I_{DS}}$

and $\lambda \propto \frac{1}{L} \therefore r_o \propto L$.

• Intrinsic gain of MOSFET will be:

$$A_v = -g_m * r_o = -169.14.$$

• Amplifier gain analytically:

$$A_v = -g_m * (r_o || RD) = -5.81.$$

From these calculations we found that intrinsic gain is greater than amplifier gain

\therefore intrinsic gain \gg amplifier gain.

•From simulation we can find DC gain:

DC gain =5.813

DC gain in dB =15.29

This values meets specs .

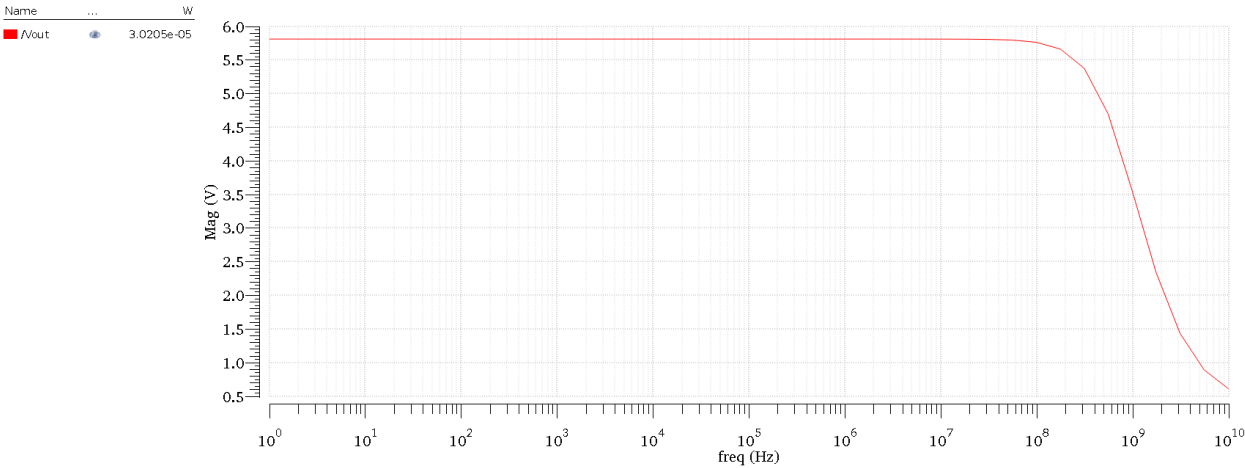


Figure 5 plot gain vs Frequency

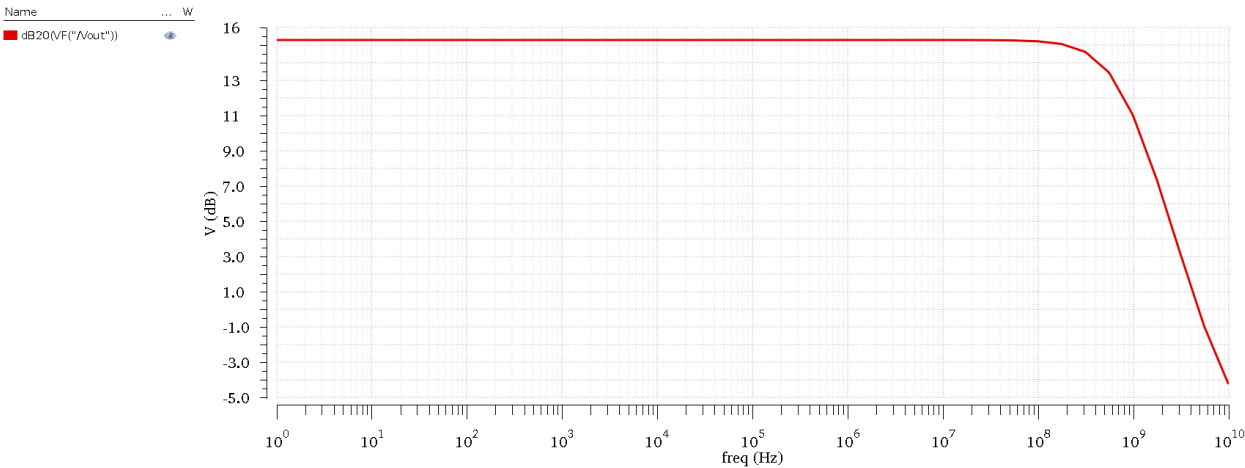


Figure 6 plot gain in dB VS Frequency



Test	Output	Nominal	Spec	Weight	Pass/Fail
LAB02:Common_Source:1	Vout				
LAB02:Common_Source:1	dB20(VF("/Vout"))				
LAB02:Common_Source:1	ymax{dB20(VF("/Vout"))}	15.29			
LAB02:Common_Source:1	ymax{mag(VF("/Vout"))}	5.813			

Figure 7 values of gain from simulator

2. Gain Non-Linearity

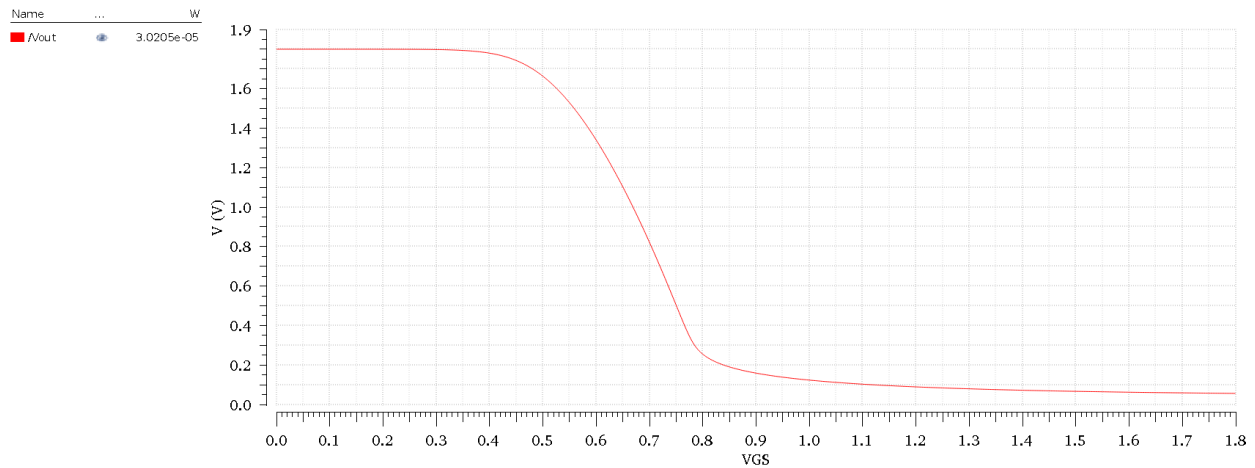


Figure 8 V_{out} Vs V_{in} in DC Sweep

- The relationship is not linear because the MOSFET's g_m is not constant and $\text{gain} = g_m \cdot R_D$. It decreases as V_{GS} increases. This is because the MOSFET's channel becomes narrower as V_{GS} increases, which reduces the number of carriers that can flow through the channel. The MOSFET enters the saturation region when V_{GS} is too large. In the saturation region, the drain-source current is nearly constant, so V_{OUT} is no longer proportional to V_{in} .

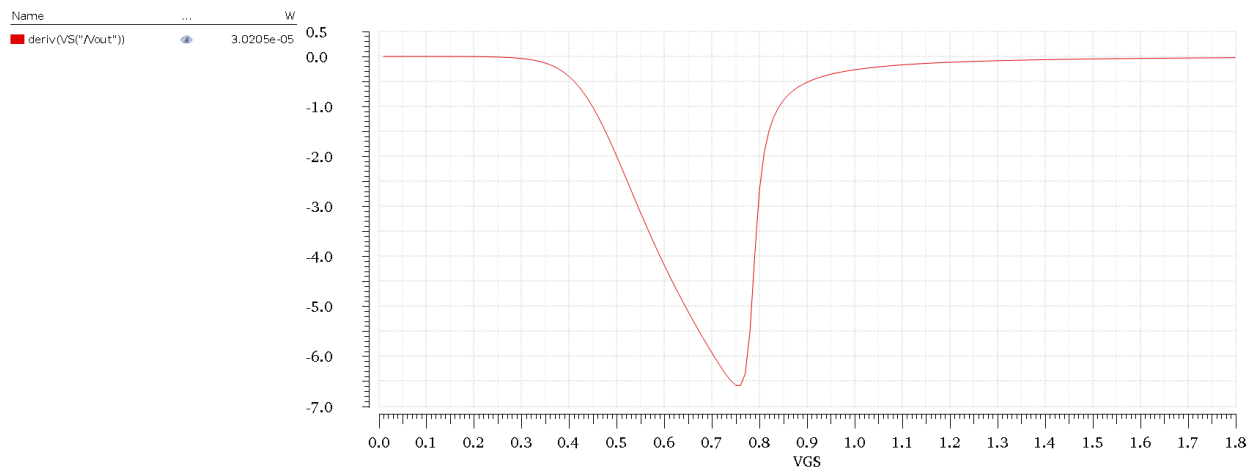


Figure 9 derivative of V_{out} vs V_{in}

- The figure shown in Figure 9 indicates that the derivative of V_{OUT} is not a constant value. This is because the relationship between V_{OUT} and V_{in} is not linear, as it depends on the transconductance (g_m), which is dependent on V_{GS} .

Transient analysis:

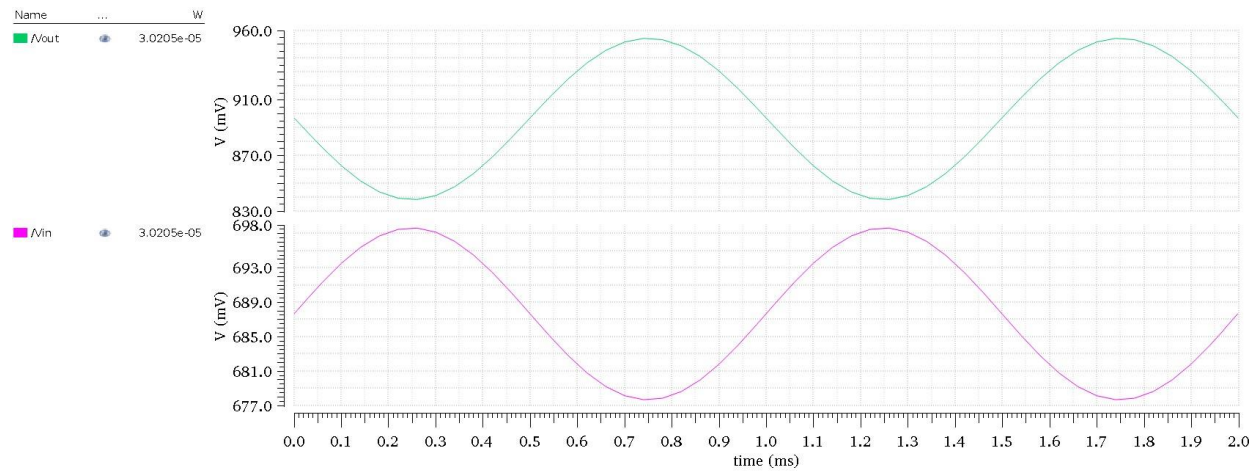


Figure 10 transient analysis of V_{out} and V_{in}

Plot gm in transient analysis

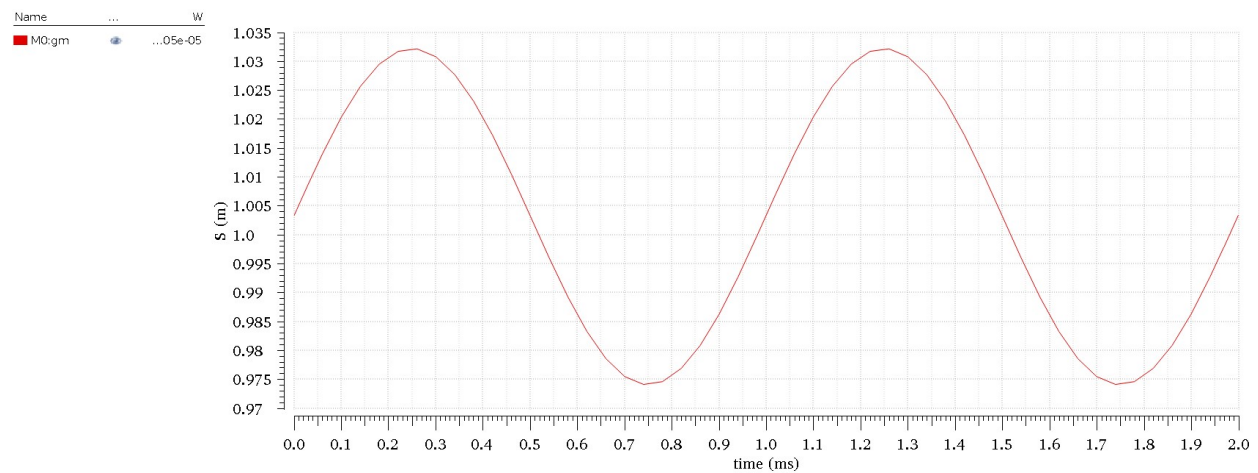


Figure 11 plot g_m in transient analysis

5) as shown figure 11, The variation of g_m over time demonstrates that g_m is dependent on the input voltage V_{GS} , which in turn is time dependent. Therefore, g_m exhibits a strong dependence on the input voltage V_{GS} .

6) Based on the previous analysis, it is evident that our amplifier is a non-linear amplifier due to the non-linear relationship between its input voltage and output voltage. This non-linearity arises from the dependence of the amplifier's gain on g_m , which is heavily strongly by the input voltage V_{GS} . As a result, the non-linear dependence of g_m on V_{GS} leads to non-linearity in the amplifier's gain.