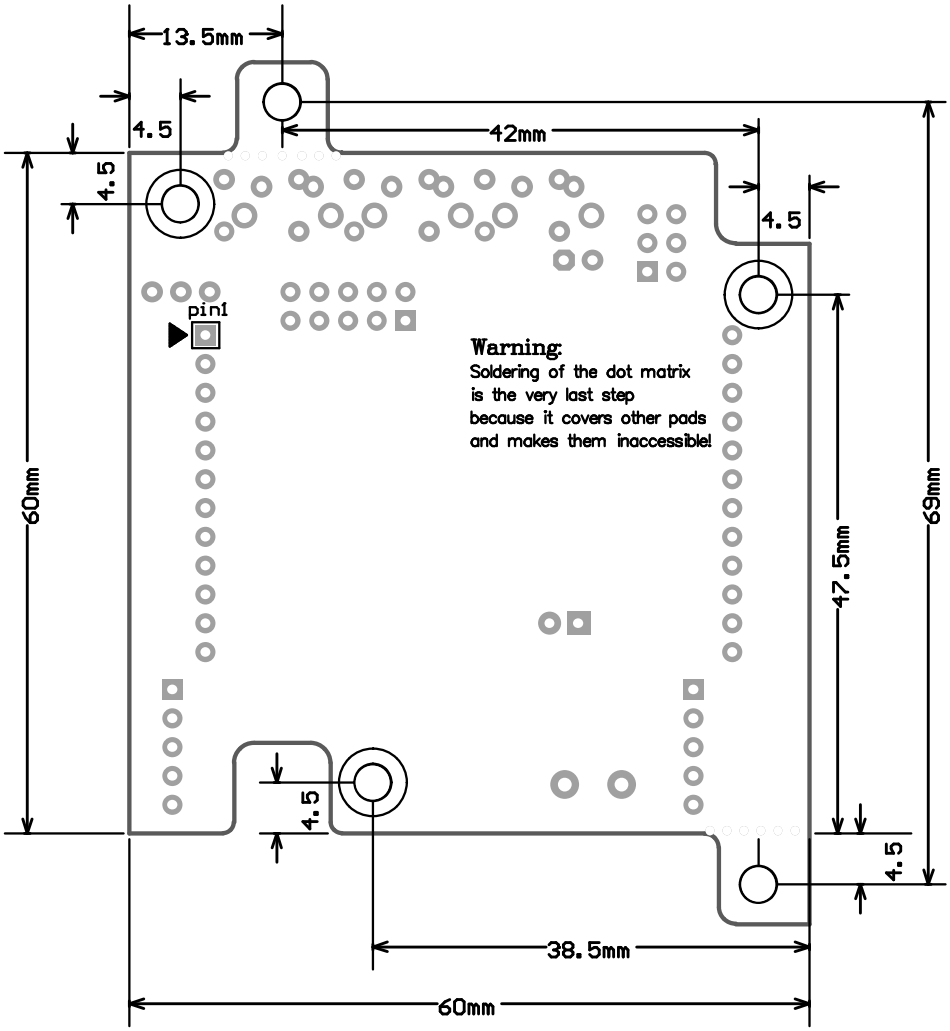
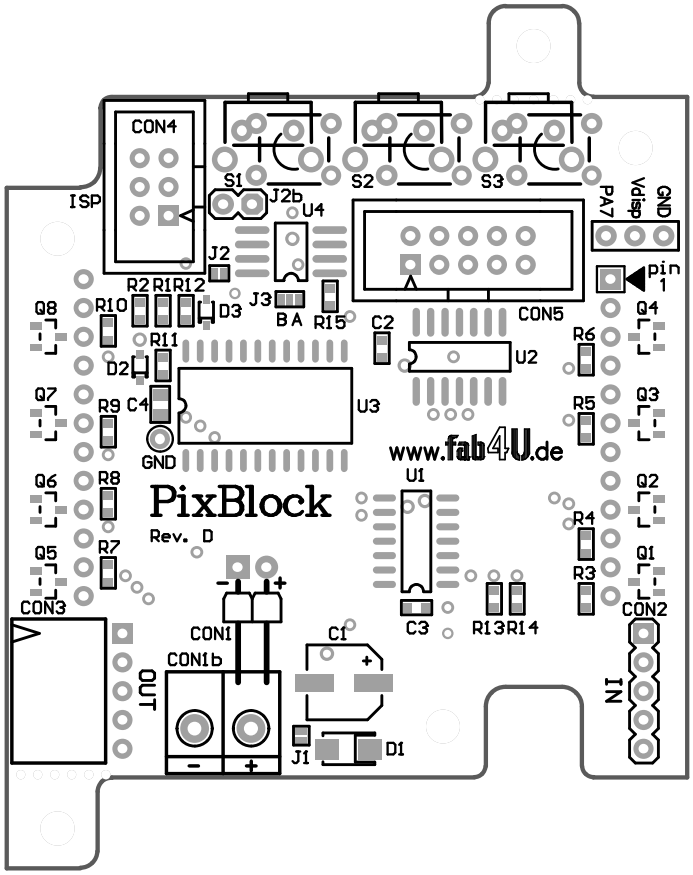
PixBlock



Rev. D


CON4
ISP
S1
J2b
U4
S2
S3
PA7
Vdisp
GND
J2
pin 1
R2
R1
R12
D3
J3
B
A
R15
C2
CON5
Q8
R10
R11
D2
U2
R6
Q4
Q7
R9
C4
U3
R5
Q3
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R8
U1
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Q2
Rev. D
R4
Q5
R7
Q1
R3
CON3
CON1
C1
C3
R13
R14
CON2
CON1b
OUT
IN
J1
D1