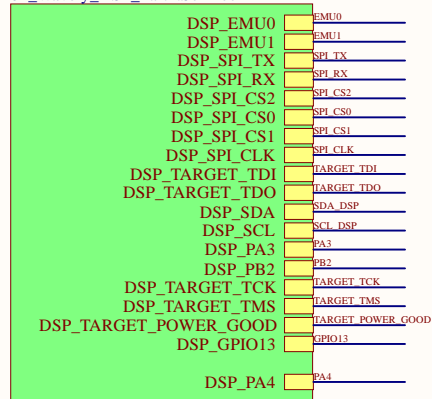
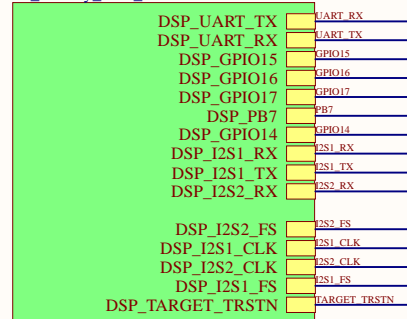


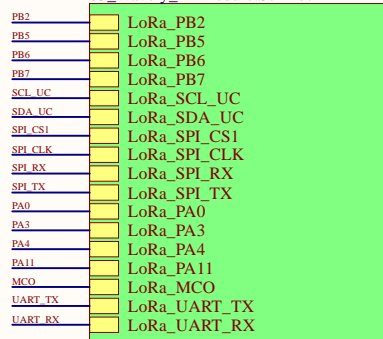
U\_01\_Wavely\_DSP\_Part1  
01\_Wavely\_DSP\_Part1.SchDoc



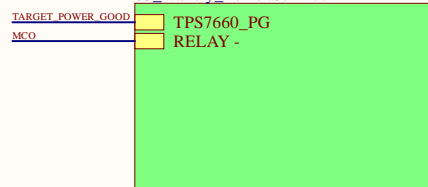
U\_02\_Wavely\_DSP\_Part2  
02\_Wavely\_DSP\_Part2.SchDoc



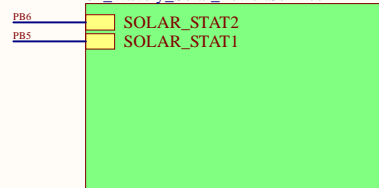
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03\_Wavely\_RFModule.SchDoc



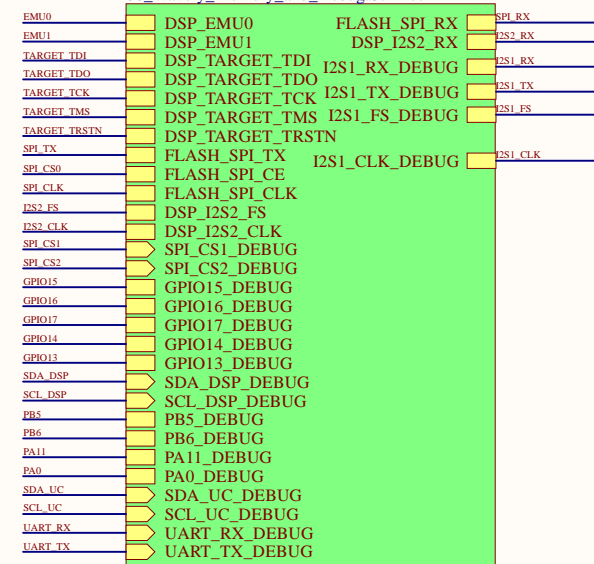
U\_05\_Wavely\_Power  
05\_Wavely\_Power.SchDoc



U\_04\_Wavely\_Solar\_Power  
04\_Wavely\_Solar\_Power.SchDoc



U\_06\_Wavely\_Memory\_and\_Debug  
06\_Wavely\_Memory\_and\_Debug.SchDoc



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File:	C:\Users\...\00_Wavely.SchDoc	Drawn By:	



A

B

C

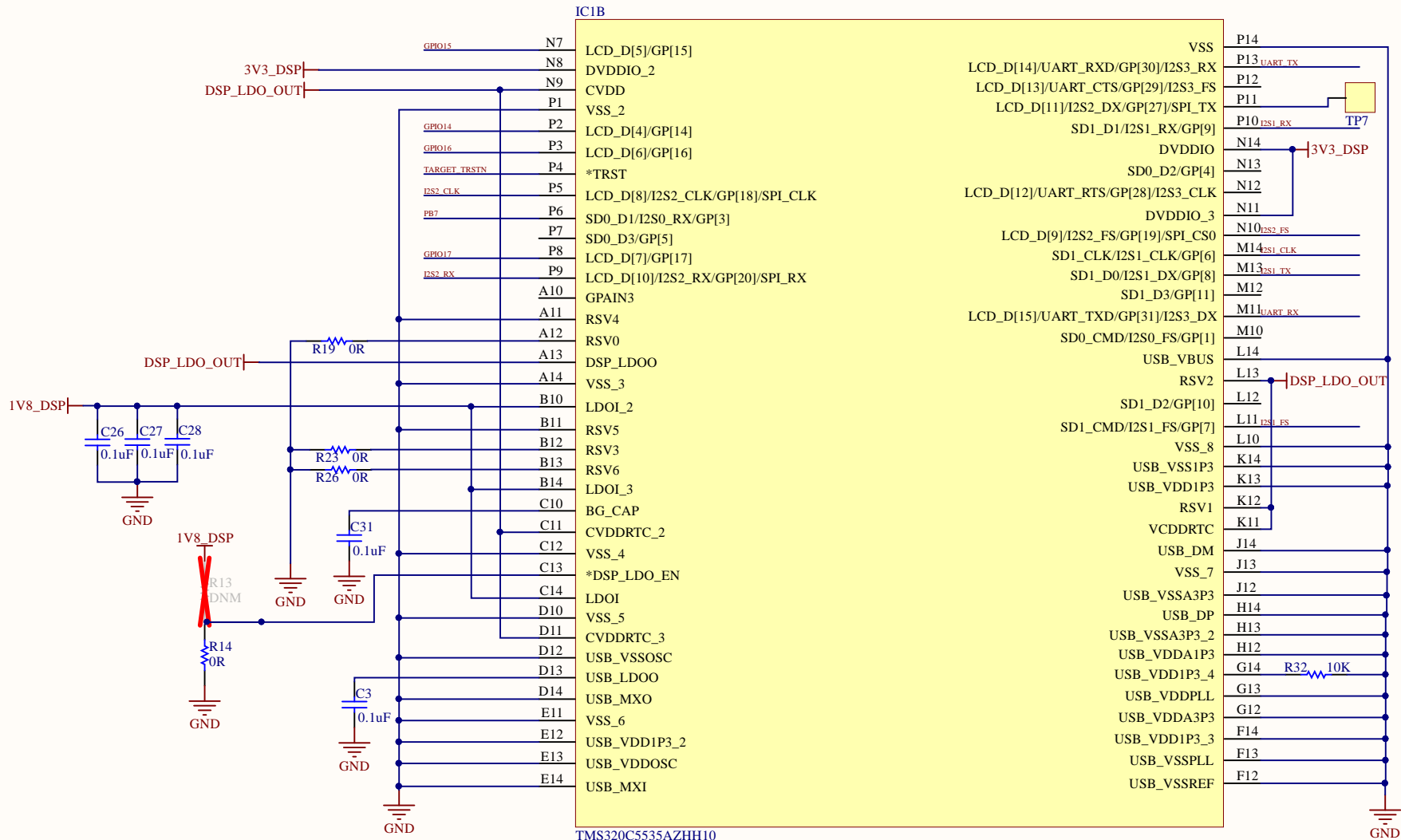
D

A

B

C

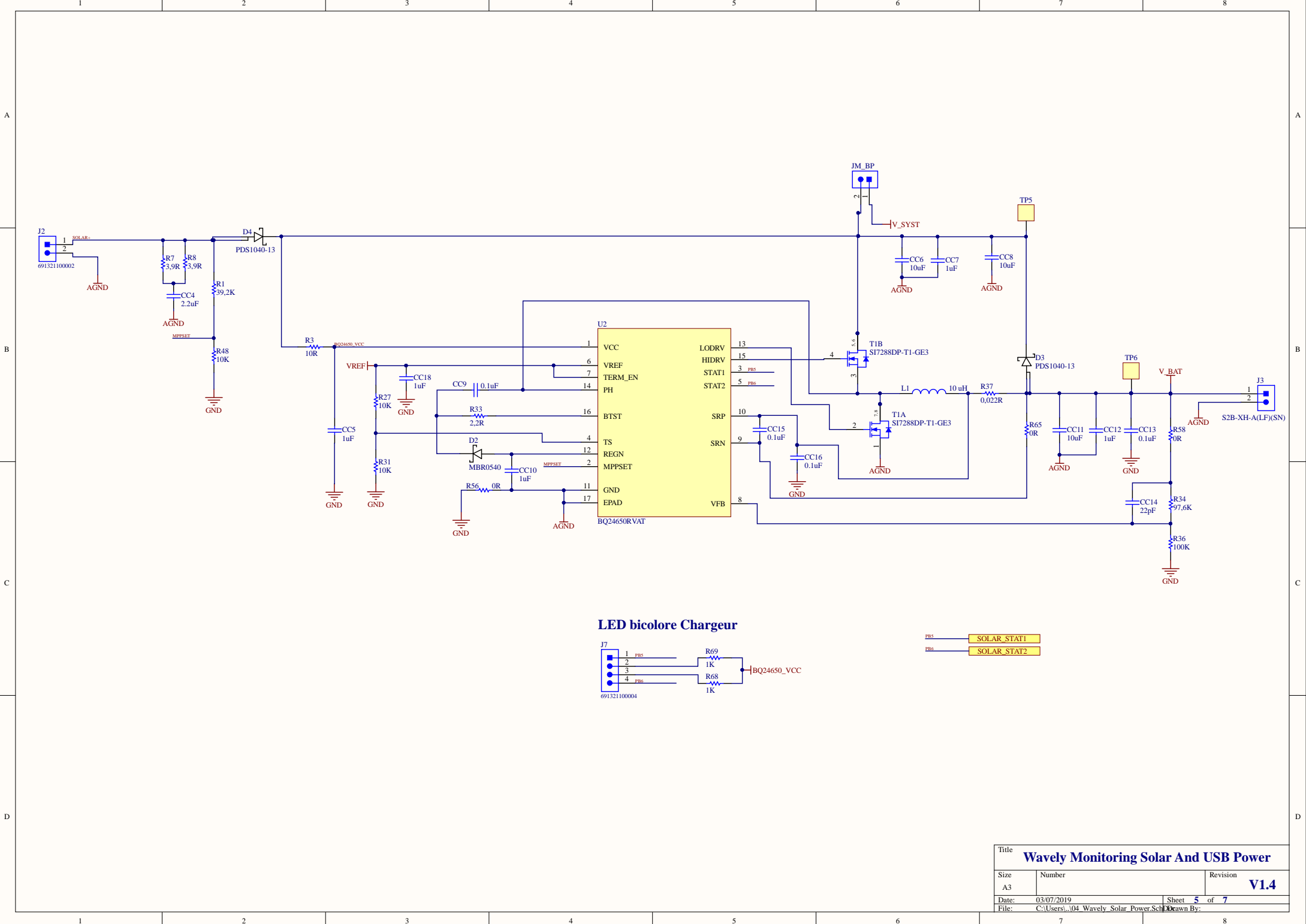
D



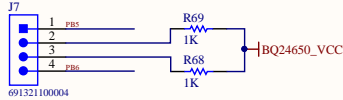
I2S2_CLK	DSP_I2S2_CLK	GPIO14	DSP_GPIO14
I2S2_RX	DSP_I2S2_RX	GPIO15	DSP_GPIO15
		GPIO16	DSP_GPIO16
I2S2_FS	DSP_I2S2_FS	GPIO17	DSP_GPIO17
I2S1_CLK	DSP_I2S1_CLK	PB7	DSP_PB7
I2S1_RX	DSP_I2S1_RX	UART_RX	DSP_UART_TX
I2S1_TX	DSP_I2S1_TX	UART_TX	DSP_UART_RX
I2S1_FS	DSP_I2S1_FS	TARGET_TRSTN	DSP_TARGET_TRSTN

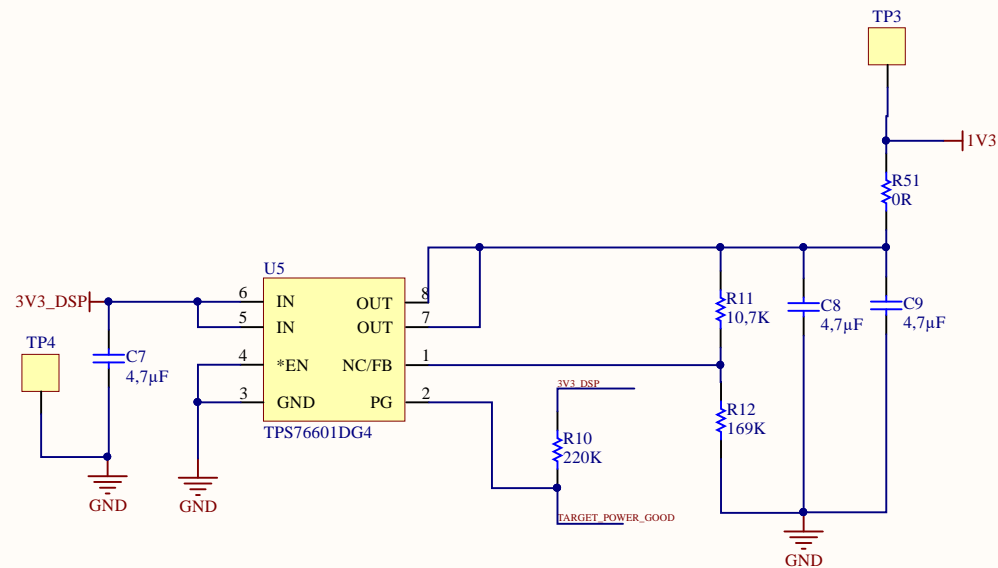
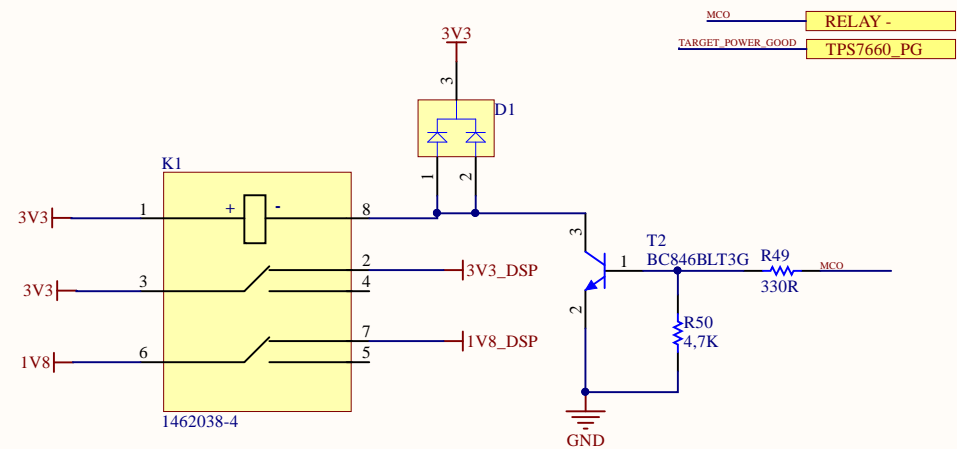
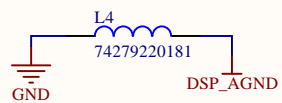
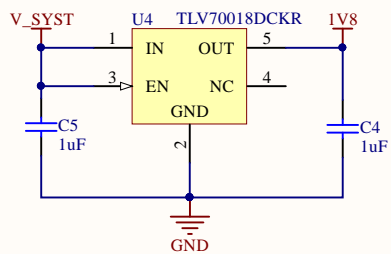
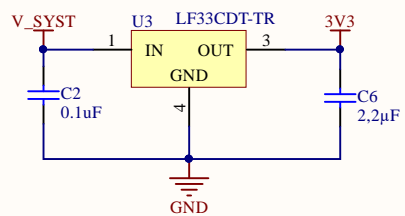
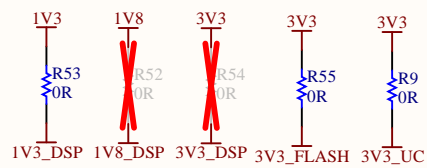
Title <b>Wavely Monitoring DSP Part2</b>		
Size A4	Number	Revision <b>V1.4</b>
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File:	C:\Users\...\02_Wavely_DSP_Part2.SchDocDrawn By:	





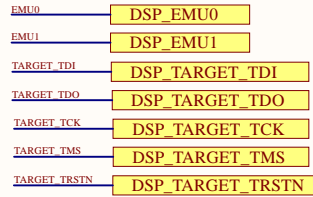
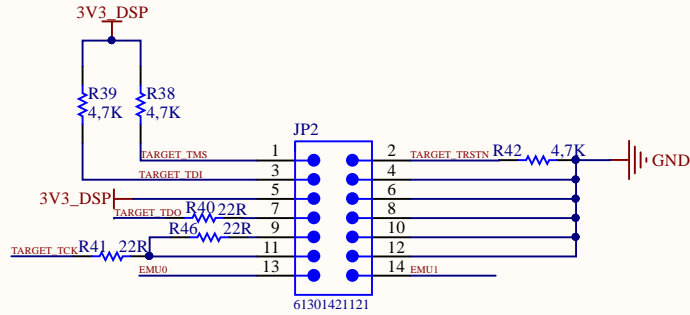
LED bicolore Chargeur



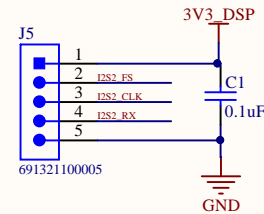


Title		
Wavely Monitoring Power		
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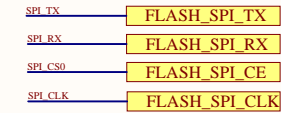
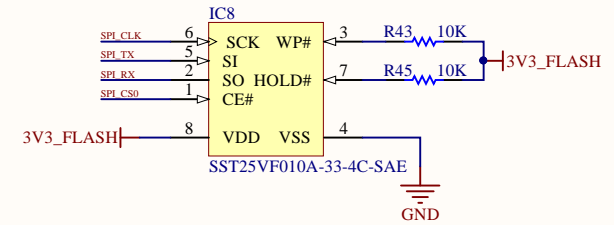
## DSP JTAG



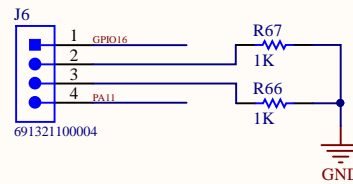
## Microphone Connector



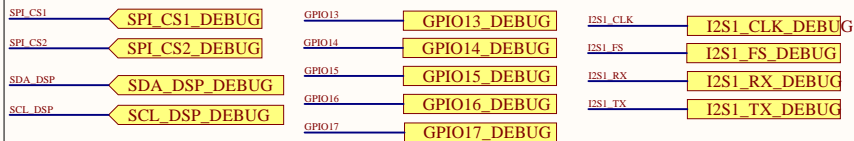
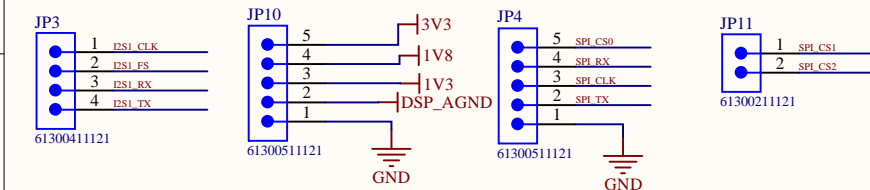
## Flash Memory



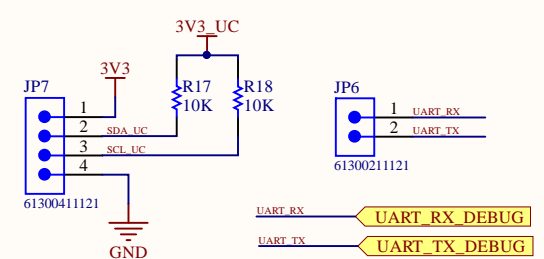
## LED bicolore DSP/Murata



## Debug DSP



## Debug Murata



## Wavely Monitoring Memory and Debug

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