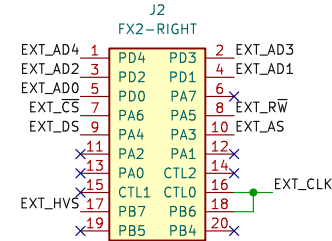
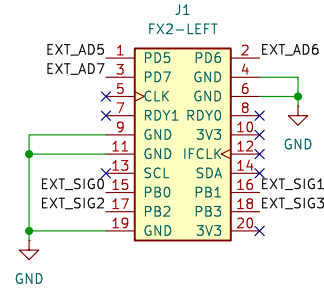
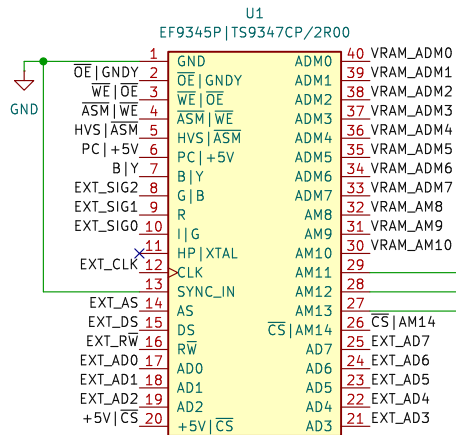
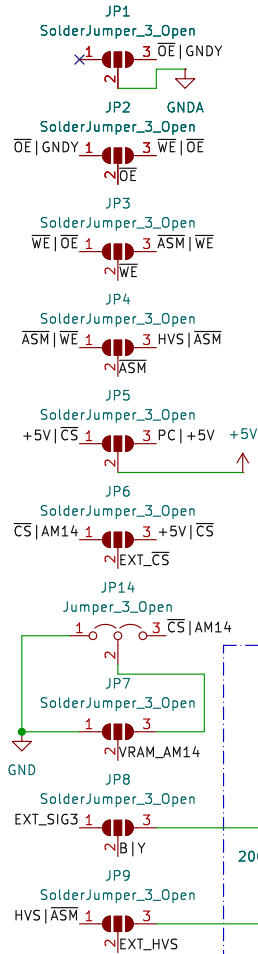
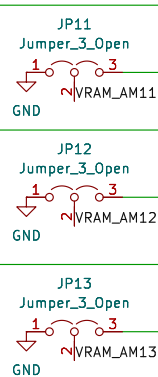
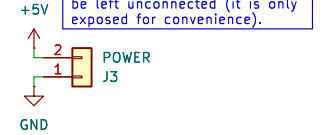


This column of solder jumpers selects the appropriate pin layout for the chip.

ALL LEFT = EF9345  
ALL RIGHT = TS9347/2R00



Note: The clock generated by the FX2 is fed back into PB6 so it can be captured in the resulting USB stream.



These jumpers JP11, JP12 and JP13 (along with JP14) mask RAM address lines as follows:

\* JP11-JP14 LEFT (grounded) = only 2 KiB of RAM are available to the video chip

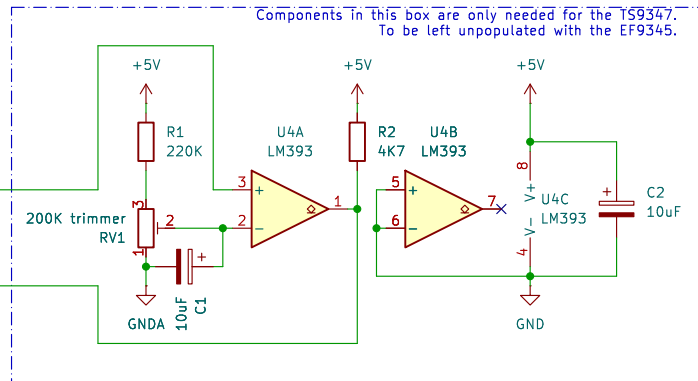
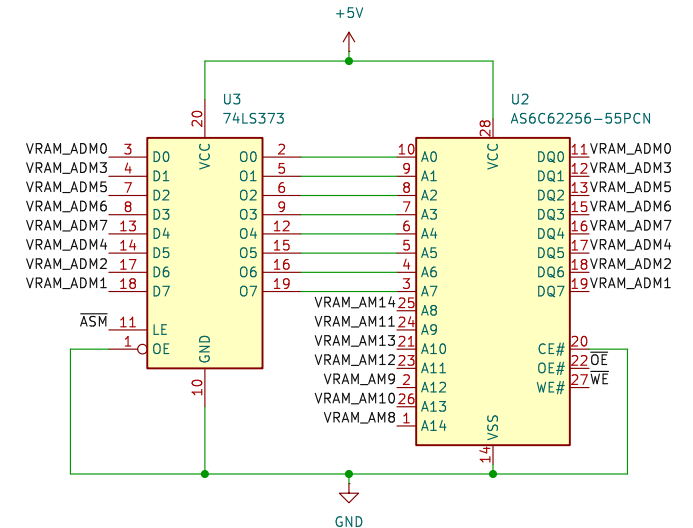
\* JP11 RIGHT, JP12-14 LEFT = 4 KiB

\* JP11-JP12 RIGHT, JP13-14 LEFT = 8 KiB

\* JP11-JP13 RIGHT, JP14 LEFT = 16 KiB

\* JP11-JP14 RIGHT = 32 KiB

Note: JP14 is ignored with the EF9347, which can only address up to 16 KiB.



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