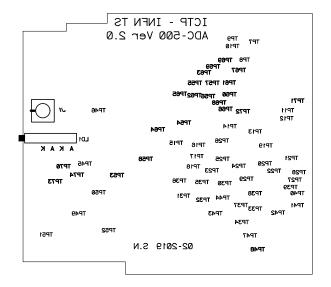
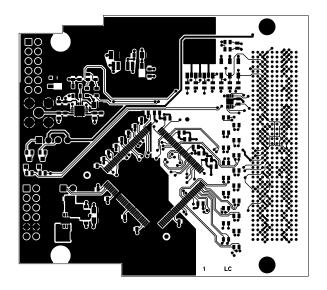


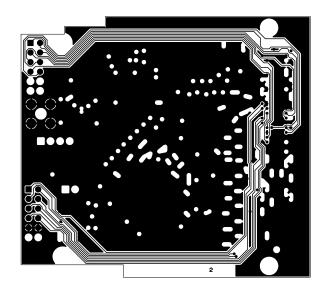
Da†a		Progetto									
11/02/2	11/02/2019		ICTP - INFN TS ADC-500 Ver								
Rev. 2			Г-	TIVI	ΓIN	13	ADC-50	טע	ver	Z.W	
PCB Rev. 2 Particolare PCB LAYOUT											
Size:	Δ4	Sheet	of		File	. (CERN1N2	ΤL	aver:	SERIGRAFIA TOF	



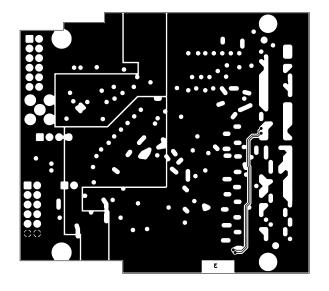
Data			Progetto:									
	11/02/2019		I TOT	ICTP - INFN TS ADC-500 Ver 2.0								
	Rev.	2		г -	T 1/1	TIN IS	3 ADC-30	שט עפו	۷.ک			
	PCB Rev.	2	particola	re:	PCE	3 LAY	/OUT					
	Size:	Α4	Shee+	of		File:	CER01N2	Layer:	SERIGRAFIA BO	TTOM		



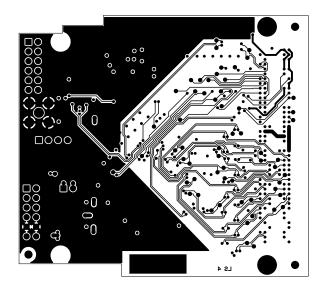
	Data	Progetto	ı									
	11/02/2019		I TOT	ICTP - INFN TS ADC-500 Ver 2.0								
	Rev.	2		Γ-	TIA	FIN I	3 ADC-30	o vei	2.0			
PCB Rev. 2 Particolare PCB LAYOUT												
	Size:	Δ4	Sheet	of		File	CER01N2	Laver:	LATO TOP			



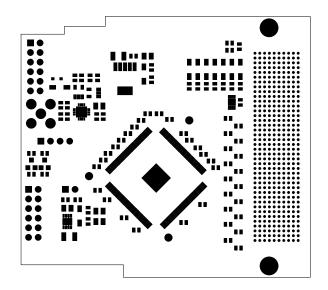
Data		Progetto	•						
11/02/2	:019	I TOT	D	TNI	ENI T	C 100 E0	0 Ver 2.0		
Rev.	2		Г -	TIVI	FIN I	3 ADC-30	W VEI 2.W		
PCB Rev.	2	Particolare: PCB LAYOUT							
Size	Α4	Shee+	of		File:	CER01N2	Layer:		



Data		Progetto:			•					
11/02/2	2019	TOTE	TNI	ENI TO	2 ADC 50	0 Van 3 0				
Rev. 2		ICIF.	ICTP - INFN TS ADC-500 Ver 2.0							
PCB Rev.	2	particolare:	PC	B LA`	YOUT					
Size:	Δ4	Sheet of		File:	CER01N2	Laver:				



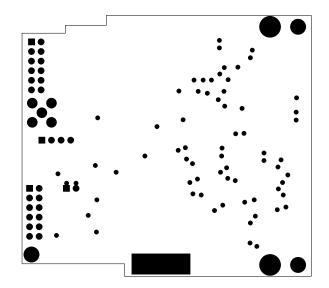
Data	Progetto	ı									
11/02/2019		I TOT	Ъ	TNI	-NI	TC	ADC-50	מה	Von	2 0	
Rev.	2		Γ -	TIVI	- 1/1	13	ADC-50	טע	ver	2.0	
PCB Rev.	2	particolare: PCB LAYOUT									
Size:	Δ4	Sheet	of		File	. (CERQ1N2	ΤL	aver:	LATO BOTTOM	



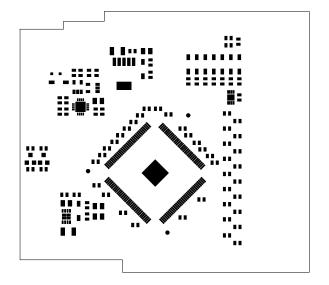
Data		Progetto:								
11/02/2019		TOTE	ICTP - INFN TS ADC-500 Ver 2.0							
Rev.	2	ICIP -	TIN	FIN I	S ADC-50	ver	2.0			
PCB Rev.	2	particolare:	PCI	3 LA	YOUT					
C1701	۸./	Sheet of		File	CEDMINO	Laver	SOLDER TOP			

Size: A4 Sheet of File: CER01N2 Layer: SOLDER TOP

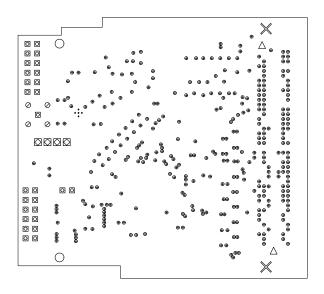
Il proprietario del presente documento si riserva di agire a termine di legge contro chi lo divulghi senza previa autorizzazione scritta



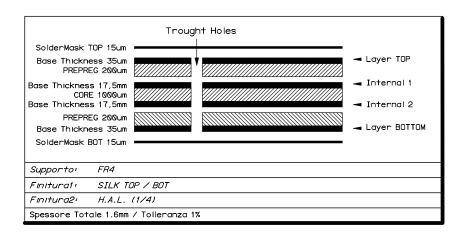
Data		Progetto									
11/02/2019		тст	ICTP - INFN TS ADC-500 Ver 2.0								
Rev.	2	101	Г -	TIVI		3 ADC-30	o vei	2.0			
PCB Rev.	2	particola	re:	PCE	3 LA	YOUT					
Size	Α4	Sheet	of		File	CER01N2	Layer:	SOLDER BOTTOM			



Data 11/02/2019		Progetto);						
] _{TC} -	ICTP - INFN TS ADC-500 Ver 2.0						
Rev.	2	10	-	TIVI	T IN I	3 ADC-30	u vei	۷.۷	
PCB Rev.	2	particol	ire:	PC	3 LA	YOUT			
Size:	Α4	Shee+	of		File	CER01N2	Layer:	PASTA TOP	



Through Holes All Drills (unless specified) +0.10 / -0.05 (MM)								
Symbol	Diameter	Plated	Quantity					
	0.20	Yes	5					
⊕	0.30	Yes	348					
	0.80	Yes	4					
O	1.00	Yes	27					
0	1.30	Yes	4					
×	2.70	Yes	2					
Δ	1.30	No	2					
0	2.70	No	2					





Data		Progetto	•								
	11/02/2019		тст	ICTP - INFN TS ADC-500 Ver 2.0							
	Rev.	2	101	Г -	TIVI	TIN I	S ADC-50	o ver	. U		
PCB Rev. 2			particola	re	PCE	3 LA`	YOUT				
	Size	Α4	Shee†	of		File	CER01N2	Layer:	LATO FORATURA		

