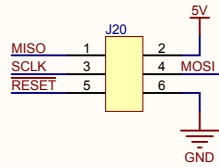
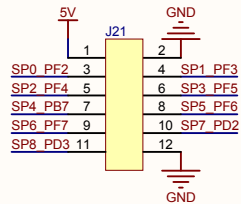


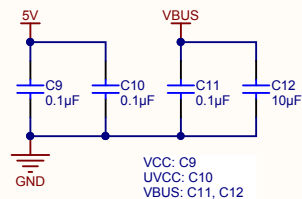
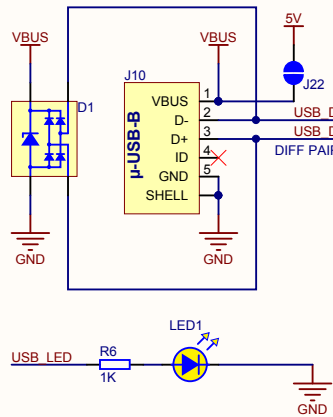
ISP



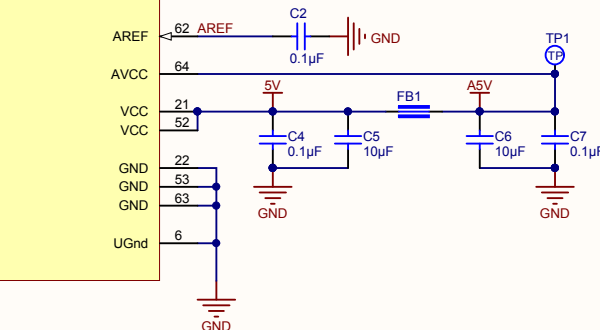
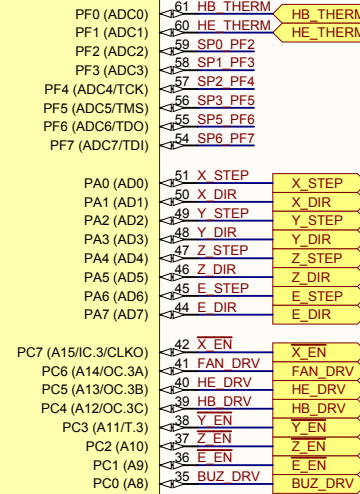
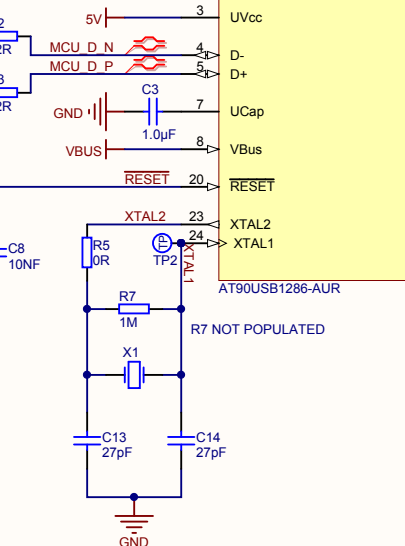
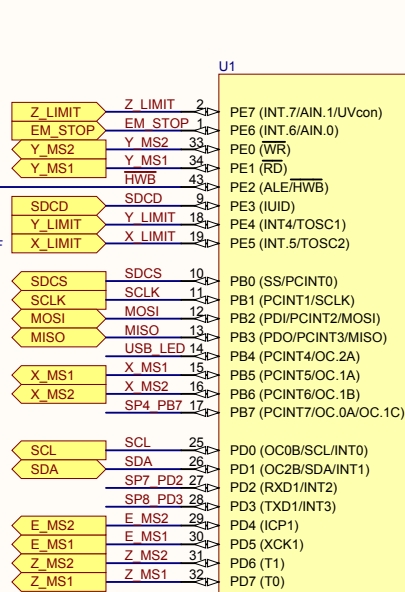
EXPANSION



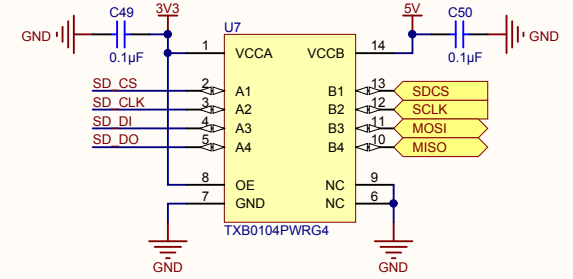
SHORT J22 TO POWER BOARD LOGIC OVER USB



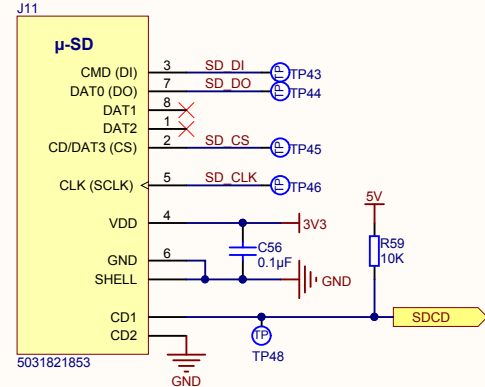
MCU



LEVEL TRANSLATOR



µSD CARD



Title: 5DPD8 Controller

Revision: 1.0

Engineer: Martyn Kay

Drawn By: Martyn Kay

Checked: J. Wong/C. Lai

Date: 10th Mar 14

Sheet: 1 of 4

Size: A4

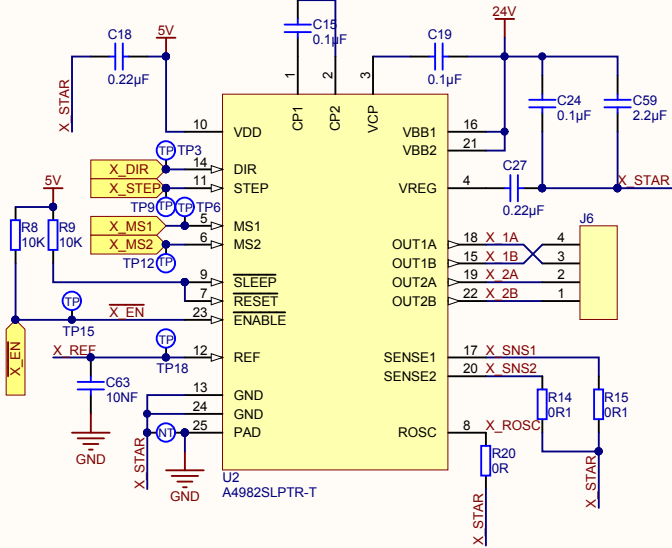
Design File Link: D8 Design Files

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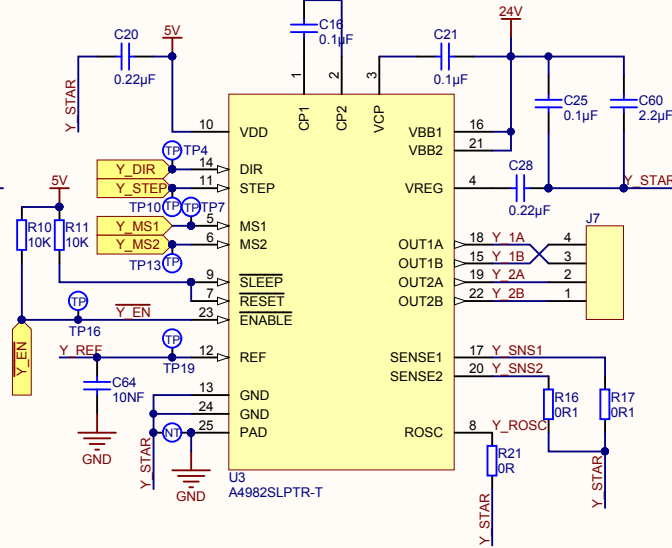
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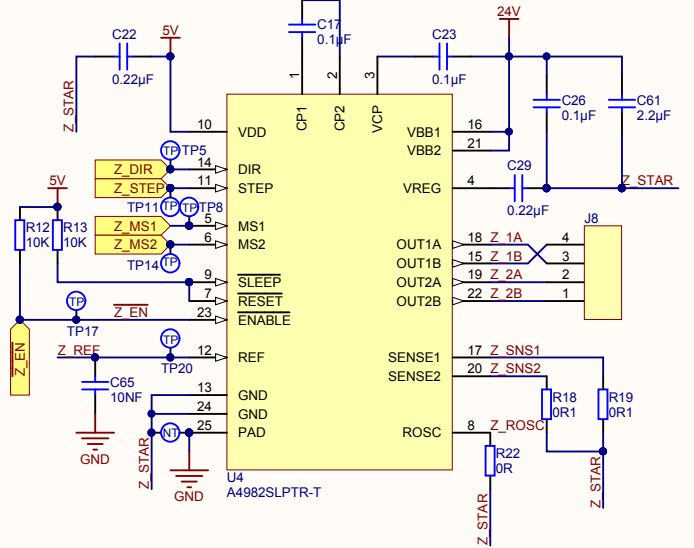
X-AXIS STEPPER DRIVER



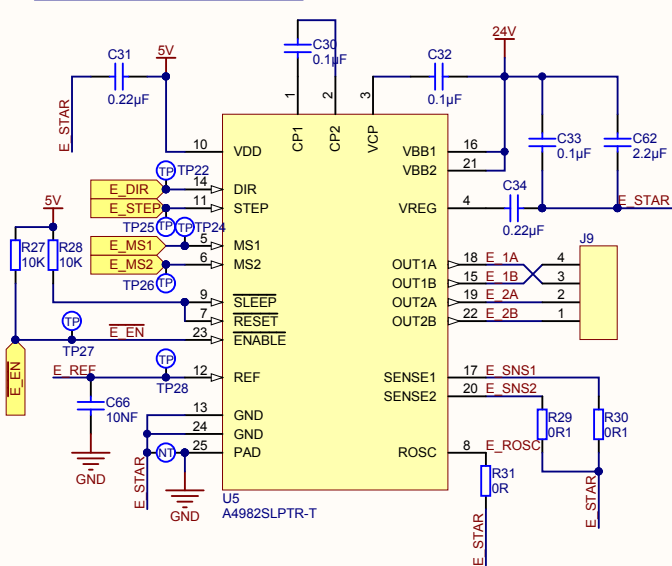
Y-AXIS STEPPER DRIVER



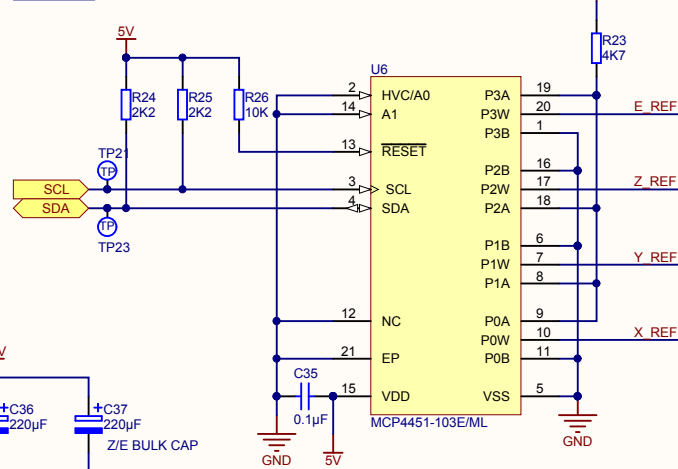
Z-AXIS STEPPER DRIVER



EXTRUDER STEPPER DRIVER



DIGI POT



uSTEP SETTINGS

MS1	MS2	Microstep Resolution	Excitation Mode
L	L	Full Step	2 Phase
H	L	Half Step	1-2 Phase
L	H	Quarter Step	W1-2 Phase
H	H	Sixteenth Step	4W1-2 Phase

STEPPER BULK CAPS
PLACE CLOSE TO CORRESPONDING VBB PINS



Title: 5DPD8 Controller

Revision: 1.0

Engineer: Martyn Kay

Drawn By: Martyn Kay

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Date: 10th Mar 14

Sheet: 2 of 4

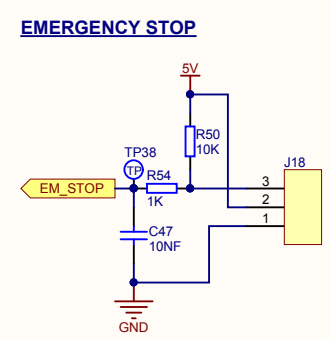
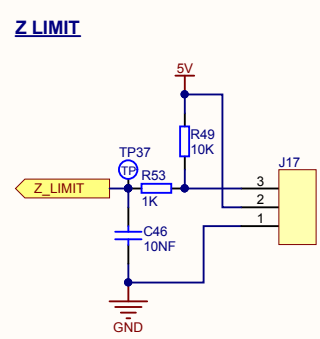
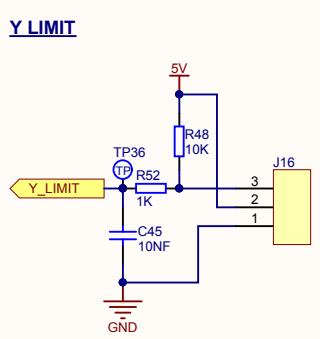
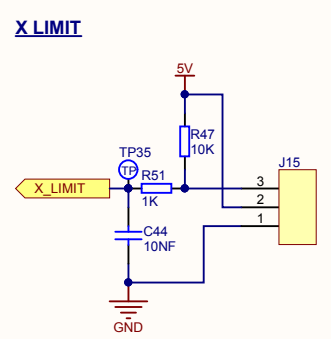
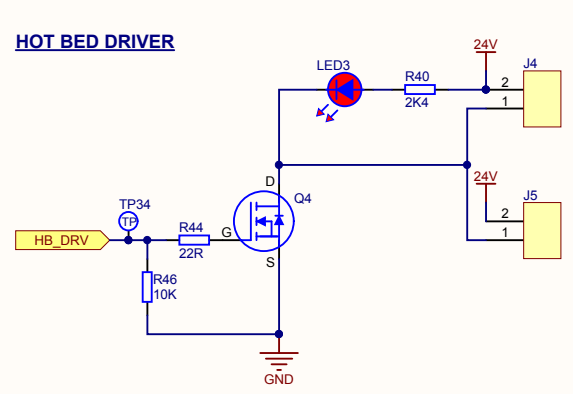
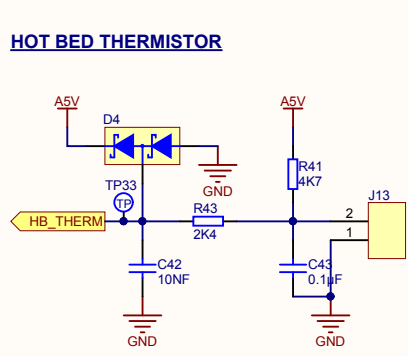
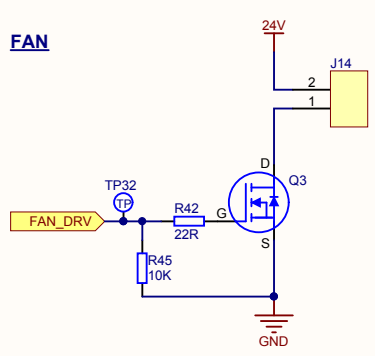
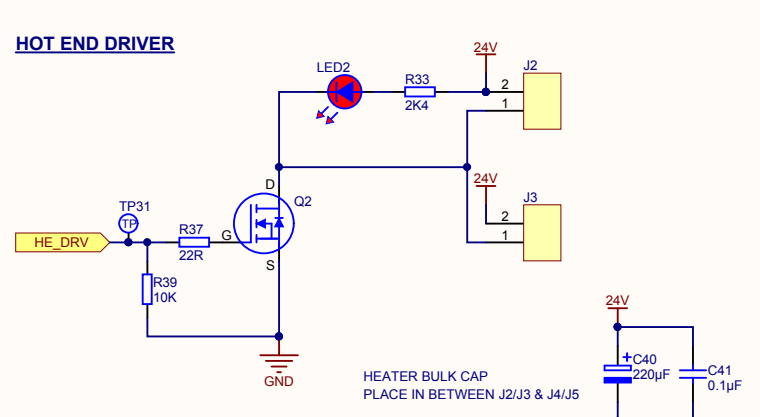
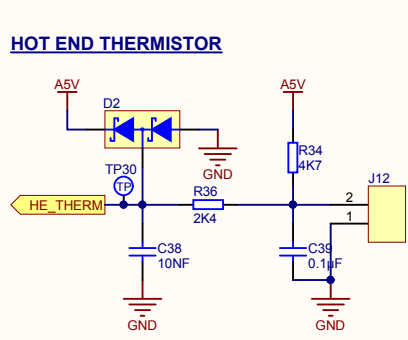
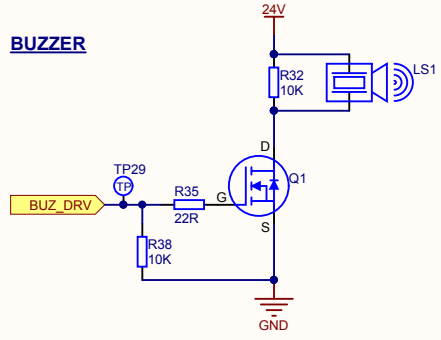
Size: A4


Design File Link: D8 Design Files

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Title: 5DPD8 Controller

Revision: 1.0 Engineer: Martyn Kay Drawn By: Martyn Kay Checked: J. Wong/C. Lai

Date: 10th Mar 14 Sheet: 3 of 4 Size: A4 Design File Link: D8 Design Files

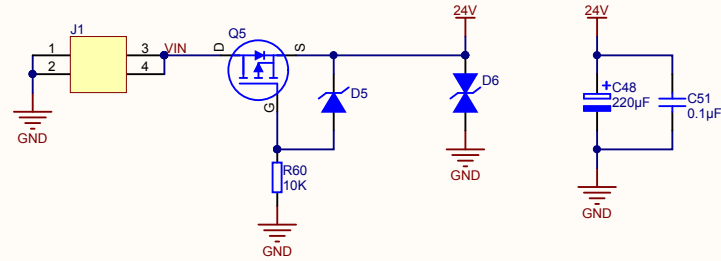
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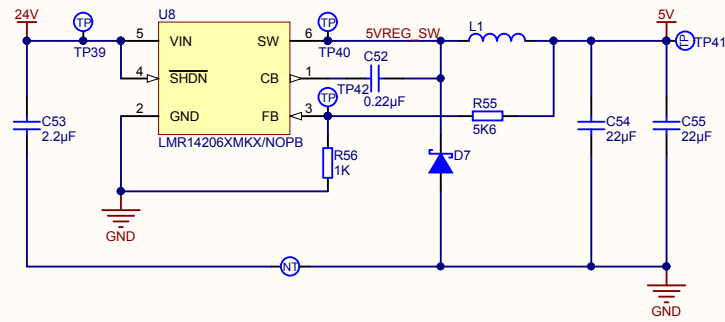


open source hardware

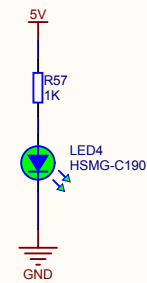
12-24V INPUT STAGE



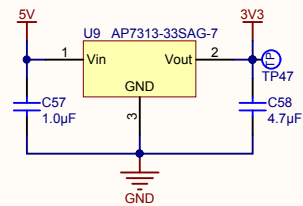
5V REGULATOR



POWER LED



3.3V REGULATOR



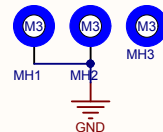
SILKSCREEN LOGOS



FIDUCIALS



MOUNTING HOLES



Title: 5DPD8 Controller

Revision: 1.0	Engineer: Martyn Kay	Drawn By: Martyn Kay	Checked: J. Wong/C. Lai
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