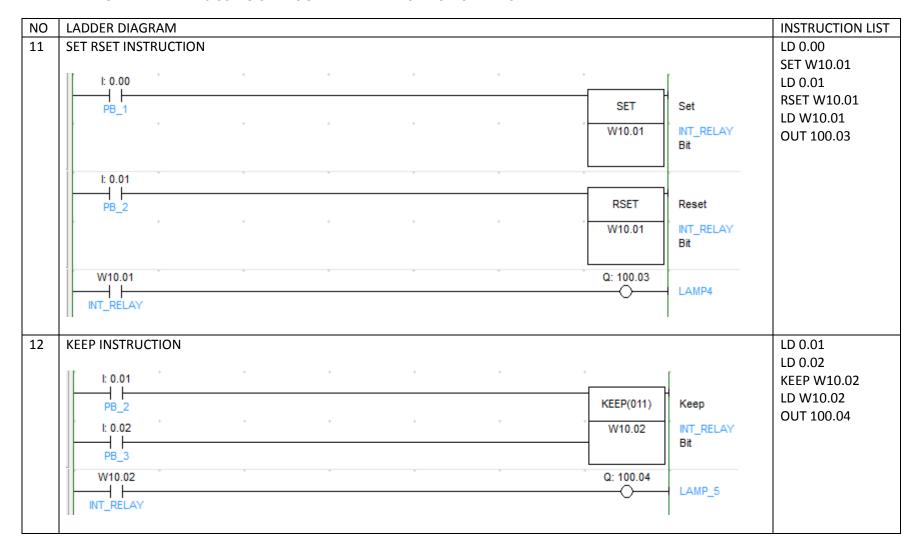
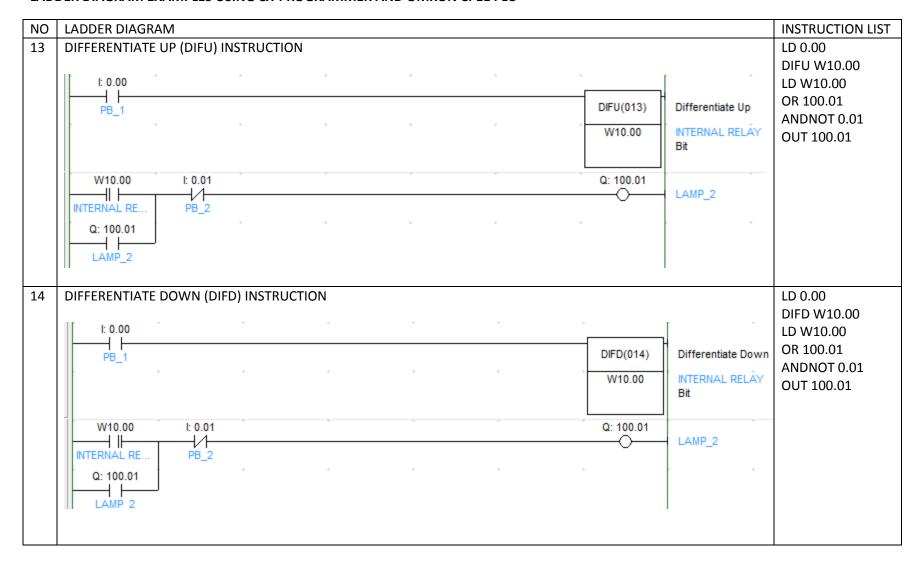
NO	LADDER DIAGRAM	INSTRUCTION LIST
1	LD INSTRUCTION	LD 0.00
1	LD INSTRUCTION	
	I: 0.00 Q: 100.00 LAMP_1	OUT 100.00
	PB_1	
2	LDNOT INSTRUCTION	LDNOT 0.00
-		
	I: 0.00	OUT 100.00
	LAMP_1	
	PB_1	
	AND INSTRUCTION	10.00
3	AND INSTRUCTION	LD 0.00
	The same of the same of	AND 0.01
	0.00	OUT 100.00
	LAMP_1	
	PB_1 PB_2	
4	ANDNOT INSTRUCTION	LDNOT 0.00
		ANDNOT 0.01
		OUT 100.00
	LAMP_1	001 100.00
	PB_1 PB_2	
3	OR INSTRUCTION	LD 0.00
3		
	[I: 0.00	OR 0.01
	LAMP_1	OUT 100.00
	PB_1	
	l: 0.01	
	PB_2	

NO	LADDER DIAGRA	AM					INSTRUCTION LIST
4	ORNOT INSTRU	CTION					LDNOT 0.00
					_		ORNOT 0.01
	1: 0.00		*	Ť	Ť	Q: 100.00	OUT 100.00
	PB_1					LAMP_1	
	1: 0.01						
	1	J					
	PB_2					l	
5	MULTIPLE OUT	DIIT					LD 0.02
	WIGETH EE GOT	101					OUT 100.00
	1: 0.02		*	*	*	° Q: 100.00	OUT 100.01
						LAMP_1	OUT 100.02
	PB_3						
						Q: 100.01 LAMP_2	
						DAMP_2	
				+	+	Q: 100.02	
						LAMP_3	
						ı	
6	ANDLD						LD 0.00
							OR 0.02
	1: 0.00	l: 0.01	•	*	+	° Q: 100.03	LD 0.01
	PB_1	PB_2				LAMP4	OR 0.03
		1 - 1					ANDLD
	1: 0.02	1: 0.03					OUT 100.03
	PB_3	PB_4					
						1	
7	ORLD		*	+	+		LD 0.00
	l: 0.00	l: 0.01	Ÿ	Ť	*	Q: 100.04	AND 0.01
	PB_1	PB 2				LAMP_5	LD 0.02
	1: 0.02	l: 0.03	*			*	AND 0.03
	1.0.02	—— ——					ORLD
	PB_3	PB_4					OUT 100.04

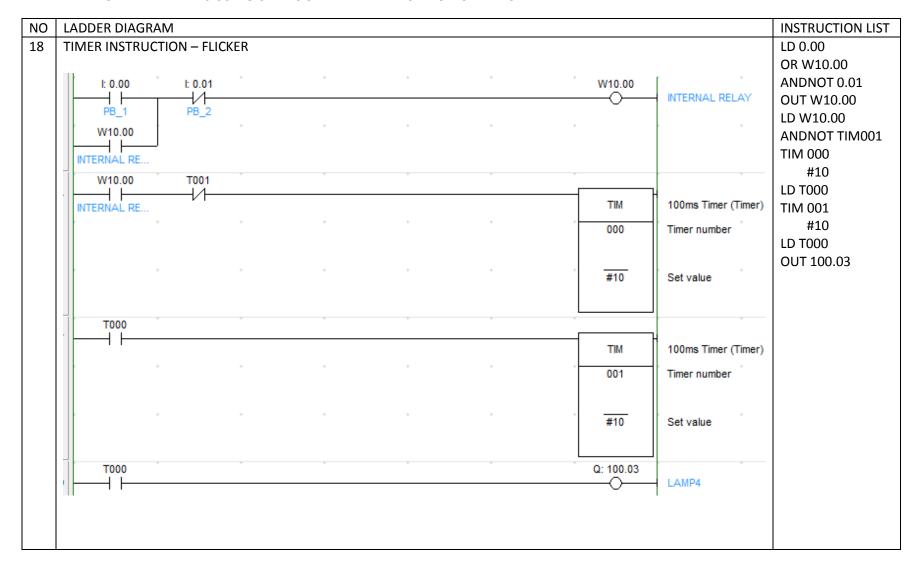
NO	LADDER DIAGRAM	INSTRUCTION LIST
8	LATCHING (SELF HOLDING)	LD 0.00 OR 100.02
	I: 0.00	ANDNOT 0.01 OUT 100.02
9	INTERLOCK	LD 0.00
	I: 0.00	OR 100.00 ANDNOT 0.02 ANDNOT 100.01 OUT 100.00 LD 0.01 OR 100.01
	I: 0.01	ANDNOT 0.02 ANDNOT 100.00 OUT 100.01
10	WORK BIT (INTERNAL RELAY)	LD 0.01 OR W10.00
	I: 0.01 I: 0.02 W10.00 INTERNAL RE W10.00 INTERNAL RE W10.00 INTERNAL RE W10.00 W1	ANDNOT 0.02 OUT W10.00 LD W10.00 OUT 100.00 LDNOT W10.00
	W10.00 Q: 100.00 LAMP_1	OUT 100.01
	W10.00 Q: 100.01 LAMP_2	

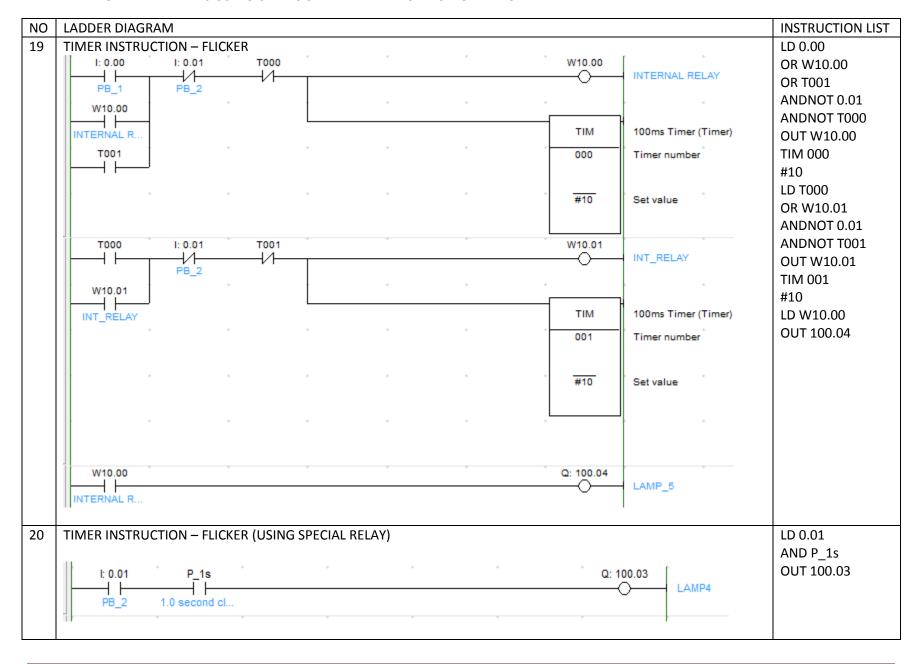


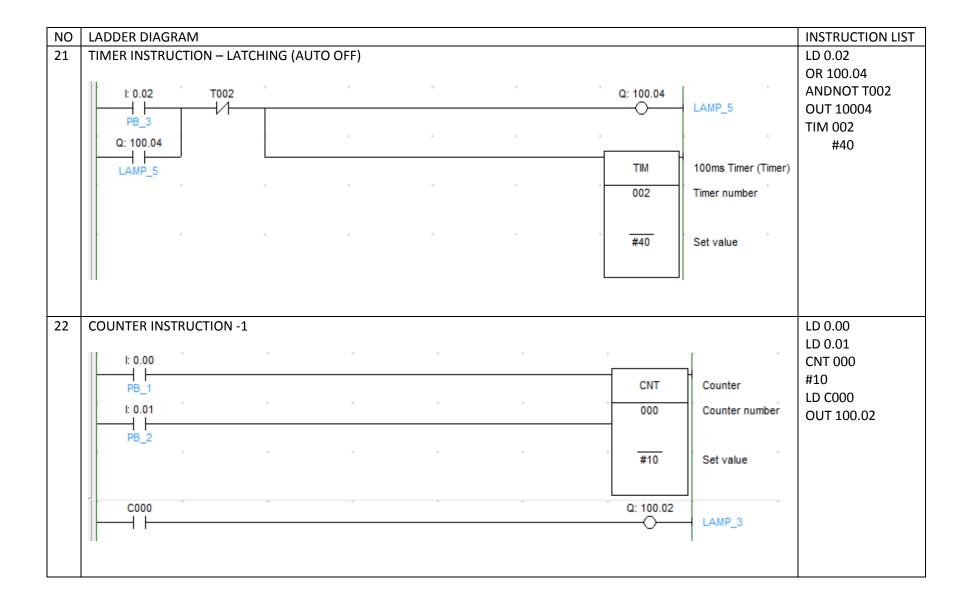


NO										
15	TIMER INSTRUCTION – DELAY TO ON									
								r +	TIM 00	
	1: 0.00	+	*	Ť	•	Ÿ		Ţ,	#50	
	PB_1						TIM	100ms Timer (Timer) [BCD Type]	LD T000	
									OUT 100.02	
							000	Timer number		
				+			#50	Set value		
							,,,,,,	oct value		
	T000	*	*	*	*	*	Q: 100.02	,		
							\longrightarrow	LAMP_3		
	II									
16	TIMER INSTRI	ICTION - DE	I AV TO O						LD 0.01	
10	I IIIVIEK IIVSTKI	JCHON - DE	LATIOU	·r					OR W10.01	
	I: 0.01	T001		*			· W1	0.01	OUT TRO	
		 1/1						INT_RELAY	ANDNOT T001	
	PB_2								OUT W10.01	
	W10.01	I: 0.01		+	•	*	·		LD TRO	
	INTERESTANCE	/_						TIM 100ms Timer (Timer)		
	INT_RELAY	PB_2		*					TIM 001	
							0	01 Timer number	#50	
									LD W10.01	
				+				Set value	OUT 100.02	
								Set value	001 100.02	
	W10.01	*		*			Q: 1	00.02		
								LAMP_3		
	INT_RELAY									
									1	

10	LADDER DI								INSTRUCTION LIST
7	TIMER INST		LD 0.00						
									TIM 000
	I: 0.00	*	*	*	•	*	*	_[#50
	PB_1						TIM	100ms Timer (Timer)	LD 100.03
			+						ANDNOT 0.00
							000	Timer number	TIM 001
									#70
	-	+	+			+	#50	Set value	LD T000
							,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	oct value	LD T001
]	KEEP 100.03
	Q: 100.0			*	*		*		
	LAMP4	PB_	1				TIM	100ms Timer (Timer)	
		+	+			*	001	Timer number	
		*	*			*	#70	Set value	
]	
	T000								
							KEEP(011)	Keep	
	T001		+				Q: 100.03	LAMP4	
							Q: 100.03	Bit	
	П							-1 .	







NO										
23	COUNTER INSTRUCTION -2									
	II.								AND P_1s	
	1: 0.00	P_1s	·	Ť	·	_		Ť	LD 0.01	
	PB_1	1.0 second cl					CNT	Counter	CNT 000	
	1: 0.01			+			000	Counter number	#10	
							000	Counter number	LD C000	
	PB_2								OUT 100.02	
				+	*	*	#10	Set value		
	C000						Q: 100.02	LAMP_3		
								EXIII3		
24	COUNTER INS	TRUCTION – EXT	ENDED COUNT	ER					LD 0.00	
									AND P_0_1s	
	1: 0.00	P_0_1s		+	+				LD 0.01	
	PB_1	0.1 second cl					CNT	Counter	OR C010	
	1: 0.01	+ second ci		+			010	Counter number	OR C011	
	1.0.01						010	Counter number	CNT 010	
	PB_2								#100	
	C010	1	•	*	*	*	#100	Set value	LD C010	
		7							LD 0.01	
	C011	+		+					CNT 011	
		_							#50	
									LD C011	
	C010								OUT 100.01	
							CNT	Counter		
	l: 0.01			+		+	011	Counter number		
	PB_2									
					+		#50	Caturatus		
							#50	Set value		
	C011			*			Q: 100.01			
							$\overline{}$	LAMP_2		

