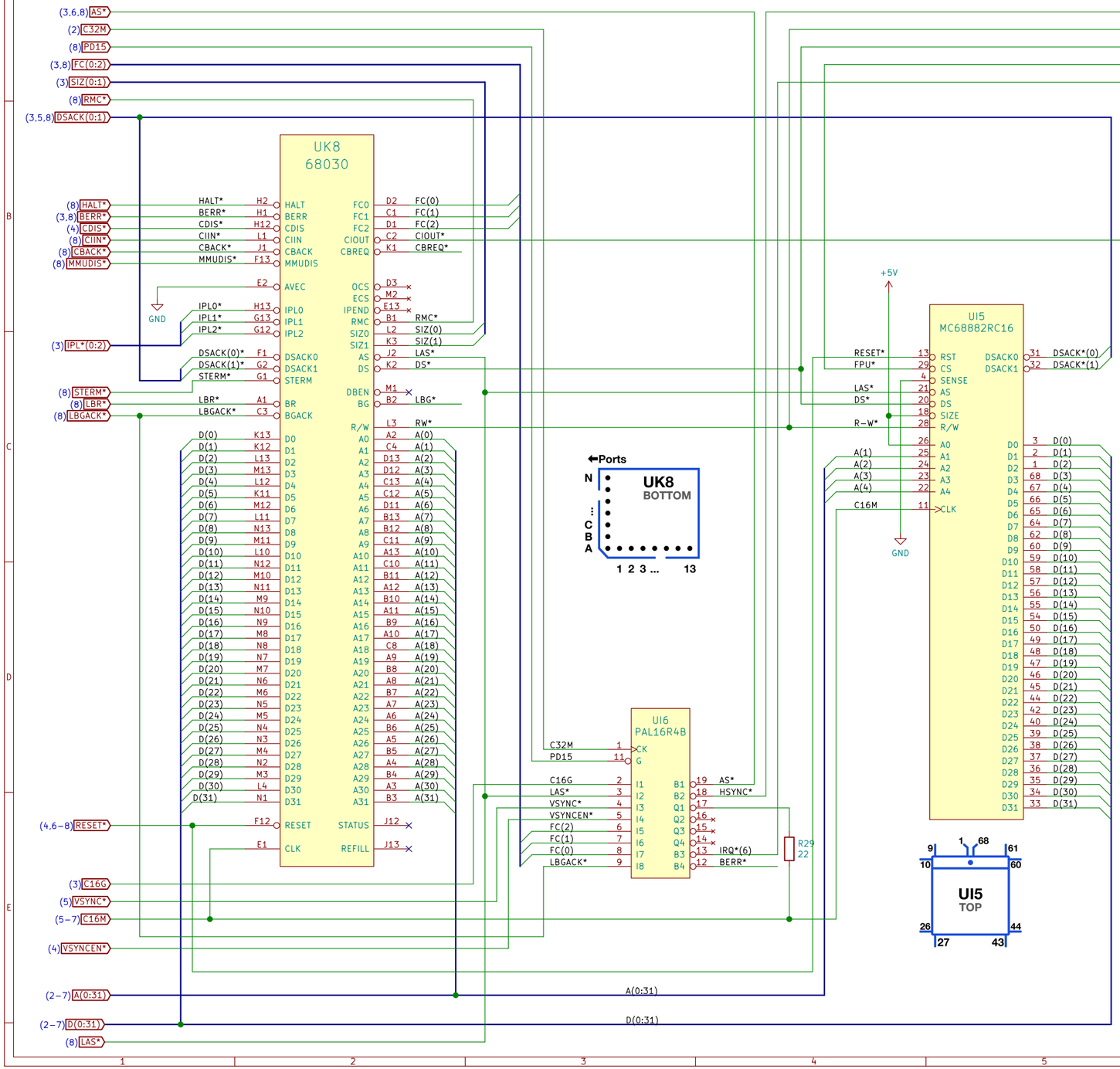
NOTE: Unless otherwise specified

1. All resistance values are in ohms, +/- 5%, 1/8 watt.
2. All capacitance values are in microfarads.
3. ( ) denotes sheet to sheet references.
4. Cold & hot ground are connected together at two locations, near the ADB connectors and at the sound circuit.



Recreation of Apple engineering drawing 050-0253-01	
Sheet: /CPU&FPU/	
File: CPU+FPU.sch	
Title: CPU (68030) & FPU (68881/68882)	
Size: USLedger	Date: 2020-05-18
KiCad E.D.A. kicad 5.1.4+dfsg1-1bpo10+1	Id: 2/10
	Rev: 1

(1,3-7) D(0:31)

+5V

ROMSIMM1

ROM*

ROM

GND

(1,3-7) A(0:31)

D4 BAS40-06

D5 BAS40-06

D6 BAS40-06

D7 BAS40-06

D8 BAS40-06

D9 BAS40-06

D10 BAS40-06

D11 BAS40-06

D12 BAS40-06

D13 BAS40-06

D14 BAS40-06

D15 BAS40-06

D16 BAS40-06

D17 BAS40-06

D18 BAS40-06

D19 BAS40-06

UJ2 74F258

UI2 74F258

UI3 74F258

UJ3 74F258

UJ4 74F258

UI4 74F258

RCMUX

RP4 22

RP5 22

RP6 22

RAAF(0:11) (3)

RABF(0:11) (3)

RCMUX* (9)

CASLL (3,9)

RASB (3,9)

WR* (6)

RASA (3,9)

RCMUX (3)

L15 Ferrite Bead

UH7 PAL16L8D

R-W* (1,3-5,7,8)

WR (6)

RAMRWA (3)

RAMRWB (3)

C32M (3)

R28 47

L14 Ferrite Bead

L13 Ferrite Bead

L12 Ferrite Bead

Y2 Oscilator 31.3344MHz

C49 0.01

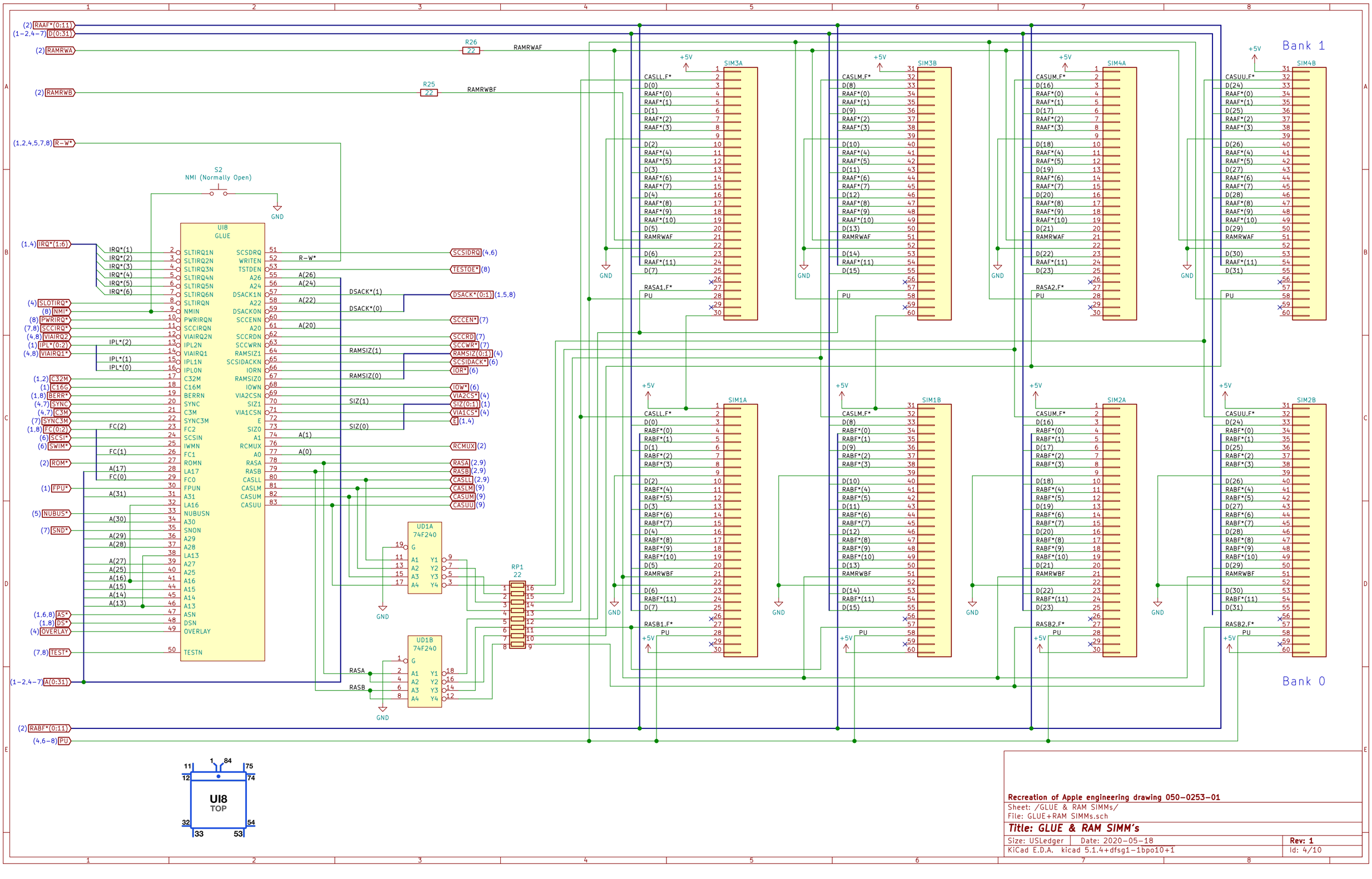
Recreation of Apple engineering drawing 050-0253-01

Sheet: /ROM & RAM Muxes/

File: ROM+RAM Muxes.sch

Title: ROM & RAM Address Muxes

Size: USLedger	Date: 2020-05-25	**Rev: 3**
KiCad E.D.A. kicad 5.1.4+dfsg1-1bpo10+1		Id: 3/10


(2) RAAF*(0:11)
(1-2,4-7) D(0:31)
(2) RAMRWA
(2) RAMRWB
R26
22
RAMRWAF
R25
22
RAMRWBF
(1,2,4,5,7,8) R-W*
S2
NMI (Normally Open)
GND
UI8
GLUE
(1,4) IRQ*(1:6)
IRQ*(1)
IRQ*(2)
IRQ*(3)
IRQ*(4)
IRQ*(5)
IRQ*(6)
(4) SLOTIRQ*
(8) NMI*
(8) PWRIRQ*
(7,8) SCCIRQ*
(4,8) VIAIRQ2
(1) IPL*(0:2)
(4,8) VIAIRQ1*
IPL*(2)
IPL*(1)
IPL*(0)
(1,2) C32M
(1) C16G
(1,8) BERR*
(4,7) SYNC
(4,7) C3M
(7) SYNC3M
(1,8) FC(0:2)
(6) SCSI*
(6) SWIM*
FC(2)
FC(1)
FC(0)
(2) ROM*
A(17)
(1) FPU*
A(31)
(5) NUBUS*
A(30)
(7) SND*
A(29)
A(28)
A(27)
A(25)
A(16)
A(15)
A(14)
A(13)
(1,6,8) AS*
(1,8) DS*
(4) OVERLAY
(7,8) TEST*
(1-2,4-7) A(0:31)
SLTIRQ1N
SLTIRQ2N
SLTIRQ3N
SLTIRQ4N
SLTIRQ5N
SLTIRQ6N
SLTIRQN
NMIN
PWRIRQN
SCCIRQN
VIAIRQ2N
IPL2N
VIAIRQ1
IPL1N
IPL0N
C32M
C16M
BERRN
SYNC
C3M
SYNC3M
FC2
SCSIN
IWMN
FC1
ROMN
LA17
FC0
FPUN
A31
LA16
NUBUSN
A30
SNON
A29
A28
LA13
A27
A25
A16
A15
A14
A13
ASN
DSN
OVERLAY
TESTN
SCSDRQ
WRITEN
TSTDEN
A26
A24
DSACK1N
A22
DSACK0N
SCCENN
A20
SCCRDN
SCCWRN
RAMSIZ1
SCSIDACKN
IORN
RAMSIZ0
IOWN
VIA2CSN
SIZ1
VIA1CSN
E
SIZ0
A1
RCMUX
A0
RASA
RASB
CASLL
CASLM
CASUM
CASUU
R-W*
A(26)
A(24)
A(22)
A(20)
RAMSIZ(1)
RAMSIZ(0)
SIZ(1)
SIZ(0)
A(1)
A(0)
SCSIDRQ (4,6)
TESTOE* (8)
DSACK*(1)
DSACK*(0)
DSACK*(0:1) (1,5,8)
SCCEN* (7)
SCCRD (7)
SCCWR* (7)
RAMSIZ(0:1) (4)
SCSIDACK* (6)
IOR* (6)
IOW* (6)
VIA2CS* (4)
SIZ(0:1) (1)
VIA1CS* (4)
E (1,4)
RCMUX (2)
RASA (2,9)
RASB (2,9)
CASLL (2,9)
CASLM (9)
CASUM (9)
CASUU (9)
UD1A
74F240
UD1B
74F240
RP1
22
RASA
RASB
SIM3A
SIM3B
SIM4A
SIM4B
SIM1A
SIM1B
SIM2A
SIM2B
+5V
CASLL.F*
CASLM.F*
CASUM.F*
CASUU.F*
RASA1.F*
RASB1.F*
RASA2.F*
RASB2.F*
PU
Bank 1
Bank 0
UI8
TOP
Recreation of Apple engineering drawing 050-0253-01
Sheet: /GLUE & RAM SIMMs/
File: GLUE+RAM SIMMs.sch
Title: GLUE & RAM SIMM's
Size: USLedger
Date: 2020-05-18
Rev: 1
KiCad E.D.A. kicad 5.1.4+dfsg1-1bpo10+1
Id: 4/10

VIA1

UK12
65C22

(1-3,5-7) A(0:31)

A(9) 42 R50 — PA0 2 VIPA0
A(10) 41 R51 — PA1 3 VIPA1
A(11) 40 R52 — PA2 4 VIPA2
A(12) 39 R53 — PA3 5 SYNC
PA4 6 OVERLAY
D(24) 36 D0 — PA5 7 HDSEL
D(25) 35 D1 — PA6 8 ALTVID
D(26) 34 D2 — PA7 9 SCCWREQ*
D(27) 32 D3
D(28) 31 D4 — PB0 10 RTC-O
D(29) 30 D5 — PB1 12 RTC-CLK
D(30) 29 D6 — PB2 13 RTC-CS*
D(31) 28 D7 — PB3 14 ADB-INT*
PB4 15 ADB-ST0
(1-3,5,7,8) R-W* 24 R/W — PB5 16 ADB-ST1
(3) E 27 PH0 — PB6 17 VSYNCEN*
(3,6-8) PU 26 CS1 — PB7 18 VIPB7
(3) VIA1CS* 25 CS0
(1,6-8) RESET* 37 RESET — IRQ 23 VIAIRQ1*
(9) VBLK* 44 CA1
(9) ADB-SCLK 43 CA2
(9) ADB-DIO 19 CB1
20 CB2

VIPA0 (8,9)
VIPA1 (8,9)
VIPA2 (8,9)
SYNC (3)
OVERLAY (3)
HDSEL (6)
ALTVID (5,9)
SCCREQ* (7)
ADB-INT* (9)
ADB-ST0 (9)
ADB-ST1 (9)
VSYNCEN* (1)
VIPB7 (9)
VIAIRQ1* (3)

UK11
UK12
TOP
1 6 7 17 18 28 29 39 40 44

RTC-1HZ

Y1
32.768KHz
C44 33pF
C45 10pF

UK4
RTC
XTL0 2
XTL1 3
O 6
SK 7
CS 5
GND 4
VCC 8 +5V-RTC
1HZ 1 RTC-1HZ

RTC-O
RTC-CLK
RTC-CS*

GND

+5V
D2 1N914
C50 0.1
R27 4.7K
D1 1N914
BT1
3.0V, 1/2AA
GND

J9
4 3 +5F-ADB
2 1 ADBF
5 6 7 8 9 10 11 12 13
GNDPWR

UL11
ADB
DIO 14 P0 11
CLK 26 P1 12
SCLK 13 P2 17
ST0 27 P6 18
ST1 28 P7 19
INT 16 FDBI 3 ADBO
RST 4 FDBD 2
FDBO 1 ADBO*

ADB-DIO
C3M
ADB-SCLK
ADB-ST0
ADB-ST1
ADB-INT*
RESET*

(3) C3M

+5V
F1 1AMP
R31 470
R33 27
R32 4.7K
Q3 2N3904

L1
LNET
ADB0 1 2 3 4
8 ADBF
7 +5F-ADB
6 GNDF-ADB
5

J10
4 3 +5F-ADB
2 1 ADBF
5 6 7 8 9 10 11 12 13
GNDPWR

C73 33pF
C72 0.01

ADBO* (9)

VIA2

UK11
65C22

A(9) 42 R50 — PA0 2 IRQ*(1)
A(10) 41 R51 — PA1 3 IRQ*(2)
A(11) 40 R52 — PA2 4 IRQ*(3)
A(12) 39 R53 — PA3 5 IRQ*(4)
PA4 6 IRQ*(5)
D(24) 36 D0 — PA5 7 IRQ*(6)
D(25) 35 D1 — PA6 8 RAMSIZ(0)
D(26) 34 D2 — PA7 9 RAMSIZ(1)
D(27) 32 D3
D(28) 31 D4 — PB0 10 CDIS*
D(29) 30 D5 — PB1 12 BUSLOCK*
D(30) 29 D6 — PB2 13 PWROFF
D(31) 28 D7 — PB3 14 V2PB3
PB4 15 TM1A*
(1-3,5-7) D(0:31) R-W* 24 R/W — PB5 16 TM0A*
E 27 PH0 — PB6 17 SNDEXT*
PU 26 CS1 — PB7 18 VBLK*
(3) VIA2CS* 25 CS0
RESET* 37 RESET — IRQ 23 VIAIRQ2*
44 CA1
(3) SLOTIRQ* 43 CA2
(3) SCSIDRQ 19 CB1
(7) SNTINT* 20 CB2
(6) SCSIIRQ

GND

IRQ*(1:6) (1,3)
RAMSIZ(0:1) (3)
CDIS* (1)
BUSLOCK* (1)
PWROFF (1)
TM1A* (1)
TM0A (1)
VIAIRQ2* (3)

Recreation of Apple engineering drawing 050-0253-01

Sheet: /VIA1, VIA2, RTC & ADB/
File: VIA1+VIA2+RTC+ADB.sch

Title: VIA1 and VIA2 (65C22), RTC, Apple Desktop Bus

Size: USLedger	Date: 2020-05-18	**Rev: 1**
KiCad E.D.A. kicad (5.1.5-0-10_14)		Id: 5/10

(8) VIDOUT
(1,8) VSYNC*
(1,6,7) C16M
(8) PD7
(8) PD1
(8,9) PU2
(1–4,6,7) A(0:31)
(8) PD11
(1–4,6,7) D(0:31)
(4,9) ALTVID

HSYNC* (1,8)
PDB (8)
DSACK*(0) (1,3,8)
NUBUS* (1,3)
VADR(6) (9)
RW* (1–4,7,8)

Binary Counter
Clock Generator
UG8A
LS393
UG7
PAL16R8B
HCTRRST
C2M
SC
SREGLD
HSYNC
TWOLINE
VIDTIME

Binary Counter
UF8A
LS393
PAL (Beginning)
UG6
PAL16R8B
VADR(0)
VADR(1)
VADR(2)
VADR(3)
VADR(4)
VADR(5)
VADR(7)
SERVID
LCTRRST
VSYNC*
VIDOUT
VIDTIME

UE7
PAL16R8B
PAL (Middle)
C16M
PD8
HSYNC*
SLTE–F
A(24)
NUBUS*
VIDREQ
VR*
VID54
VID53
VID52
VID51
VID50
VID–V

PAL (End)
UE6
PAL16L8
+5V
GND
VIDROM*
VC*
DSACK*(0)
DOE*
VIDMUX*
VIDW*

R17 22
R16 22
R19 22
R23 22
R18 22

A(24)–GLU–56
A(16)–ROM–39
UJ6A
74LS30
A(31)
A(30)
A(29)
A(28)
A(27)
A(26)
A(25)
PU2
SLTE–F
VID–V

SERVID
+5V
PU2
VID(0)
VID(1)
VID(2)
VID(3)
VID(4)
VID(5)
VID(6)
VID(7)
SREGLD
C16M
Shift Register
UE8
74LS166
GND
VID(0:7)

VIDMUX*
Video Multiplexers
GND
UD8
74F253
UB8
74F253
UC8
74F253
UA8
74F253
VADR(1)
VADR(2)
VADR(3)
VADR(4)
VADR(5)
VADR(6)
VADR(7)
VADR(0)
A(0)
A(8)
A(1)
A(9)
A(2)
A(10)
A(3)
A(11)
A(4)
A(12)
A(5)
A(13)
A(6)
A(14)
A(7)
A(15)
TWOLINE
RA(0)
RA(1)
RA(2)
RA(3)
RA(4)
RA(5)
RA(6)
RA(7)
+5V

Video ROM
UK?
2764
A(0)
A(1)
A(2)
A(3)
A(4)
A(5)
A(6)
A(7)
A(8)
A(9)
A(10)
A(11)
A(12)
D(24)
D(25)
D(26)
D(27)
D(28)
D(29)
D(30)
D(31)
+5V
GND

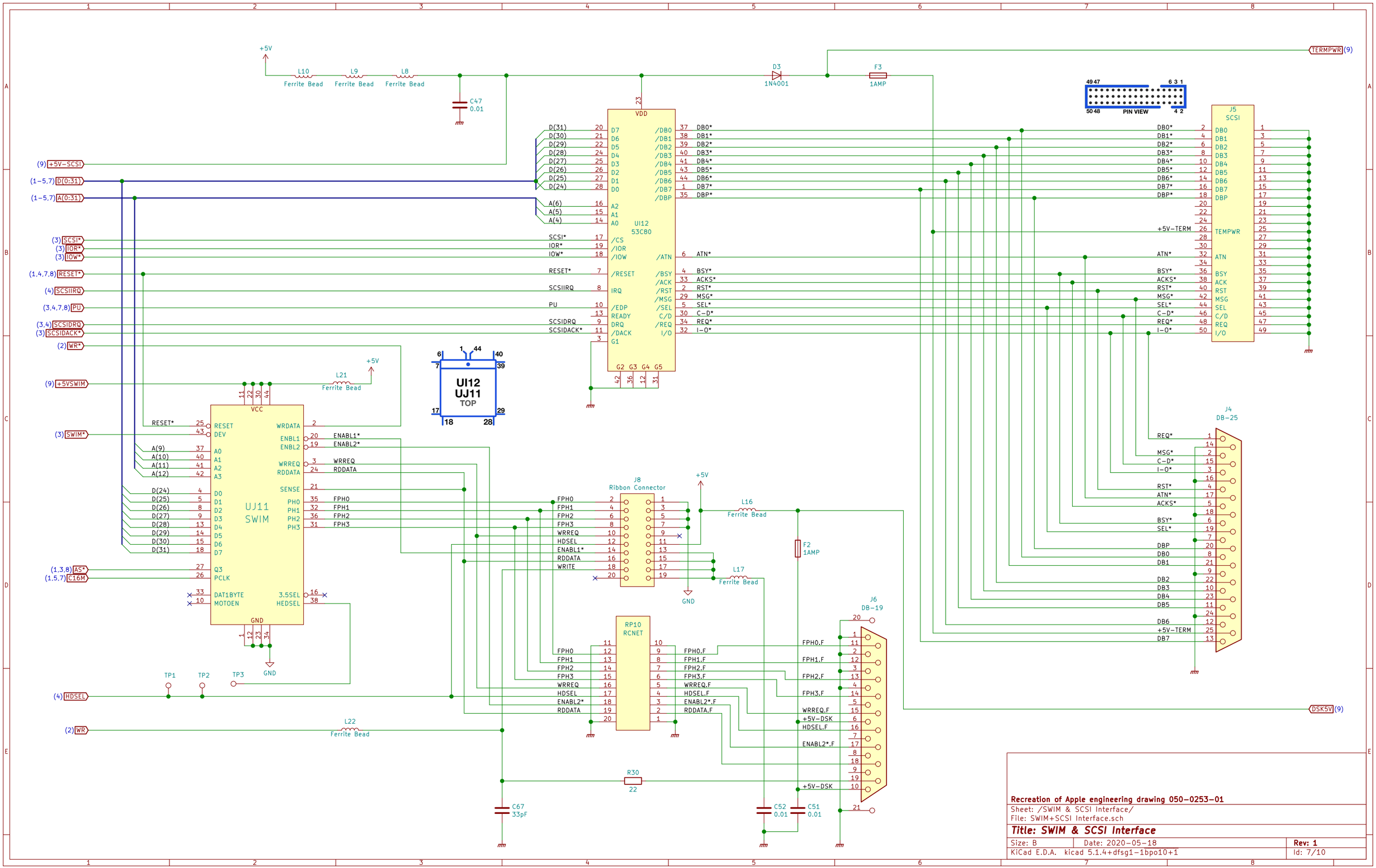
Video RAM
UC6
D41264C–12
UC7
D41264C–12
PD11
SC.F
DT–OE*
VIDRAS*
VIDCAS*
VIDW*
RA(0)
RA(1)
RA(2)
RA(3)
RA(4)
RA(5)
RA(6)
RA(7)
VID(4)
VID(5)
VID(6)
VID(7)
VID(0)
VID(1)
VID(2)
VID(3)
D(28)
D(29)
D(30)
D(31)
D(24)
D(25)
D(26)
D(27)
+5V
GND

Recreation of Apple engineering drawing 050–0253–01

Sheet: /Video Interface/
File: Video Interface.sch

Title: Video Interface

Size: B	Date: 2020–05–21	**Rev: 2**
KiCad E.D.A. kicad (5.1.5–0–10_14)		Id: 6/10


+5V
L10
Ferrite Bead
L9
Ferrite Bead
L8
Ferrite Bead
C47
0.01
D3
1N4001
F3
1AMP
TERMPWR (9)
49 47
6 3 1
50 48
PIN VIEW
4 2
J5
SCSI
(9) +5V-SCSI
(1-5,7) D(0:31)
(1-5,7) A(0:31)
(3) SCSI*
(3) IOR*
(3) IOW*
(1,4,7,8) RESET*
(4) SCSIIRQ
(3,4,7,8) PU
(3,4) SCSIDRQ
(3) SCSIDACK*
(2) WR*
VDD
UI12
53C80
/CS
/IOR
/IOW
/RESET
IRQ
/EDP
READY
DRQ
/DACK
G1
G2 G3 G4 G5
/DB0
/DB1
/DB2
/DB3
/DB4
/DB5
/DB6
/DB7
/DBP
/ATN
/BSY
/ACK
/RST
/MSG
/SEL
C/D
/REQ
I/O
DB0*
DB1*
DB2*
DB3*
DB4*
DB5*
DB6*
DB7*
DBP*
ATN*
BSY*
ACKS*
RST*
MSG*
SEL*
C-D*
REQ*
I-O*
+5V-TERM
TEMPWR
(9) +5VSWIM
L21
Ferrite Bead
VCC
UJ11
SWIM
RESET
DEV
A0
A1
A2
A3
D0
D1
D2
D3
D4
D5
D6
D7
Q3
PCLK
DAT1BYTE
MOTOEN
WRDATA
ENBL1
ENBL2
WRREQ
RDDATA
SENSE
PH0
PH1
PH2
PH3
3.5SEL
HEDSEL
(3) SWIM*
(1,3,8) AS*
(1,5,7) C16M
GND
TP1
TP2
TP3
(4) HDSEL
(2) WR
L22
Ferrite Bead
UI12
UJ11
TOP
J8
Ribbon Connector
L16
Ferrite Bead
L17
Ferrite Bead
F2
1AMP
RP10
RCNET
J6
DB-19
J4
DB-25
DSK5V (9)
R30
22
C67
33pF
C52
0.01
C51
0.01
Recreation of Apple engineering drawing 050-0253-01
Sheet: /SWIM & SCSI Interface/
File: SWIM+SCSI Interface.sch
Title: SWIM & SCSI Interface
Size: B
Date: 2020-05-18
Rev: 1
KiCad E.D.A. kicad 5.1.4+dfsg1-1bpo10+1
Id: 7/10

(8) TXDAT–
(8) TXDAT+
(9) TXDA*
(9) RTSA*
(9) RTSB*
(9) TXDB
(3) SYNC
(9) DCDA*
(9) CTSA*
(3) SCCIRQ*
(3,4,6,8) PU
(1–6) A(0:31)
(1–6) D(0:31)
(3) SCCEN*
(3) SCCRD*
(3) SCCWR*
(3,9) SYNC3M
(3) C3M
(4) SCCWREQ*
(9) DTRA*
(9) RXDB
(9) DTRB*
(9) DCDB*
(9) CTSB*
(8,9) RXDA
(1,4,6,8) RESET*
(9) F-CLOCK
(4) SNDINT*
(3) SND*
(1–5,8) R-W*
(1,5,6) C16M
RESET*
(8) RXDAT+
(8) RXDAT–

UG12
8530
IEI
IEO
INTACK
IRQ6
A/B
D/C
D0
D1
D2
D3
D4
D5
D6
D7
TRXCA
CTSA
DCDA
DTRA
RTSA
TXDA
RXDA
WREQA
CS
RD
WR
SYNCA
TRXCS
CTSB
DCDB
DTRB
RTSB
TXDB
RXDB
WREDB
SYNCB
RTXCA
RTXCB
PCX
A(1)
A(2)
D(24)
D(25)
D(26)
D(27)
D(28)
D(29)
D(30)
D(31)
PU
SCCIRQ*
CTSA*
DCDA*
DTRA*
RTSA*
TXDA
RXDA
SCCWREQ*
CTSB*
DCDB*
DTRB*
RTSB*
TXDB
RXDB
SCCWREQ*

UD12
26LS30
IA
IB/GA
IC/GD
ID
SRA
SRB
SRC
SRD
MODE
GND
VEE
VCC
OA
OB
OC
OD
TXDA
RTSA*
RTSB*
TXDB
TXDA+
TXDA–
TXDB–
TXDB+
+5V
C28
0.01
–5V

UB12
75175
OA
OB
OC
OD
GAB
GCD
IA
IB
IC
ID
SYNC3M
CTSA*
DCDA*
SYNC
PU
GPIA
HSKIA

UC12
75175
RXDA
RXDB
CTSB*
DCDB*
RXDA+
RXDA–
RXDB+
RXDB–
HSKIB
GPIB

UE12
26LS30
DTRA*
DTRB*
HSKDA
HSKDB

R40 100
R41 100
R42 100
R43 100

RP2
RCNET
HSKDA
TXDA–
TXDA+
GPIA
RXDB+
RXDA–
HSKIA
HSXDAF
TXDAF–
TXDAF+
GPIAF
RXDAF+
RXDAF–
HSKIAF

RP3
RCNET
HSKDB
TXDB–
TXDB+
GPIB
RXDB+
RXDB–
HSKIB
HSKDBF
TXDBF–
TCDBF+
GPIBF
RXDBF+
RXDBF–
HSKIBF

J2
J3

UE10
DSC
+5A
D0
D1
D2
D3
D4
D5
D6
D7
A0
A1
A2
A3
A4
A5
A6
A7
A8
A9
A10
A11
CS
R/W
C16M
RESET
AGND
INT
PWMA
PWMB
ANALOGA
ANALOGB
SV0
SV1
SV2
FILTERCLK
ATEST
XTALIN
XTALOUT
REFA
REFB
SNDINT*
PWMA
PWMB
SV0
SV1
SV2
F-CLOCK
C33
33pF
Y3
16.9344MHz
C34
33pF

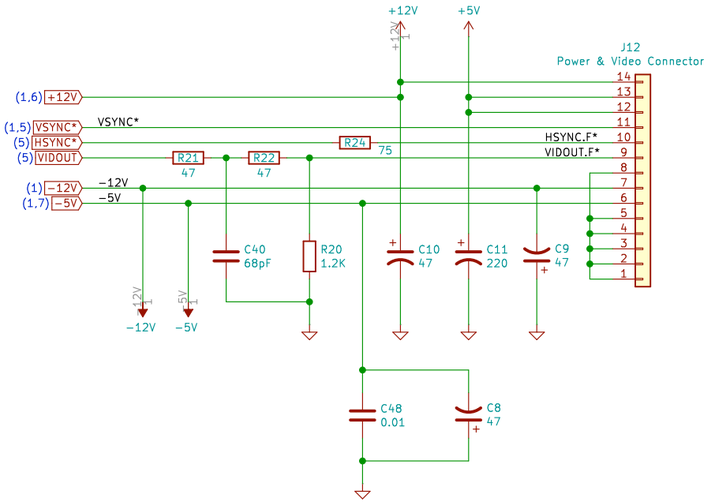
UE10
UG12
TOP

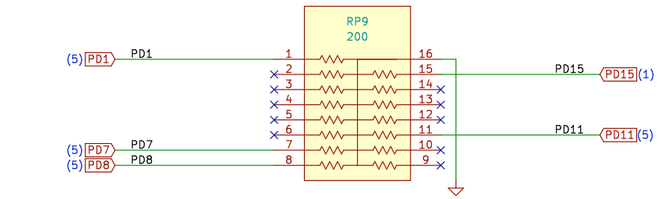
UB11
SONY SND
UB10
SONY SND
SV0
SV1
SV2
SV3
CMNGND
PWM
TEST
AMP12V
+12V
LPF12V
VSNS
SND
VREF
AGND
RESET
PWMA
PWMB
+12V-SND
+12VR
+12VL
330K
SND-R-DC
SND-R-A-D
SND-L-DC
SND-L-A-D
C4
47
R12
180
C6
1uF
C5
47
C3
47
R13
180
C24
0.1

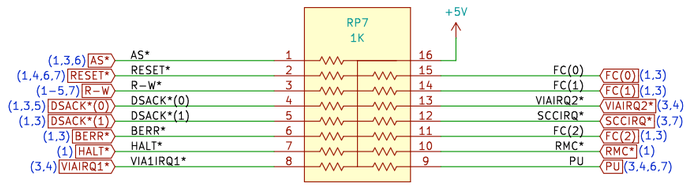
L2
CMC
R2
1K
R1
1K
R4
47
R3
47
SND-R-AC
AND-L-AC
Phone Jack
J1
C83
0.01
Q1
2N3904
Q2
2N3906
UA9
TL071
R34
1K
C84
0.01
R9
150
R10
27
C17
2200pF
R8 3.3K
SND-SPKR
J11
Speaker
SND-M1 (9)
SND-M2 (9)
TEST* (3)

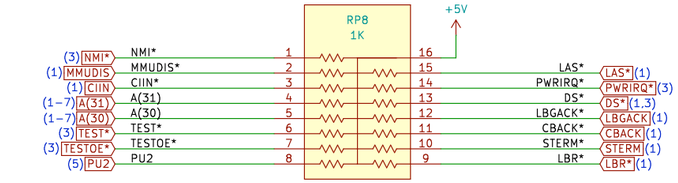
L11
Ferrite Bead
R15
10
R14
10
C2
470uF
C25
0.1
+12V
–5V

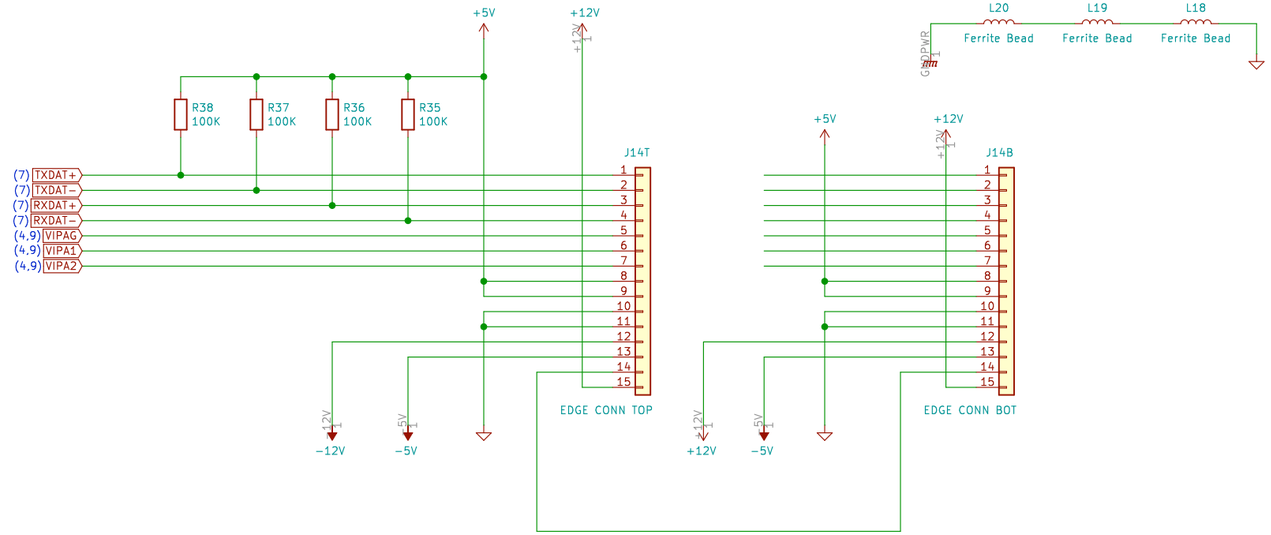
Recreation of Apple engineering drawing 050-0253-01
Sheet: /Serial Interface & Sound Interface/
File: Serial Interface + Sound Interface.sch
Title: SE/30 Serial Interface & Sound Interface
Size: USLedger
Date: 2020-05-18
Rev: 1
KiCad E.D.A. kicad 5.1.4+dfsg1-1bpo10+1
Id: 8/10

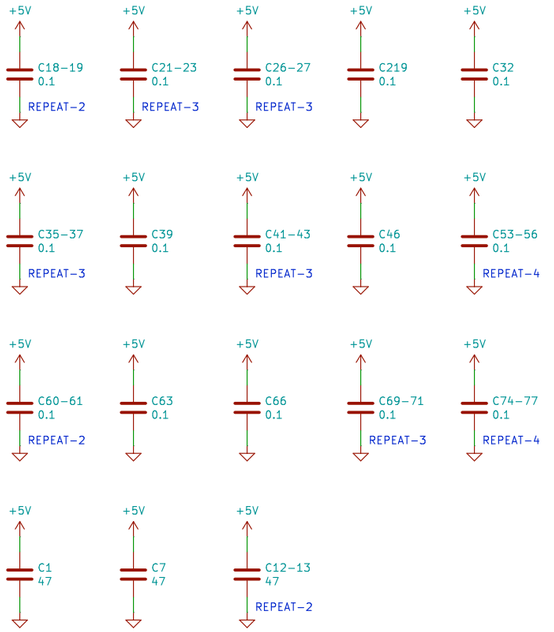












Recreation of Apple engineering drawing 050−0253−01

Sheet: /Power Connector, Pullups & Pulldowns/
File: Power Connector + Pullups + Pulldowns.sch

Title: Power Connector, Pullups & Pulldowns

Size: B	Date: 2020−05−18	**Rev: 1**
KiCad E.D.A. kicad (5.1.5−0−10_14)		Id: 9/10

This page is referenced in the other drawings,
but its contents are unknown. It was possibly a
“bed of nails” flying probe schematic.

Recreation of Apple engineering drawing 050-0253-01

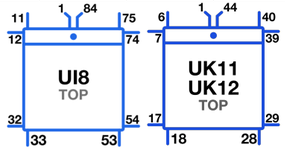
Sheet: /
File: SE:30 09 [Unknown].sch

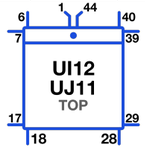
Title: Unknown

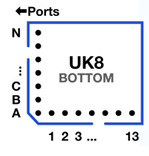
Size: USLedger	Date: 2020-05-18	**Rev: 1**
KiCad E.D.A. eeschema (5.1.5-0-10_14)		Id: 1/1

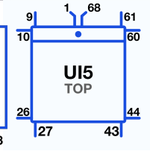
Macintosh SE/30 MLB Address Line Pin Matrix

Address	ROM	UJ2 RAM Mux	UI2 RAM Mux	UI3 RAM Mux	UJ3 RAM Mux	UJ4 RAM Mux	UI4 RAM Mux	UI8 GLUE	UK12 VIA1	UK11 VIA2	UK6 Vid ROM	UA8 Video Mux	UB8 Video Mux	UC8 Video Mux	UD8 Video Mux	UJ11 SWIM	UG12 Serial	UE10 Sound	UK8 CPU	UI5 FPU	J13 PDS Slot
A(0)								77			10				4			41	A2		116
A(1)								74			9				12		39	42	C4	25	76
A(2)	4	3,6									8			4			37	43	D13	24	36
A(3)	5	2,5									7			12				44	D12	23	115
A(4)	6	10,13									6		4					1	C13	22	75
A(5)	7	11,14									5		12					2	C12		35
A(6)	8		3,6								4	4						3	D11		114
A(7)	9		2,5								3	12						4	B13		34
A(8)	31		10,13								25				3			7	B12		113
A(9)	32		11,14						42	42	24				13	37		8	C11		73
A(10)	33			3,6					41	41	21			3		40		9	A13		33
A(11)	34			2,5					40	40	23			13		41		10	C10		112
A(12)	35			10,13					39	39	2		3			42			B11		72
A(13)	36			11,14				46					13						A12		32
A(14)	37				3,6			45											B10		111
A(15)	38				2,5			44											A11		31
A(16)	39				10,13			41											B9		110
A(17)	40				11,14			28											A10		70
A(18)	41					3,6													C8		30
A(19)	42					2,5													A9		109
A(20)	43					10,13		61											B8		69
A(21)	44					11,14													A8		29
A(22)	45						3,6	58											B7		108
A(23)							2,5												A7		28
A(24)							10,13	56											A6		107
A(25)							11,14	40											B6		67
A(26)								55											A5		27
A(27)								39											B5		106
A(28)								37											A4		66
A(29)								36											B4		26
A(30)								34											A3		105
A(31)								31											B3		25









Macintosh SE/30 MLB Data Line Pin Matrix

Address	ROM	D4 Diode	D5 Diode	D6 Diode	D7 Diode	D8 Diode	D9 Diode	D10 Diode	D11 Diode	D12 Diode	D13 Diode	D14 Diode	D15 Diode	D16 Diode	D17 Diode	D18 Diode	D19 Diode	S1A RAM	S1B RAM	S2A RAM	S2B RAM	S3A RAM	S3B RAM	S4A RAM	S4B RAM	UK12 VIA1	UK11 VIA2	UK6 VROM	UC6 VRAM	UC7 VRAM	UJ11 SWIM	UI12 SCSI	UG12 Serial	UE10 DSC	UK8 CPU	UI5 FPU	J13 PDS
D(0)	14	1																3				3													K13	3	13
D(1)	15			1														6				6													K12	2	93
D(2)	16					1												10				10													L13	1	14
D(3)	17							1										13				13													M13	68	54
D(4)	18									1								16				16													L12	67	94
D(5)	19											1						20				20													K11	66	15
D(6)	20													1				23				23													M12	65	55
D(7)	21															1		25				25													L11	64	95
D(8)	22	2																	3				3													N13	62	16
D(9)	23			2															6				6													M11	60	96
D(10)	24					2													10				10													L10	59	17
D(11)	25							2											13				13													N12	58	57
D(12)	26									2									16				16													M10	57	97
D(13)	27											2							20				20													N11	56	18
D(14)	28													2					23				23													M9	55	58
D(15)	29															2			25				25													N10	54	98
D(16)	47		1																	3				3												N9	50	19
D(17)	48				1															6				6												M8	49	99
D(18)	49						1													10				10												N8	48	20
D(19)	50								1											13				13												N7	47	60
D(20)	51										1									16				16												M7	46	100
D(21)	52												1							20				20												N6	45	21
D(22)	53														1					23				23												M6	44	61
D(23)	54																1			25				25												N5	42	101
D(24)	55		2																		3				3	36	36	11		5	4	28	1	22	M5	40	22
D(25)	56				2																6				6	35	35	12		6	5	27	2	21	N4	39	102
D(26)	57					2															10				10	34	34	13		19	8	26	44	20	N3	38	23
D(27)	58							2													13				13	32	32	15		20	9	25	3	19	M4	37	63
D(28)	59									2											16				16	31	31	16	5		13	24	43	18	N2	36	103
D(29)	60											2									20				20	30	30	17	6		14	22	4	17	M3	35	24
D(30)	61													2							23				23	29	29	18	19		15	21	42	16	L4	34	64
D(31)	62																2				25				25	28	28	19	20		18	20	5	15	N1	33	104