Advanced Operating Systems - Report

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Memory management and capabilities

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Processes, threads, and dispatch

TODO: Reason about why we don't handle unaligned addresses in paging. (We want the client to actively think about what they actually map.)

TODO: Explain the changes in the paging interface to the given interface TODO: Maybe document our difficulties that address to params has to be passed in ${\bf x0}$ register

Message prassing

3.1 channel setup init ¡-¿ child

- 1. in create_child_channel (spawn.c) register recv_setup_closure from init (used to receive initial ep from child)
- 2. in init.c (called before each thread):
 - create child endpoint i.e channel for this child (we are already in child)
 - register receive_init_closure (used to save ep received from init)
 - send child ep to init and wait until receive_init_closure get called
- 3. recv_setup_closure gets called:
 - save child ep in init channel struct (lmp_chan)
 - send message to child that channel is ready (in rpc_send_setup_closure
- 4. barrelfish_recv_init_closure gets called on the child side
 - child is now ready to communicate with init
 - on child side continue after recv init success
 - init is now dispatching events in recv_regular_closure

Page fault handling

Multicore

User-level message passing

Summary of overall system

Appendix A

User guide

Bibliography

[1] Timothy Roscoe and the ETH Barrelfish team. AOS Book. URL: https://www.systems.ethz.ch/sites/default/files/file/drupal6/courses/2020-spring/aos/book.pdf. (accessed: 14.03.2020).