

redpitaya

Red Pitaya d.o.o.
Velika pot 22
5250 Solkan
Slovenia

www.redpitaya.com

Electrical schematics for:

name: Red Pitaya
version: V1.0.1
variant: Release1

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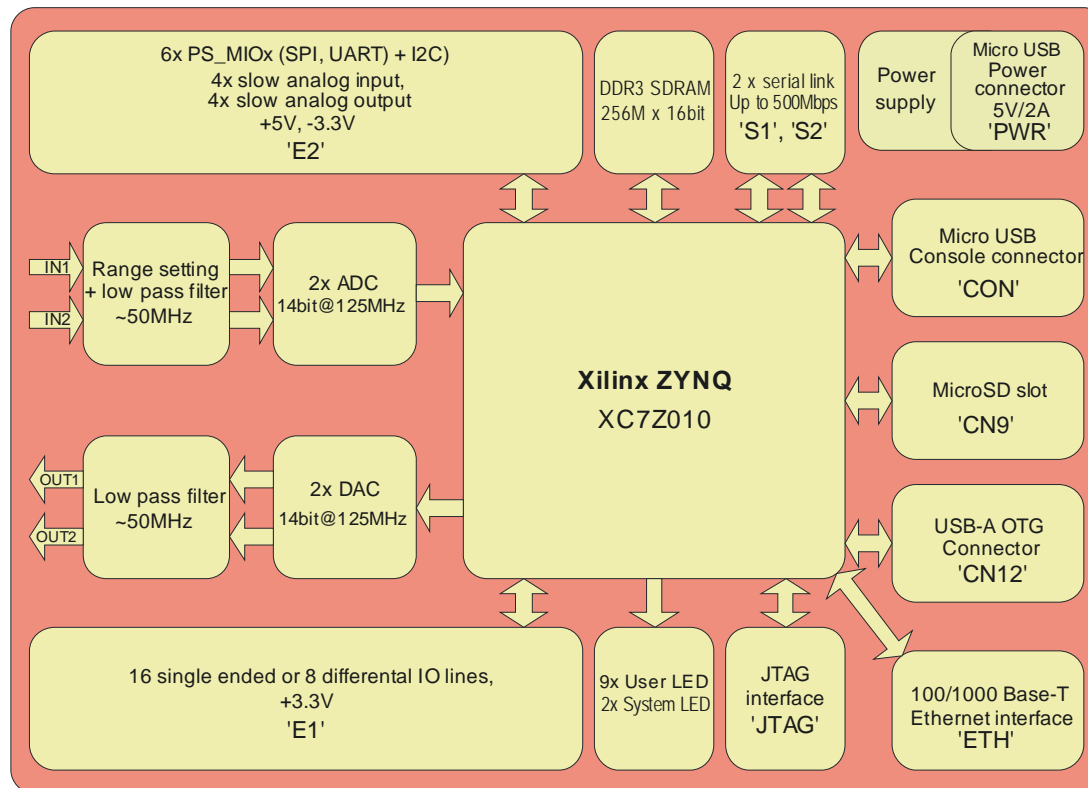
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Drawn By:	Z.L.	Modif. Date:	6.3.2014	SCHEMATIC
Approved By:	R.U.	Print Date:	6.3.2014	Size: A4 H

Red Pitaya



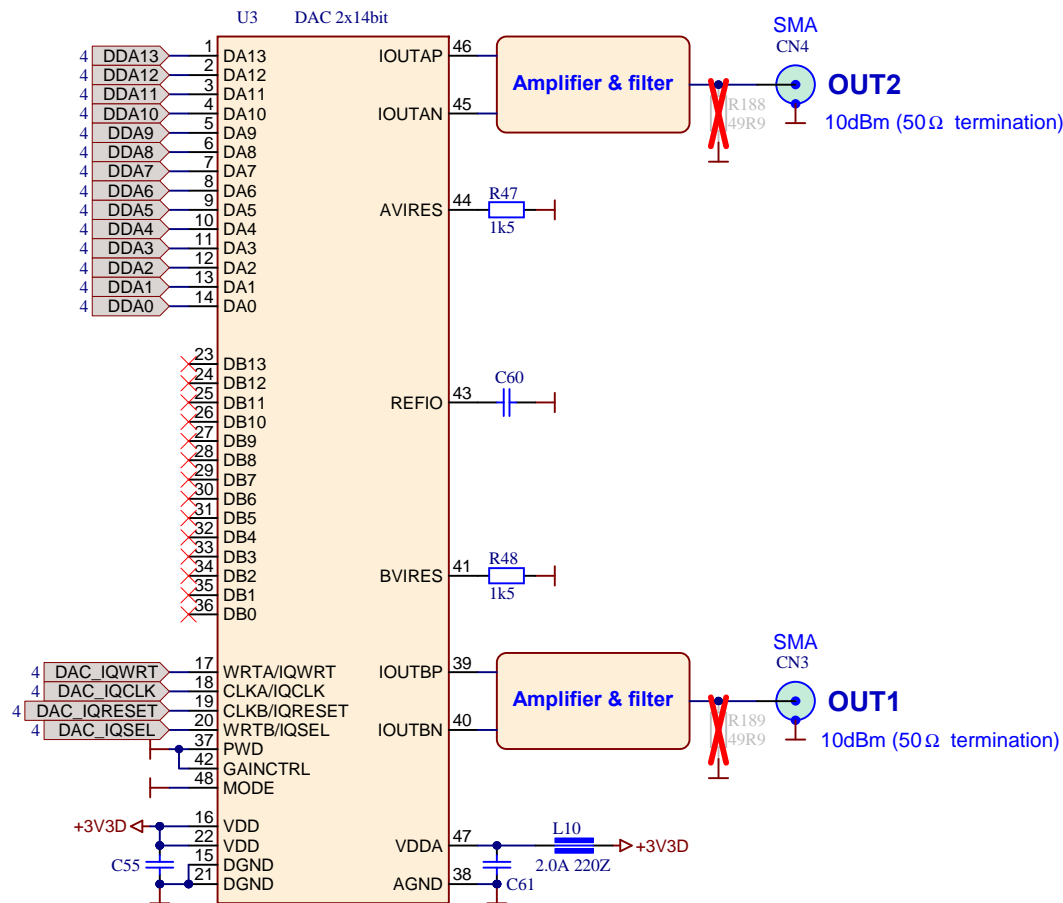
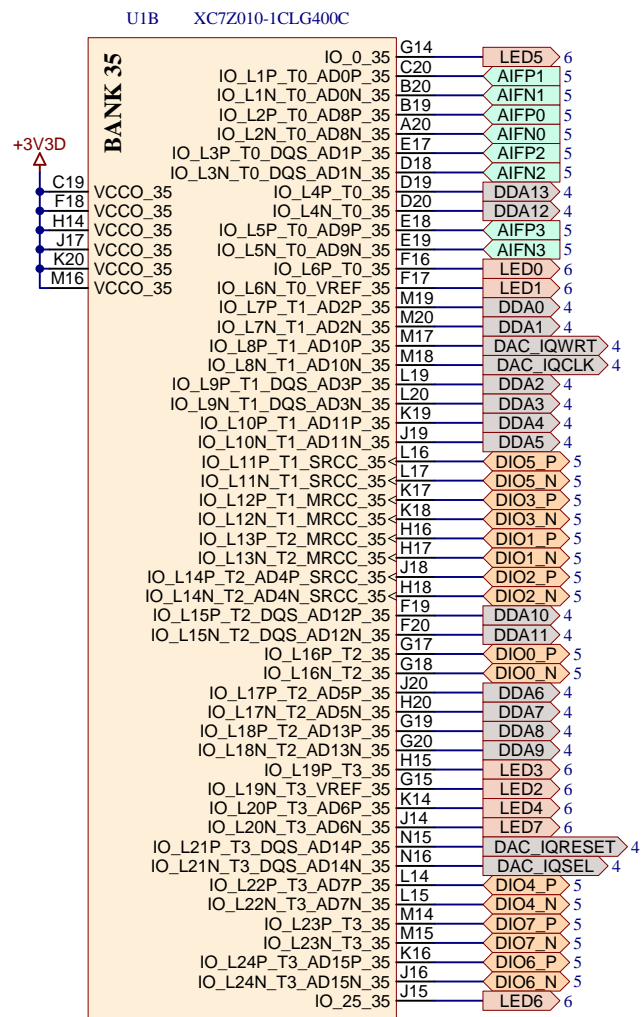
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Red Pitaya v1.0 block schematics





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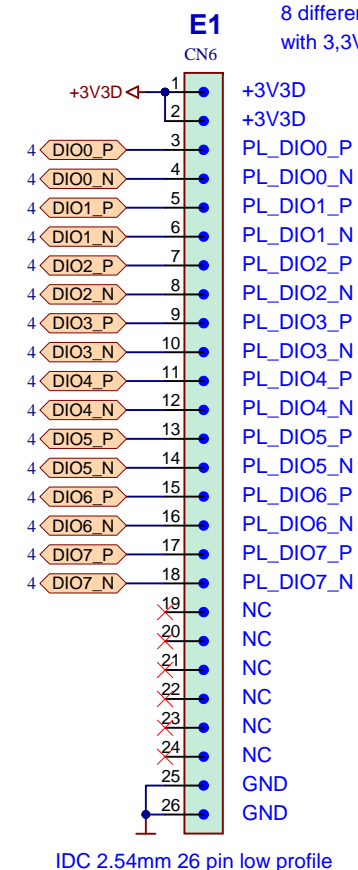
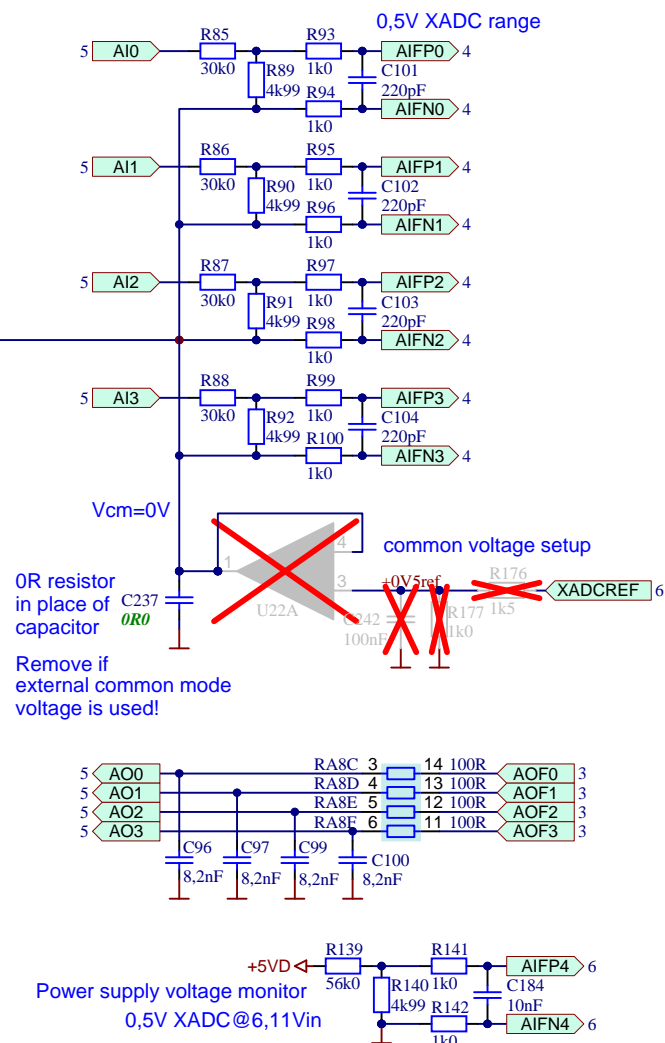
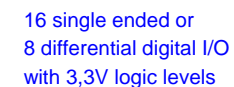
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Analog back-end and digital-analog converter, FPGA bank 35

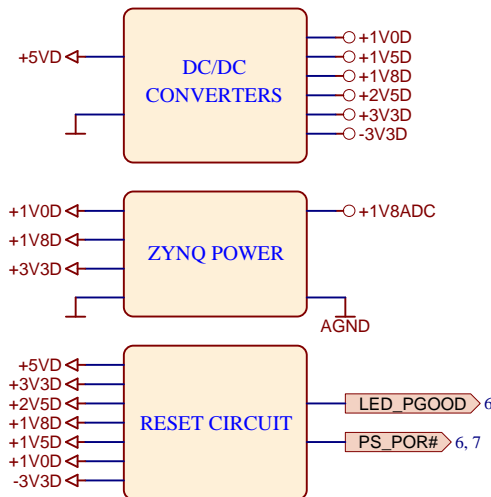
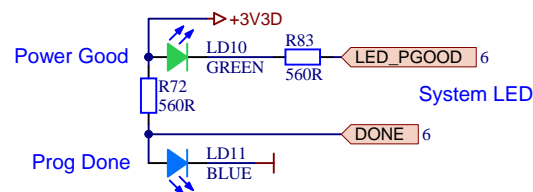
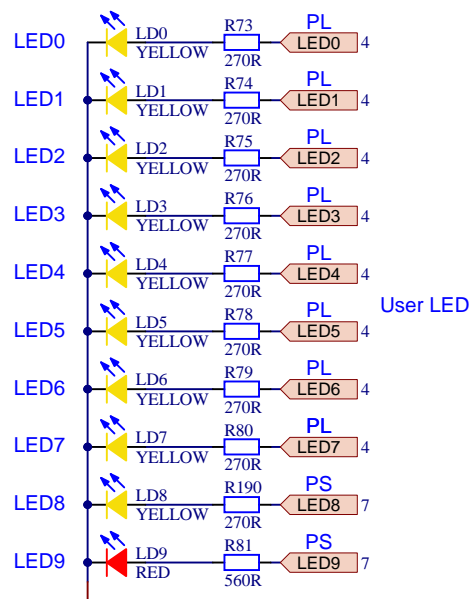
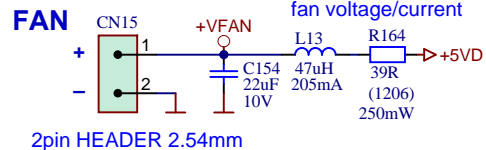
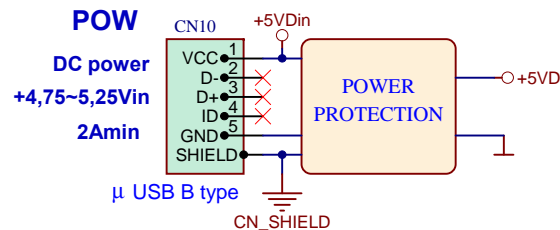
Note: number next to port symbol indicates the sheet where the signal is connected



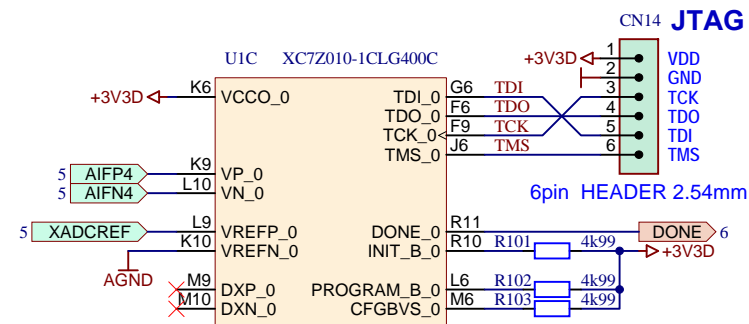
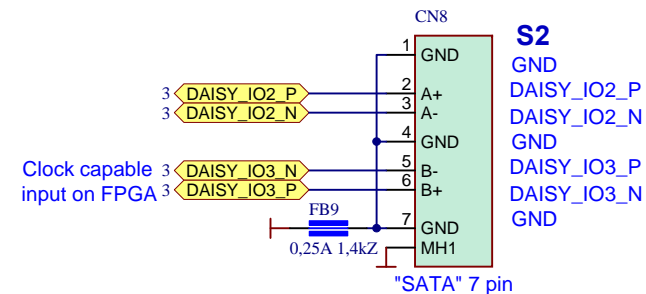
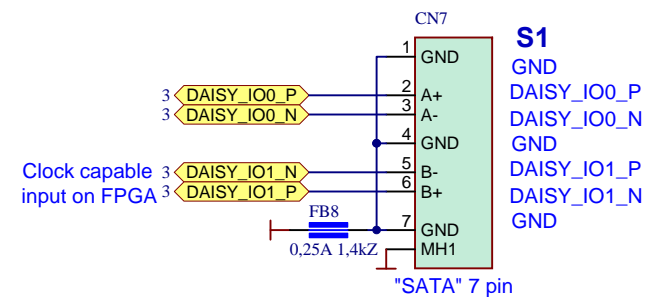
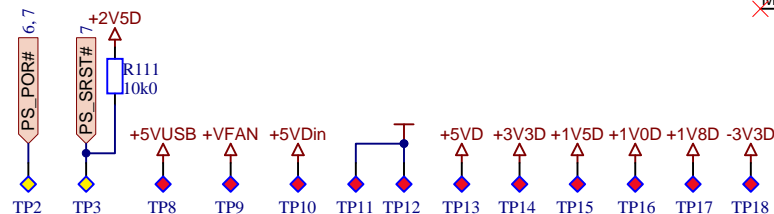
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I/O digital, I/O slow analog, Extension connectors





All power supply voltages must be within tolerances to activate Power Good and Power-On Reset signal

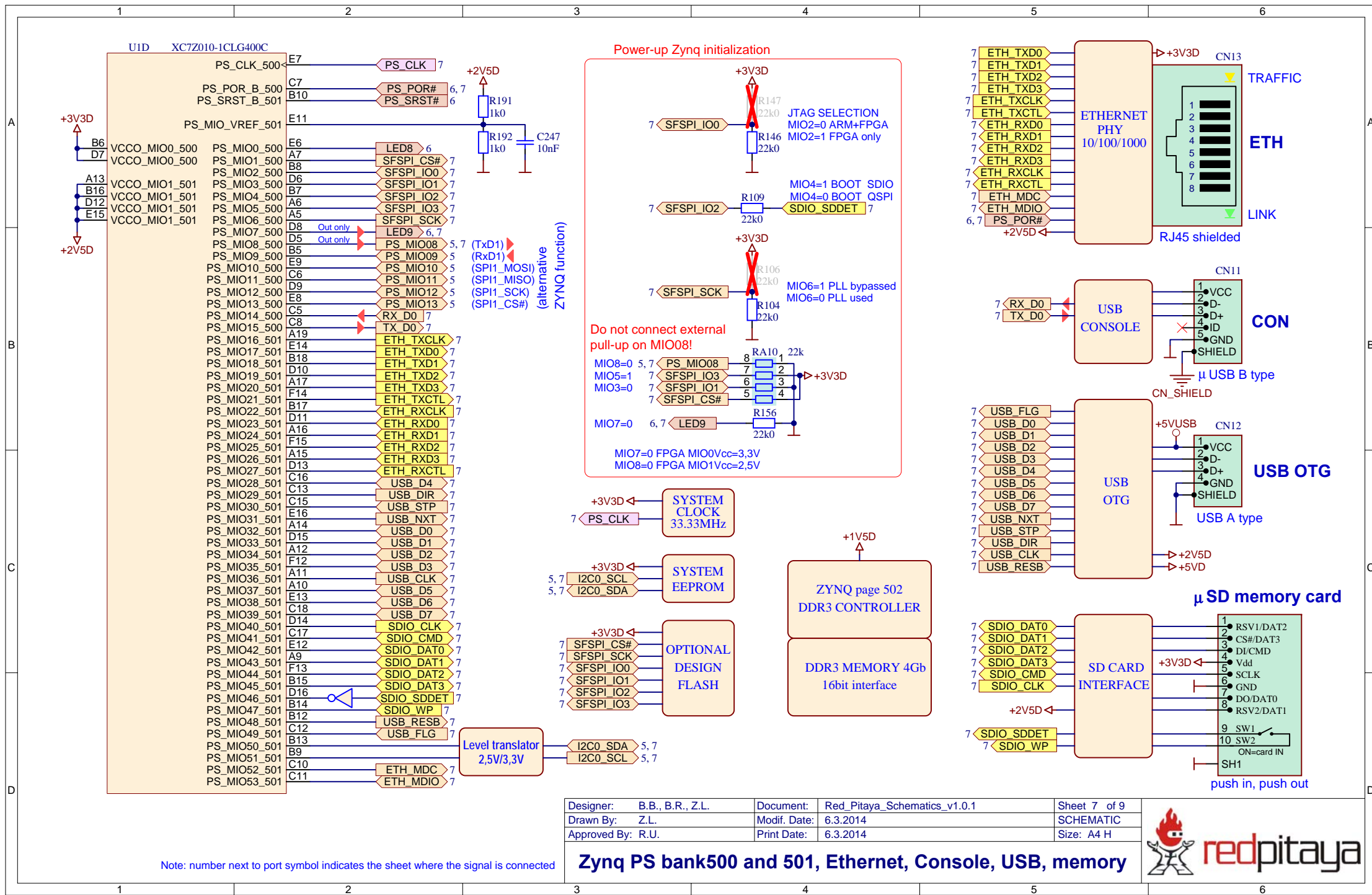


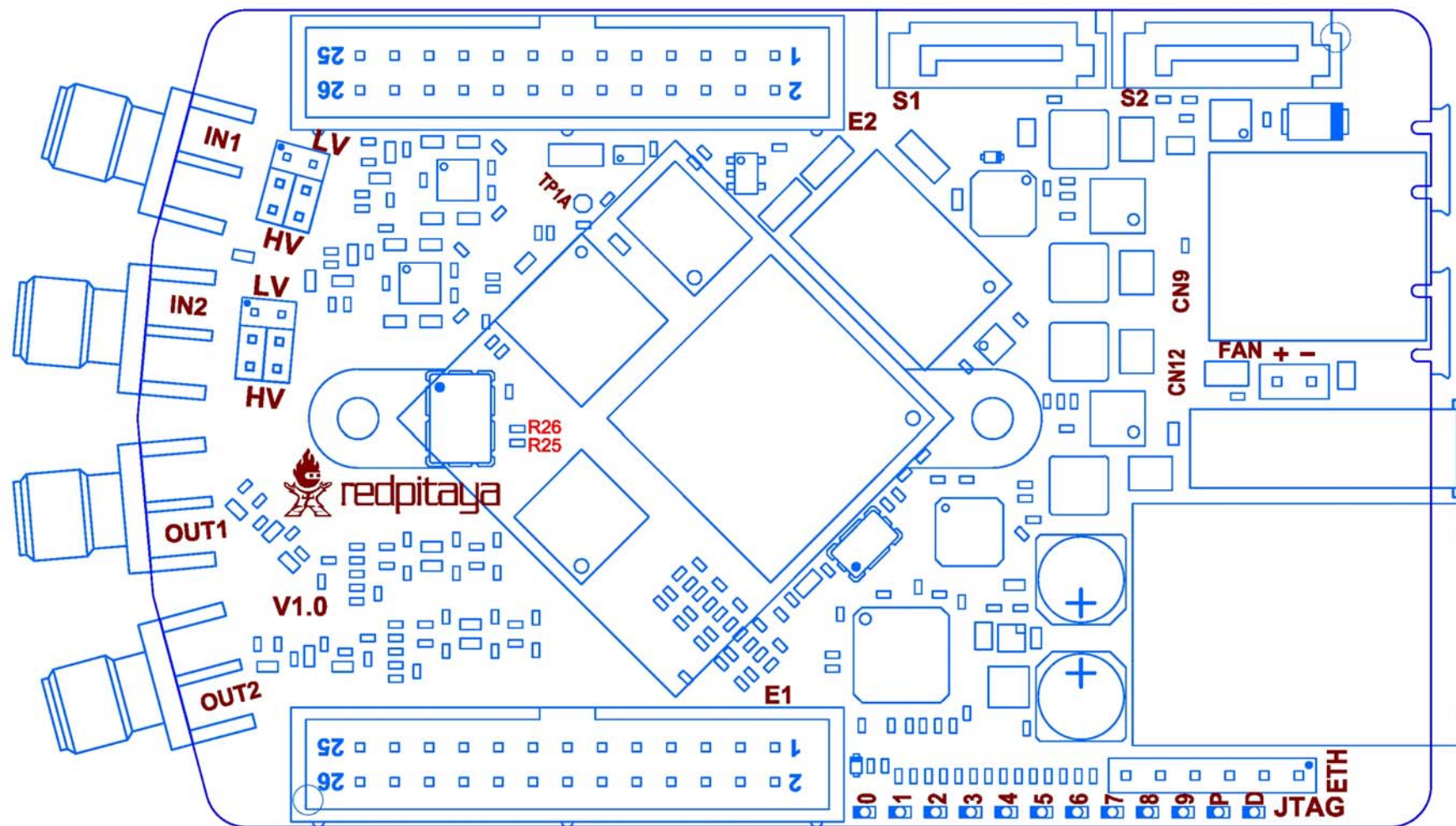
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LED, Power, Fan, Serial I/O, JTAG, testpoints



Note: number next to port symbol indicates the sheet where the signal is connected

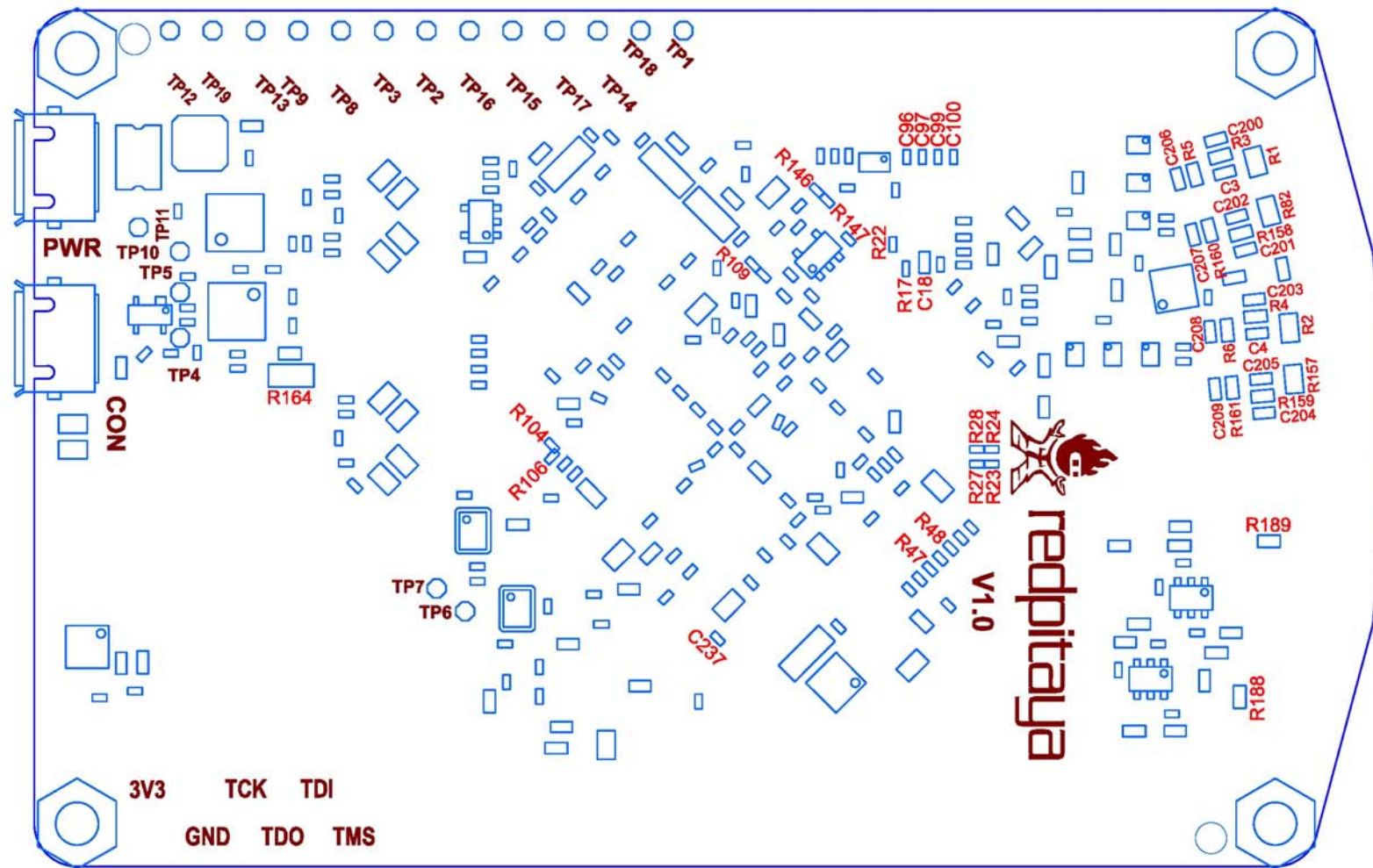




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Top Assembly





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Bottom Assembly

