

# **LN235N3T5G**

# 30V, Single N-channel Trench MOSFET

#### 1. FEATURES

- Fast switching
- Low RDS(ON)
- Trench MOSFET technology
- This is a Pb Free Device
- We declare that the material of product compliance with RoHS requirements and Halogen Free.
- S- prefix for automotive and other applications requiring unique site and control change requirements; AEC-Q101 qualified and PPAP capable.

#### 2. APPLICATIONS

- Low Side Load Switch
- Level Shift Circuits
- DC-DC Converter
- Portable Applications i.e. DSC, PDA, Cell Phone, etc.

#### 3. DEVICE MARKING AND ORDERING INFORMATION

Device	Marking	Shipping
LN235N3T5G	N5	10000/Tape&Reel

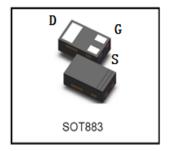
## 4. MAXIMUM RATINGS(Ta = 25°C)

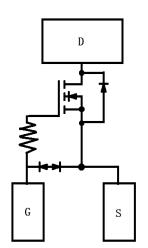
Parameter	Symbol	Limits	Unit	
Drain-to-Source Voltage	VDSS	30	V	
Gate-to-Source Voltage	VGS	±12	V	
Drain Current (Note 1)				
Steady State (TA = 25°C)	ID	930	mΑ	
(TA = 100°C)		590		
Power Dissipation (Note 1)	PD		mW	
Steady State		715	IIIVV	
Pulsed Drain Current (tp = 10 μs)	IDM	3.7	Α	
Operating Junction and Storage Temperature Range	TJ, TSTG	-55~+150	$^{\circ}$	
Lead Temperature for Soldering Purposes	TL	260	$^{\circ}$ C	
(1/8" from case for 10 s)	'-			

#### 5. THERMAL CHARACTERISTICS

Parameter	Symbol	Limits	Unit
Junction-to-Ambient - Steady State (Note 1)	RΘJA	305	°C/W

Note 1: Surface-mounted on FR4 board using 1 in sq pad size (Cu area = 1.127 in sq [1 oz] including traces)







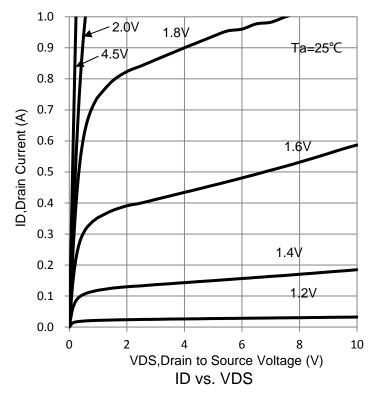
# 6. ELECTRICAL CHARACTERISTICS (Ta= 25°C)

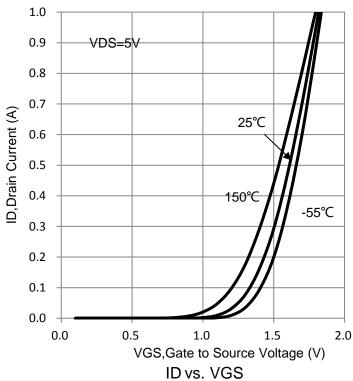
Characteristic		Symbol	Min.	Тур.	Max.	Unit
STATIC CHARACTERISTICS						
Drain-Source Breakdown Voltage (VGS =0V, ID =250μA)		V(BR)DSS	30	-	1	V
Gate Threshold Voltage (VDS =VGS , ID =250µA)		VGS(th)	0.45	-	1.5	V
Gate-Body Leakage Current (VDS =0V, VGS =±12V)		IGSS	-	-	±10	пΔ
Zero Gate Voltage Drain Current (VDS =30V, VGS =0V)		IDSS	-	-	1	μΑ
Drain-Source On-Resistance (VGS =4.5V, ID = 200mA)		RDS(ON)	-	-	0.46	Ω
Drain-Source On-Resistance (VGS =2.5V, ID = 200mA)		1123(311)	-	-	0.68	32
Diode Forward Voltage (IS =300mA, VGS =0V)		VSD	-	-	1.2	V
DYNAMIC PARAMETERS						
Total Gate Charge	(VDS =15V,	Qg	1	0.65	0.87	
Gate-Source Charge	VGS =4.5V, ID	Qgs	-	0.14	-	nC
Gate-Drain Charge	=1.0A)	Qgd	-	0.18	-	
Input Capacitance	(VDS =25V,	Ciss	-	37	56	
Output Capacitance	VGS =0V,	Coss	-	8.6	-	pF
Reverse Transfer Capacitance	f=1MHz)	Crss	-	5.4	-	
Turn-On Delay Time	(VDS =15V, RL	td(on)	-	6.5	13	
Rise Time	=15Ω, VGEN	tr	-	9.5	-	nc
Turn-Off Delay Time			-	14	28	ns
Fall Time Ω)		tf	-	5.5	-	

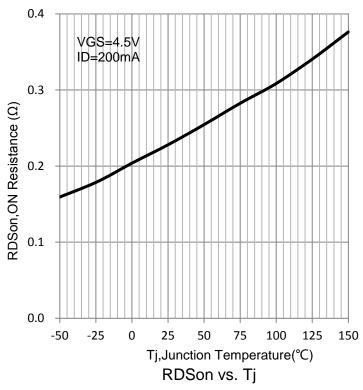
Note 2: Pulse test; pulse width≤300µs, duty cycle≤ 2%

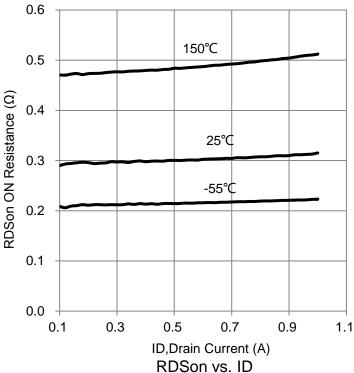


#### 7. ELECTRICAL CHARACTERISTICS CURVES



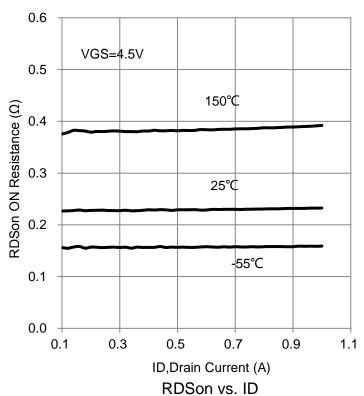


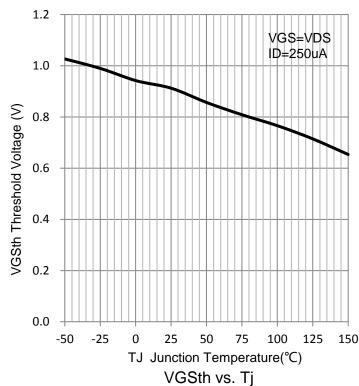






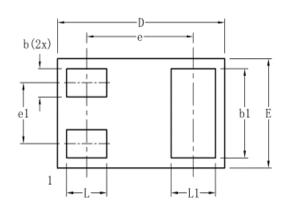
# 7. ELECTRICAL CHARACTERISTICS CURVES(Con.)

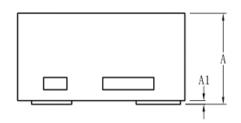






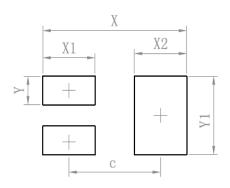
#### **8.OUTLINE AND DIMENSIONS**





SOT883				
DIM	MIN	TYP	MAX	
D	0.95	1.00	1.05	
Е	0.50	0.60	0.65	
е	1	0.64	-	
e1	1	0.34	-	
L	0.19	0.24	0.29	
L1	0.22	0.27	0.32	
b	0.10	0.15	0.20	
b1	0.44	0.49	0.54	
Α	0.43	0.48	0.53	
A1	0	-	0.05	
All Dimensions in mm				

### 9.SOLDERING FOOTPRINT



Dimensions	(mm)		
С	0.70		
X	1.10		
X1	0.40		
X2	0.40		
Y	0.20		
Y1	0.55		

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