

Bus

File: Bus.kicad_sch

CPU, FPGA, FRAM

File: CPU,_FPGA,_FRAM.kicad_sch

Power

File: Power.kicad_sch

A/V

File: A%2FV.kicad_sch

USB

File: USB.kicad_sch

VRAM, DAC

File: VRAM,_DAC.kicad_sch

PETIO

File: PETIO.kicad_sch

SID

File: SID.kicad_sch

SID Power

File: SID_Power.kicad_sch

Mixer

File: Mixer.kicad_sch

Keylock

File: Keylock.kicad_sch

Smallbus

File: Smallbus.kicad_sch

UART

File: UART.kicad_sch

Userport

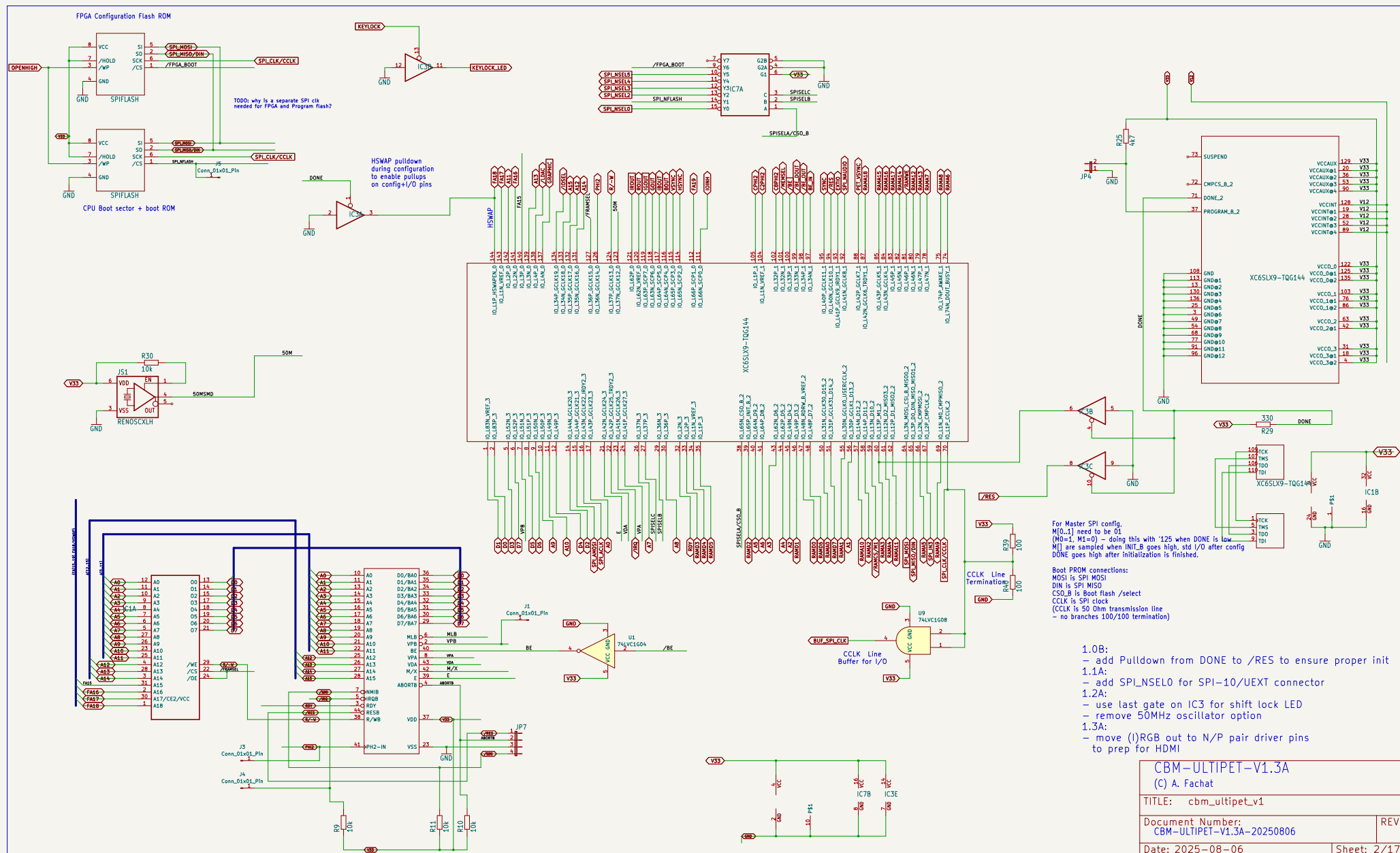
File: Userport.kicad_sch

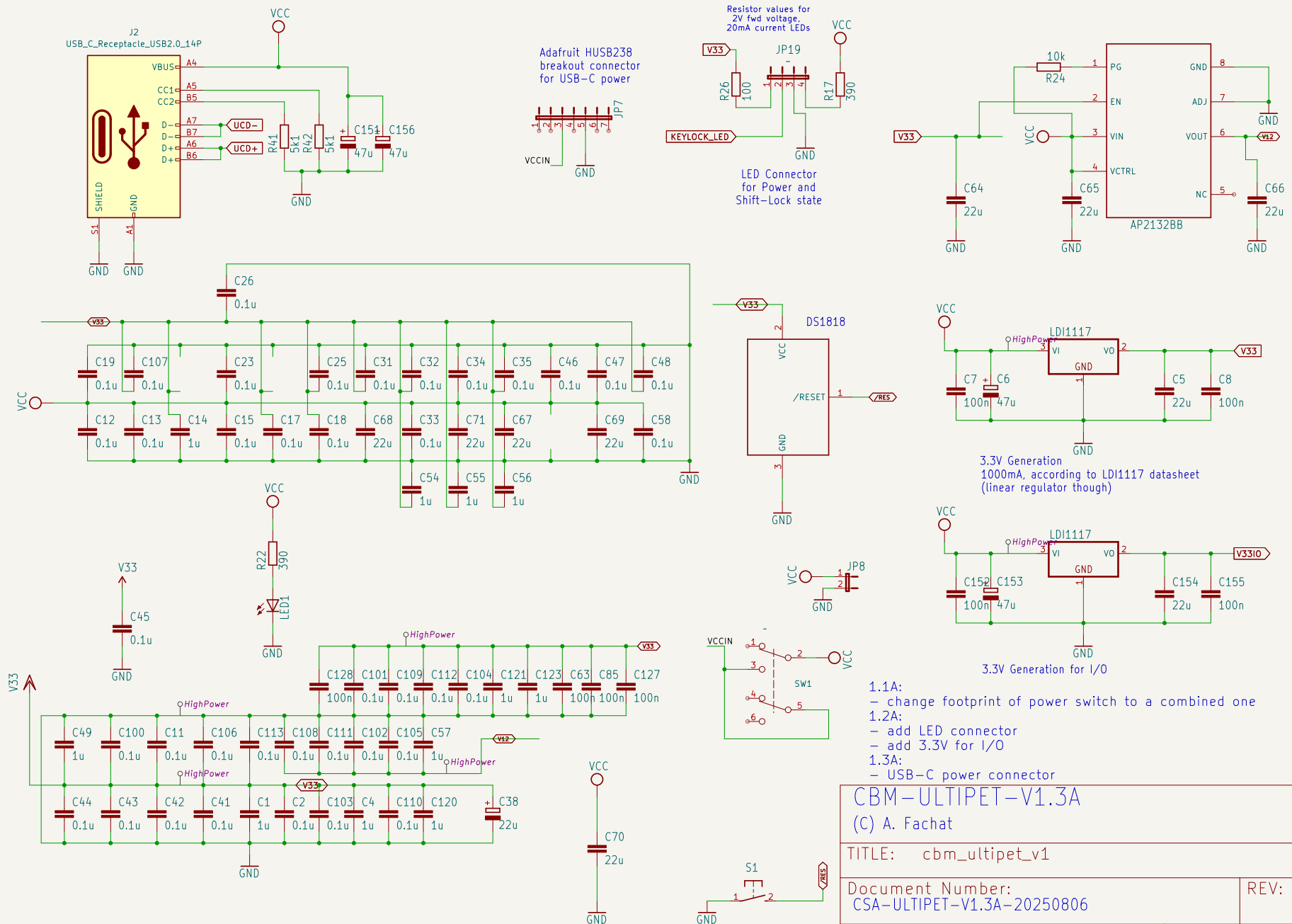
FastIEC

File: FastIEC.kicad_sch

I2C,UEXT

File: I2C,UEXT.kicad_sch





- 1.1A:
- change footprint of power switch to a combined one
- 1.2A:
- add LED connector
- add 3.3V for I/O
- 1.3A:
- USB-C power connector

CBM-ULTIPET-V1.3A

(C) A. Fachat

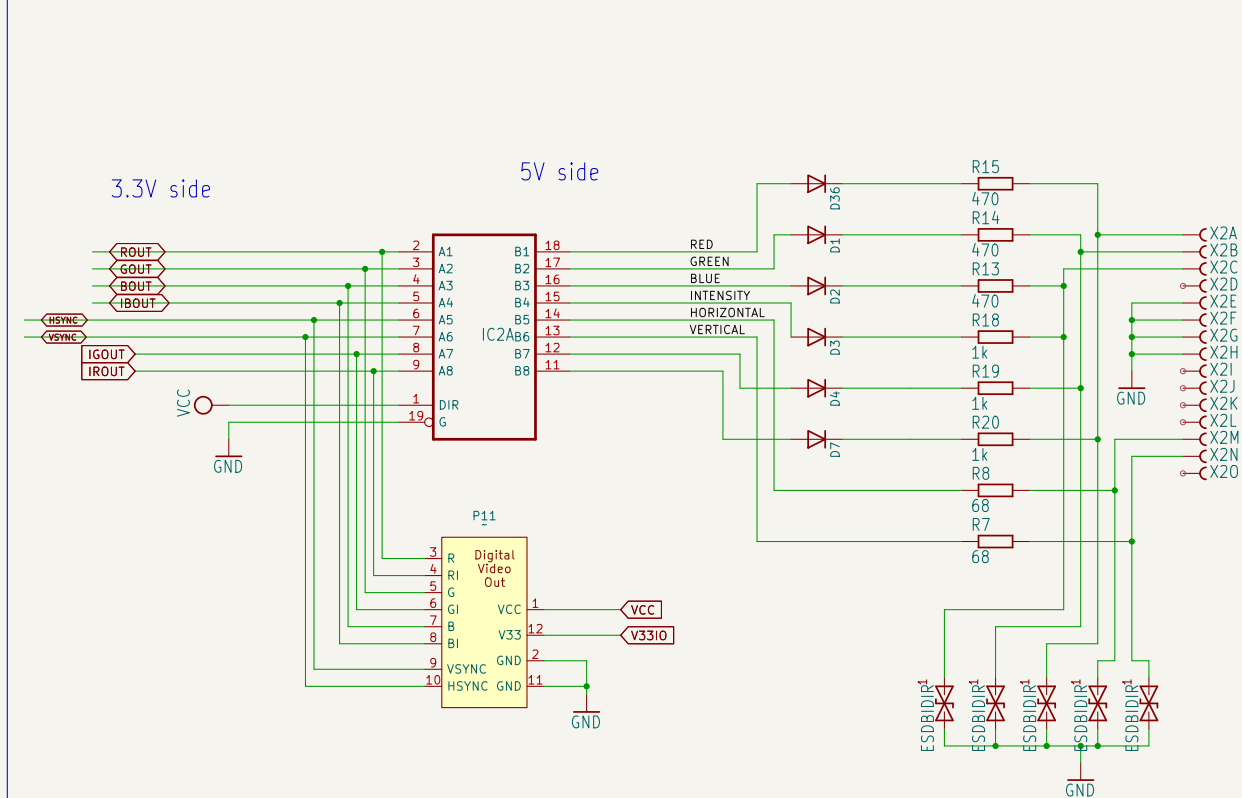
TITLE: cbm_ultipet_v1

Document Number:
CSA-ULTIPET-V1.3A-20250806

REV:

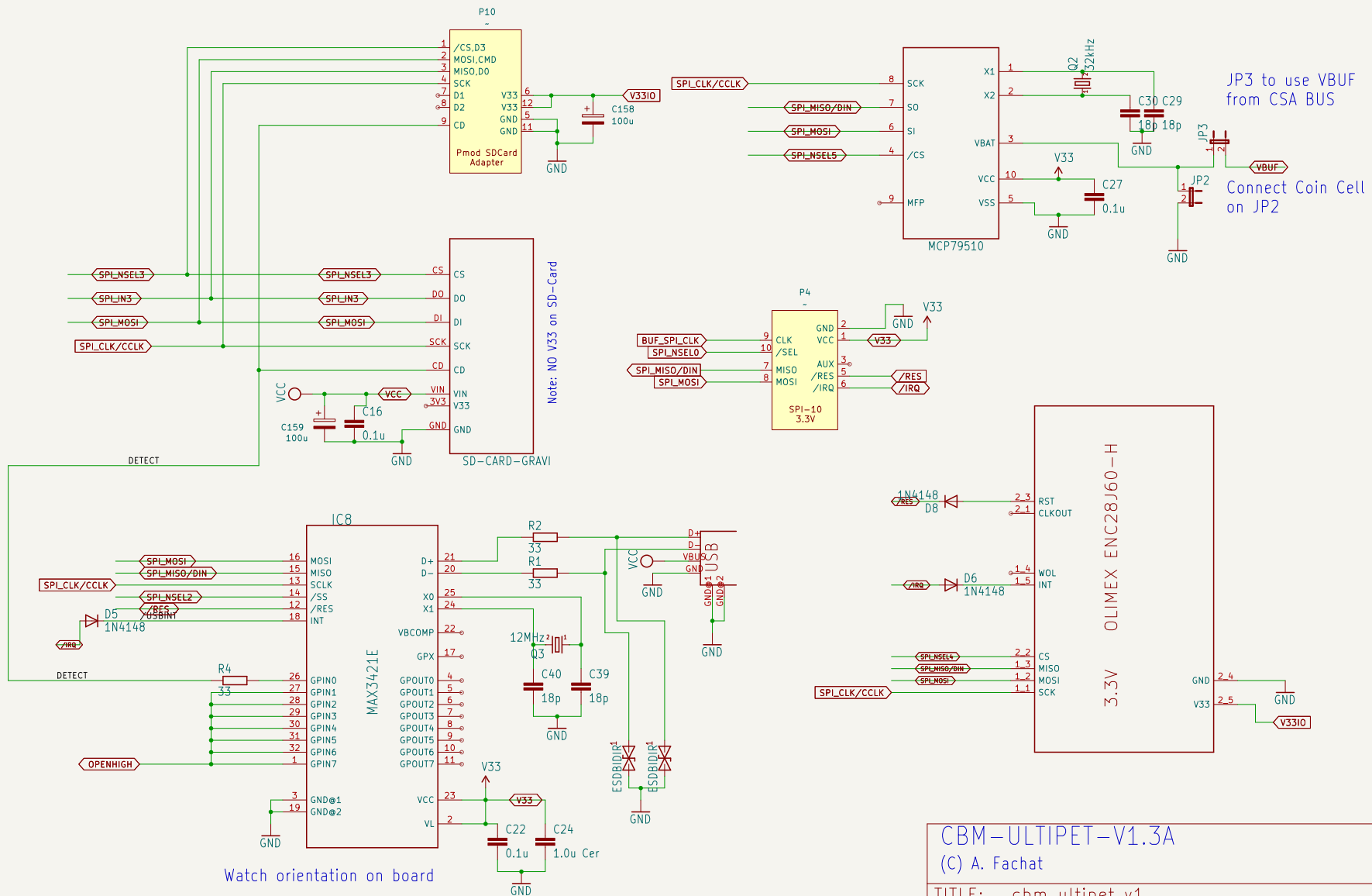
Date: 2025-08-06

Sheet: 3/17



- 1.2A:
– add Digital Video Out connector
- 1.3A:
– VGA footprint with solderable supports

CBM-ULTIPET-V1.3A (C) A. Fachat	
TITLE: cbm_ultipet_v1	
Document Number: CSA-ULTIPET-V1.3A-20250806	REV:
Date: 2025-08-06	Sheet: 4/17



CBM-ULTIPET-V1.3A

(C) A. Fachat

TITLE: cbm_ultipet_v1

Document Number:
CSA-ULTIPET-V1.1A-20241117

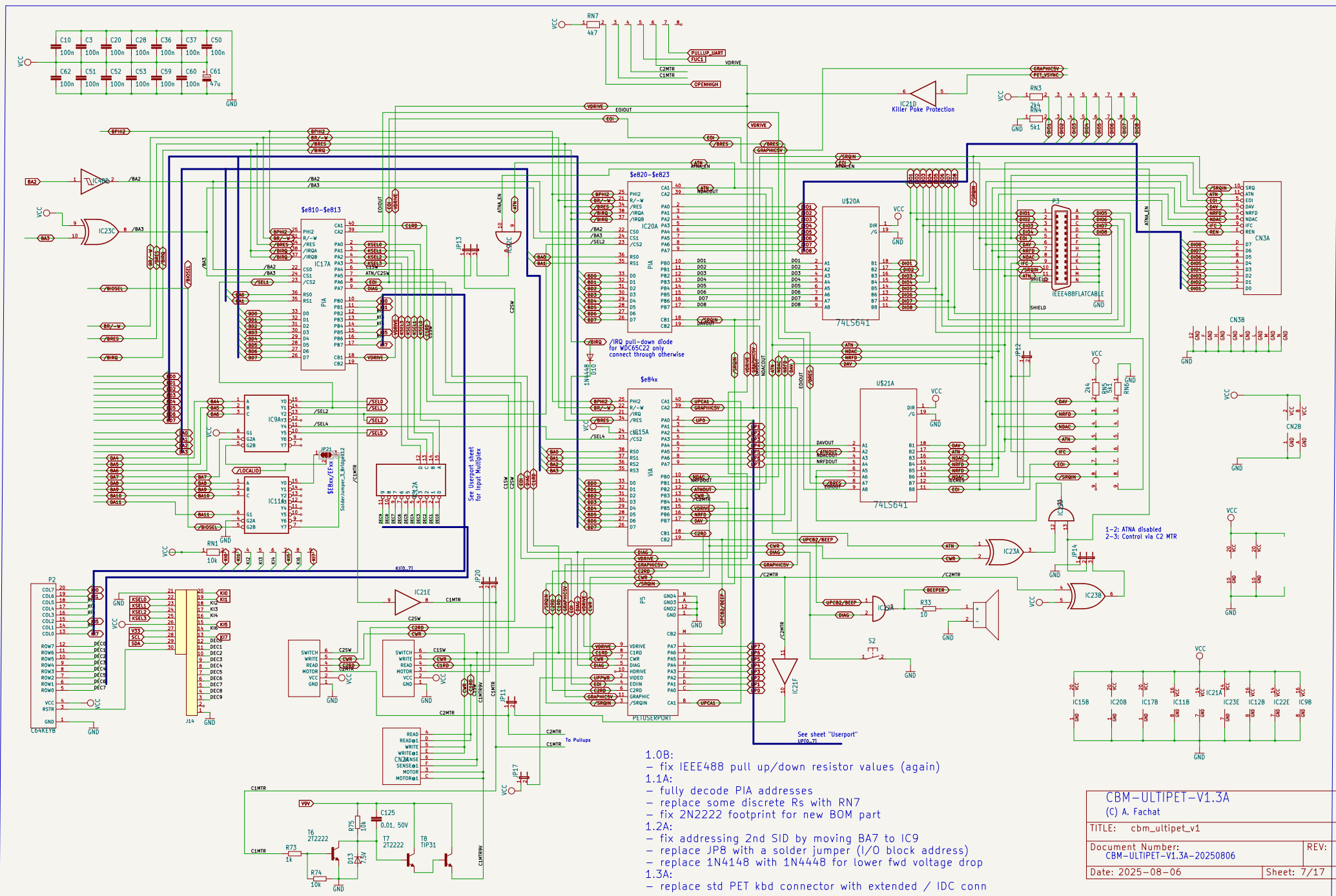
REV:

Date: 2025-08-06

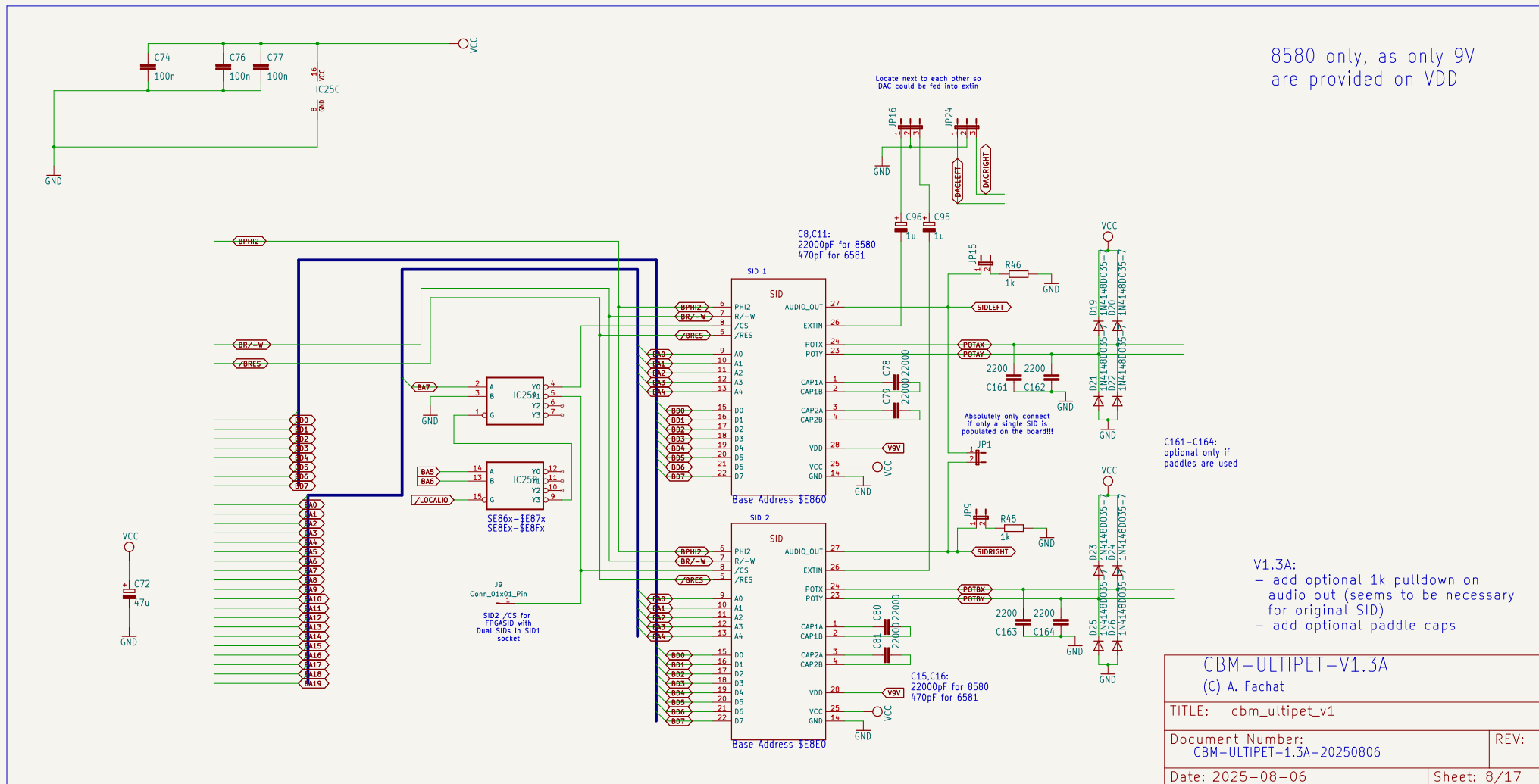
Sheet: 5/17



CBM-ULTIPET-V1.3A (C) A. Fachat	
TITLE: cbm_ultipet_v1	
Document Number: CBM-ULTIPET-V1.2A-20241010	REV:
Date: 2025-08-06	Sheet: 6/17



CBM-ULTIPET-V1.3A (C) A. Fachat	
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Document Number: CBM-ULTIPET-V1.3A-20250806	REV:
Date: 2025-08-06	Sheet: 7 / 17



8580 only, as only 9V are provided on VDD

CBM-ULTIPET-V1.3A (C) A. Fachat

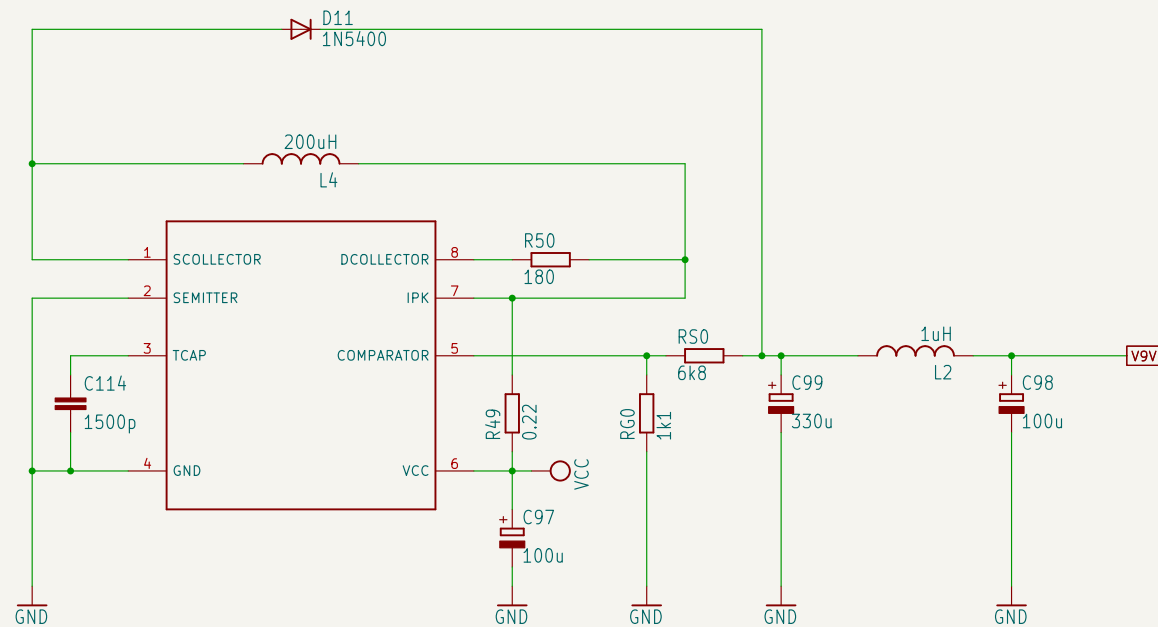
TITLE: cbm_ultipet_v1

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REV:

Date: 2025-08-06

Sheet: 8/17



V1.0B:
– Fix parts footprint for L4, D2 and some caps

$$V_{out} = 1.25 \times (1 + R_S/R_G)$$

$$R_S/R_G = (V_{out} / 1.25) - 1$$

$$V_{out} = 9V \rightarrow R_S/R_G = 6.2$$

$$\rightarrow R_G = 1.1k, R_S = 6.8k$$

$$V_{out} = 12V \rightarrow R_S/R_G = 8.6$$

$$\rightarrow R_G = 1.2k, R_S = 10k$$

9V is not only used by SID
but also by Tape

CBM-ULTIPET-V1.3A

(C) A. Fachat

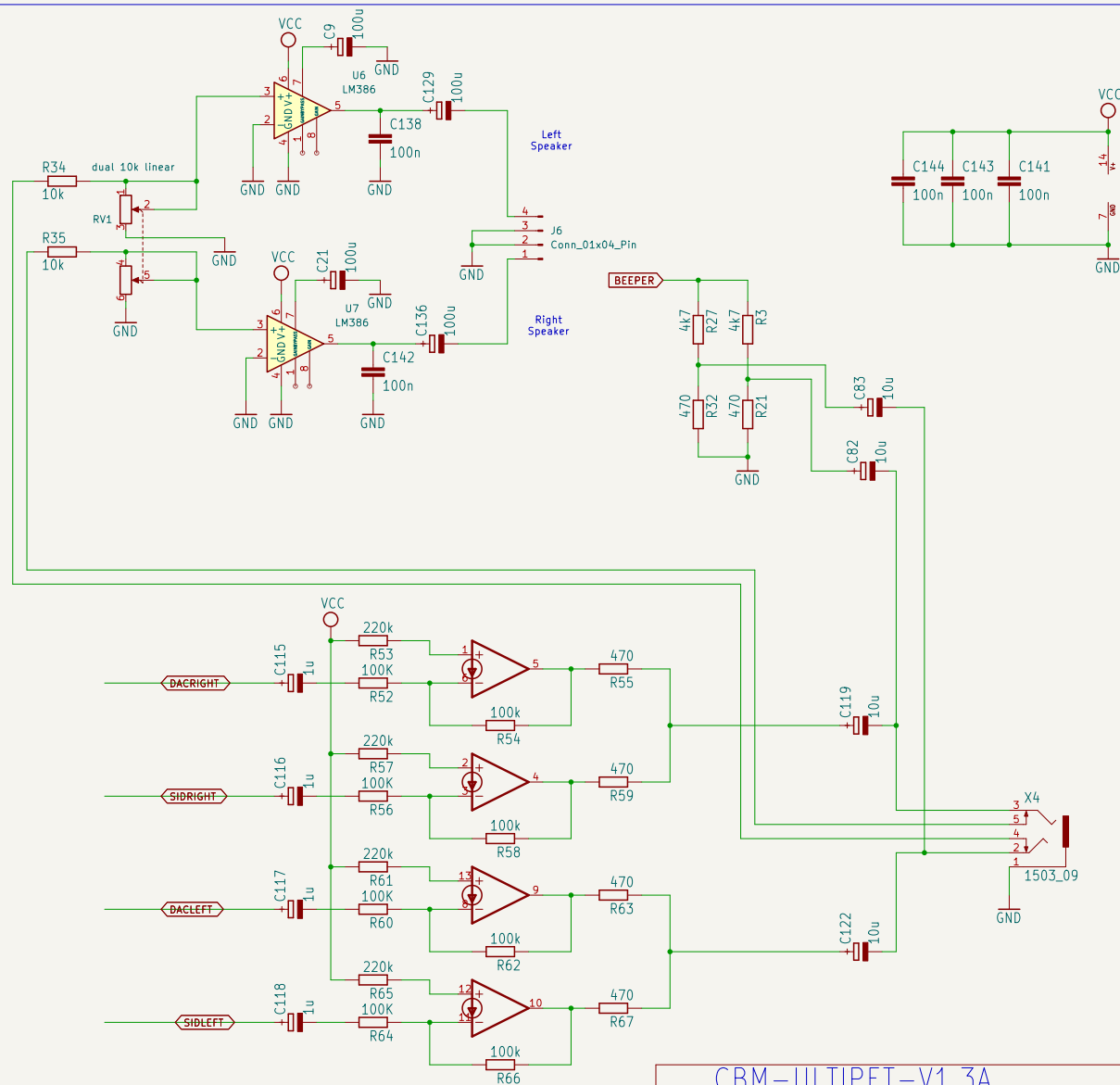
TITLE: cbm_ultipet_v1

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CSA-DUALSID-1.0B-20231212

REV:

Date: 2025-08-06

Sheet: 9/17



1.0B: add voltage divider to reduce beeper
volume on mixer output

1.2A:

- duplicate beeper voltage divider to reduce L/R crosstalk
- adjust beeper voltage divider resistor values
- add bypass / gain caps on audio amp

CBM-ULTIPET-V1.3A

(C) A. Fachat

TITLE: cbm_ultipet_v1

Document Number:
CBM-ULTIPET-1.2A-20241117

REV:

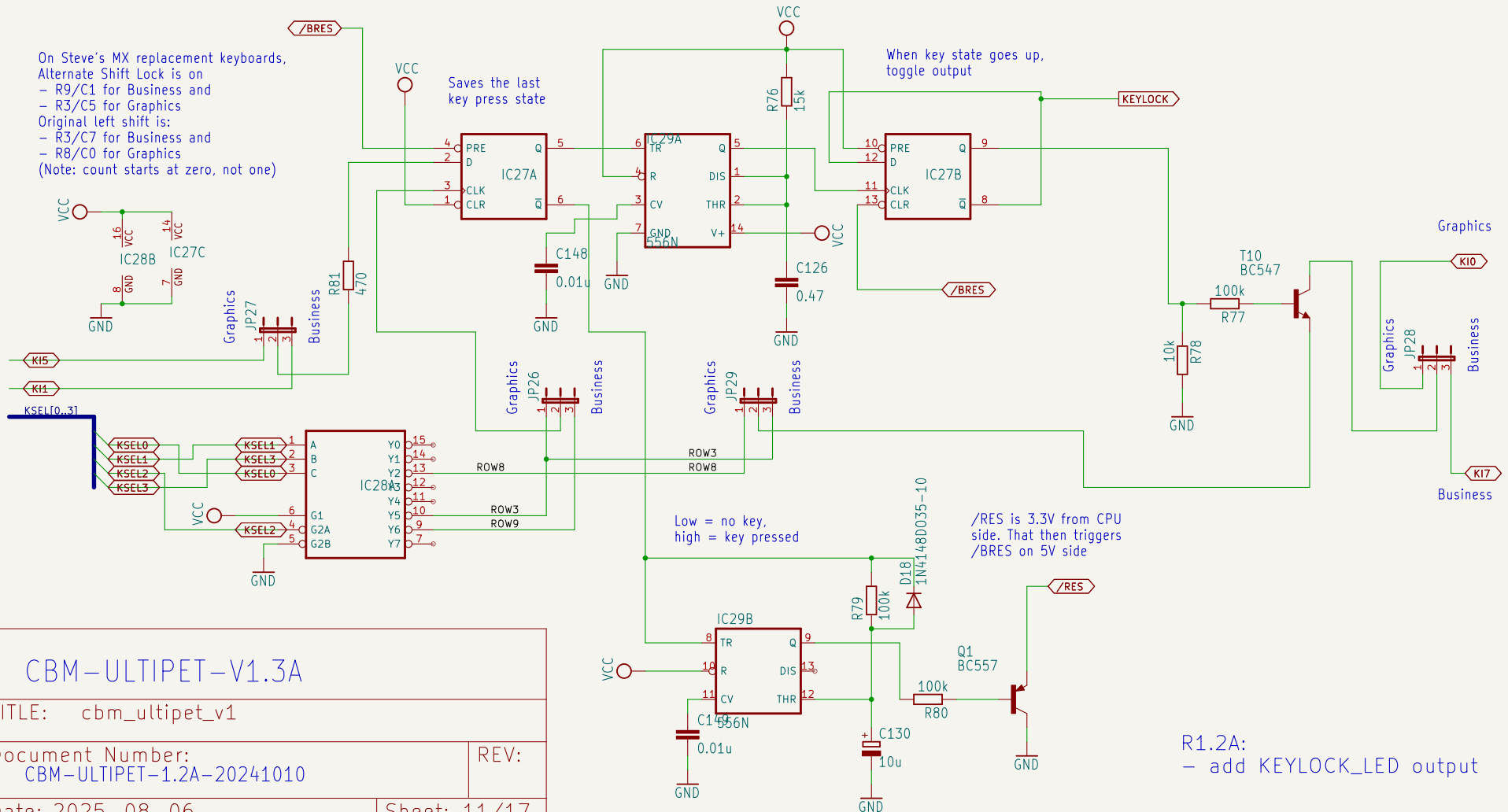
Date: 2025-08-06

Sheet: 10/17

Commodore PET SHIFT-LOCK simulator + RESET on long push

Inspired by SX64 keyboard and discussions with Steve Gray and Mike Naberezny

On Steve's MX replacement keyboards,
Alternate Shift Lock is on
- R9/C1 for Business and
- R3/C5 for Graphics
Original left shift is:
- R3/C7 for Business and
- R8/C0 for Graphics
(Note: count starts at zero, not one)



CBM-ULTIPET-V1.3A

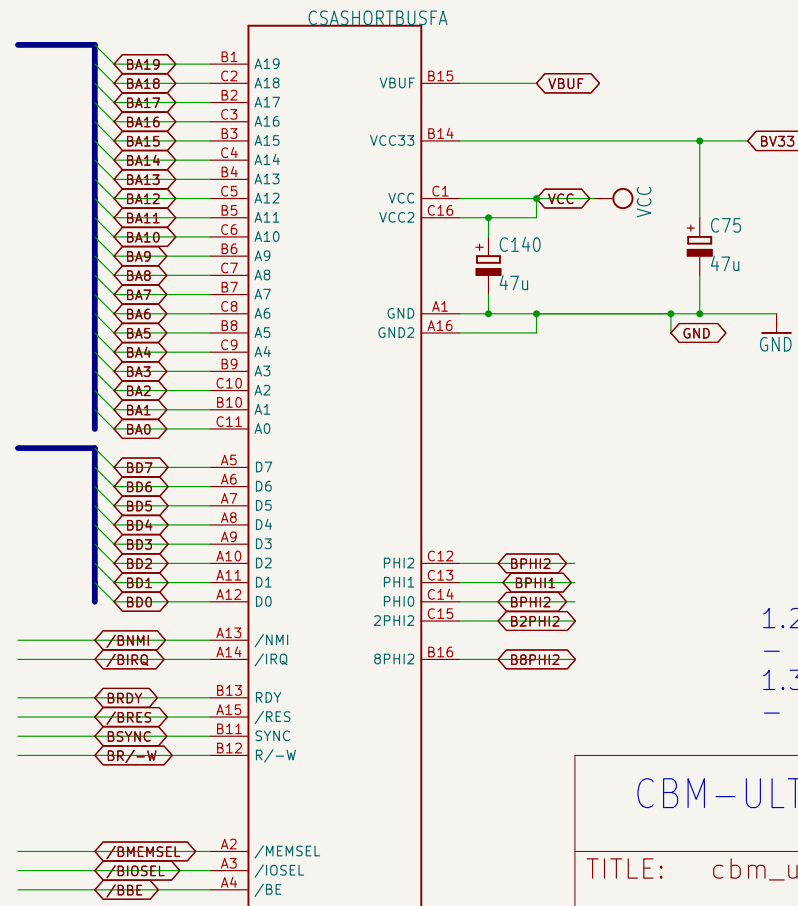
TITLE: cbm_ultipet_v1

Document Number:
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REV:

Date: 2025-08-06

Sheet: 11/17



- 1.2A:
 – replace V33 with V33IO
 1.3A:
 – correct (mirrored) pinout

CBM-ULTIPET-V1.3A

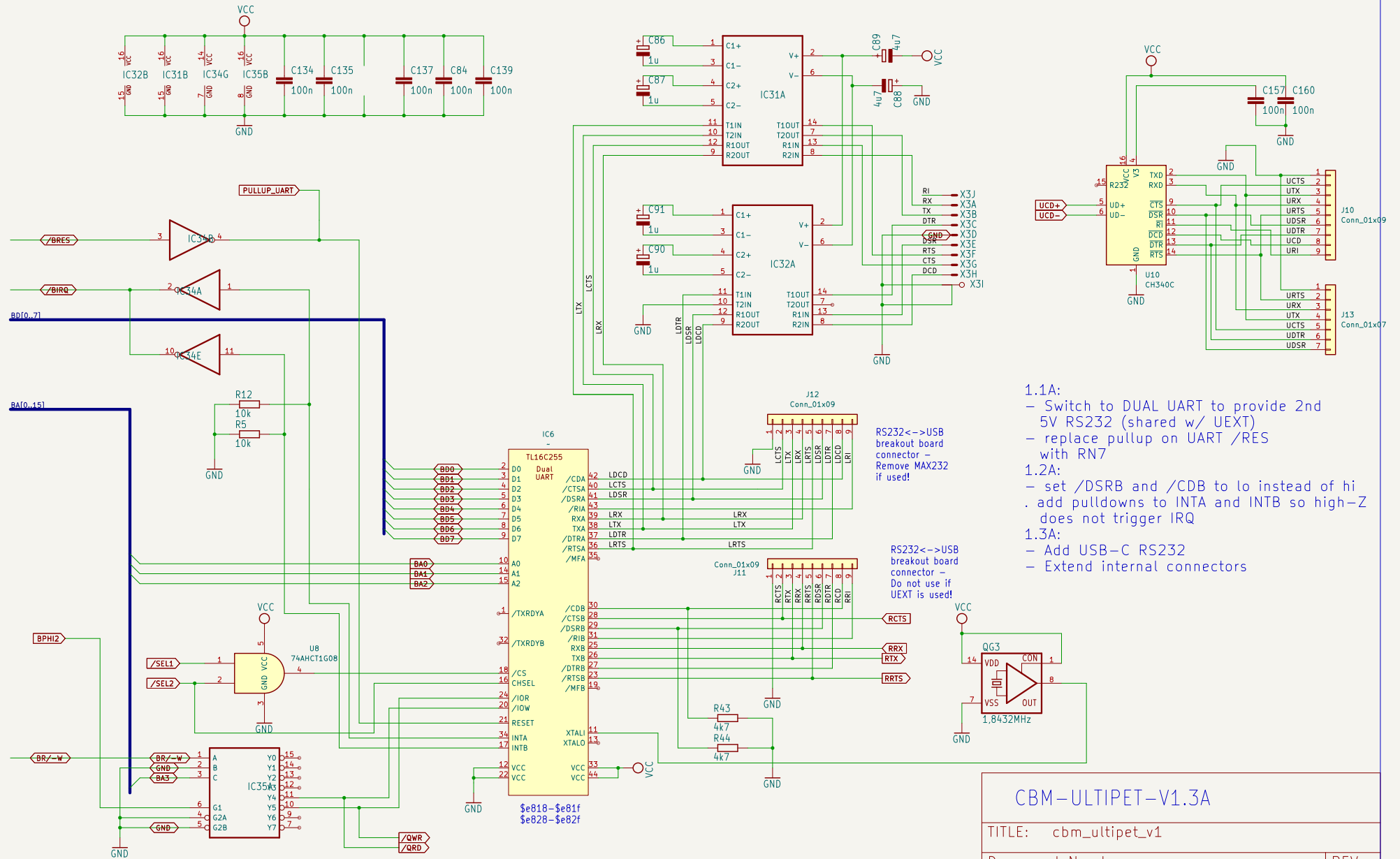
TITLE: cbm_ultipet_v1

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Date: 2025-08-06

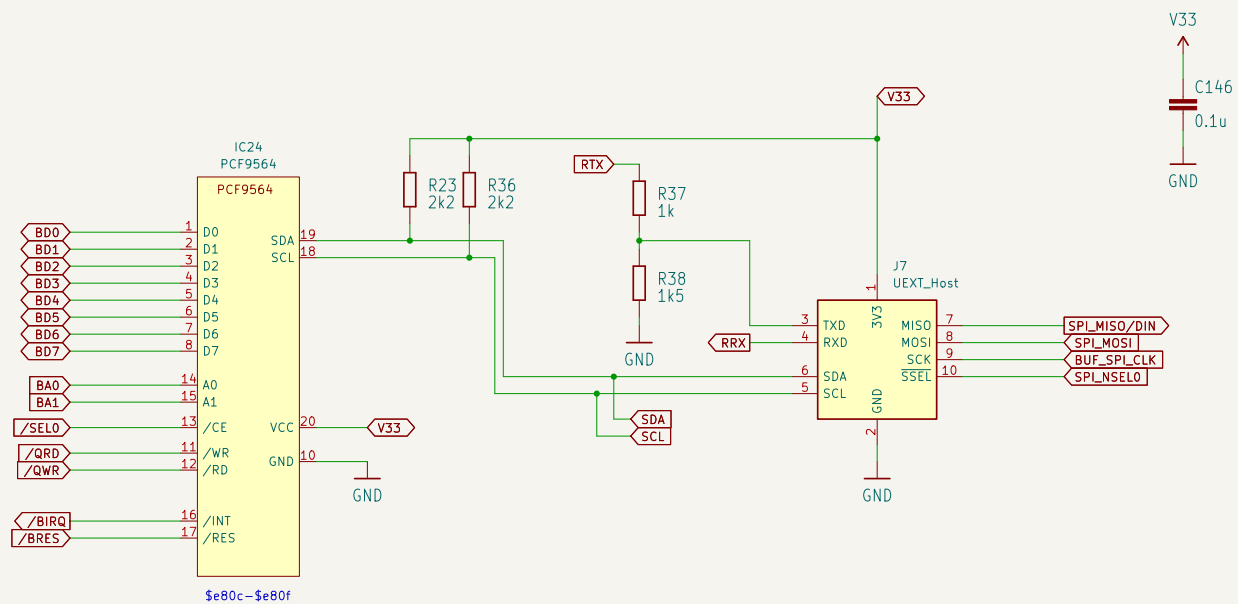
Sheet: 12/17



- 1.1A:
- Switch to DUAL UART to provide 2nd 5V RS232 (shared w/ UEXT)
 - replace pullup on UART /RES with RN7
- 1.2A:
- set /DSRB and /CDB to lo instead of hi
 - add pulldowns to INTA and INTB so high-Z does not trigger IRQ
- 1.3A:
- Add USB-C RS232
 - Extend internal connectors

CBM-ULTIPET-V1.3A

TITLE: cbm_ultipet_v1	
Document Number:	CBM-ULTIPET-V1.3A-20250806
Date: 2025-08-06	Sheet: 13/17



1.1A:
- this page is new
1.3A:
- extra labels for SDA/SCL

CBM-ULTIPET-V1.3A

TITLE: cbm_ultipet_v1

Document Number:
CBM-ULTIPET-1.1A-20250806

REV:

Date: 2025-08-06

Sheet: 16/17