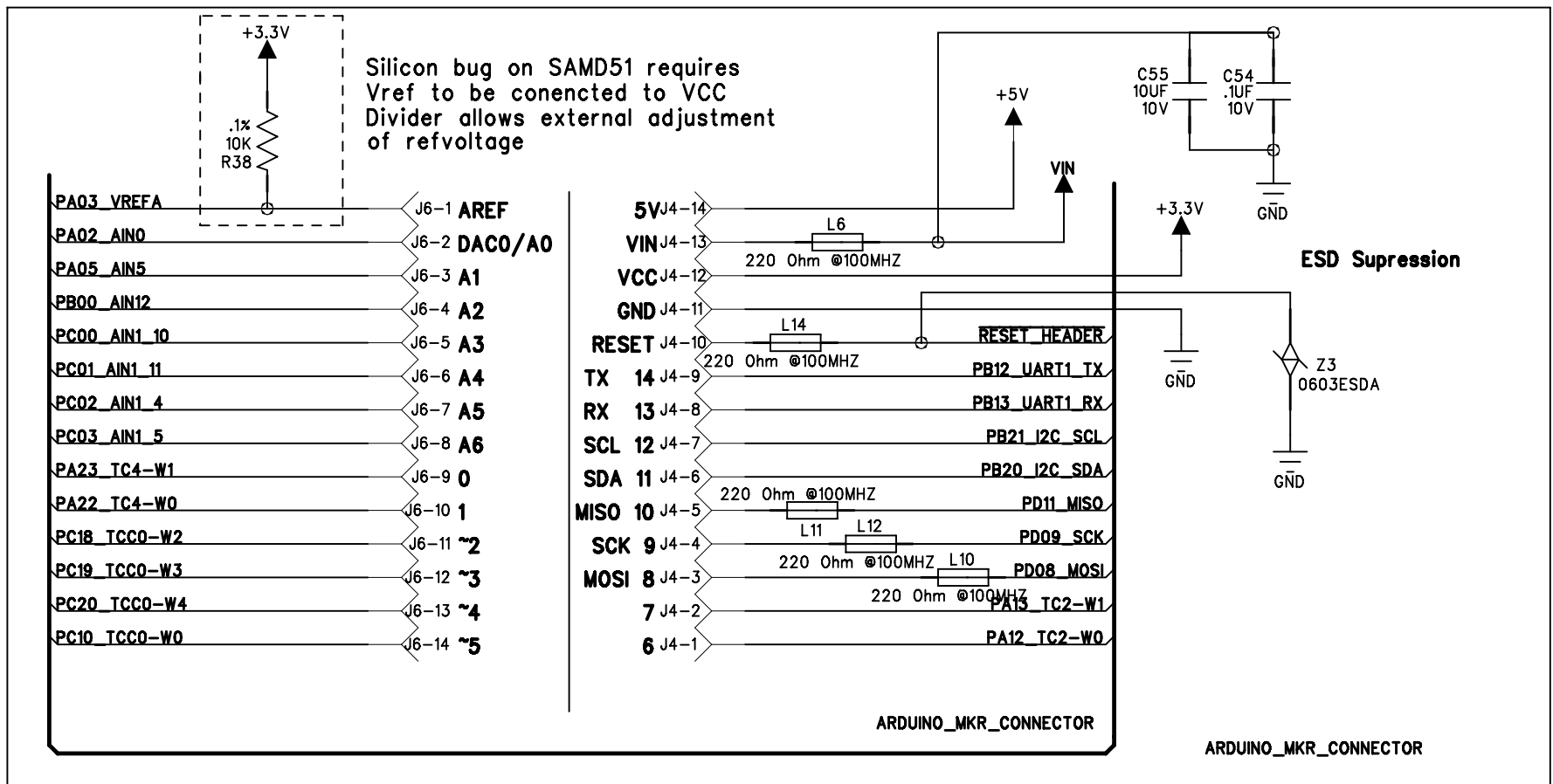


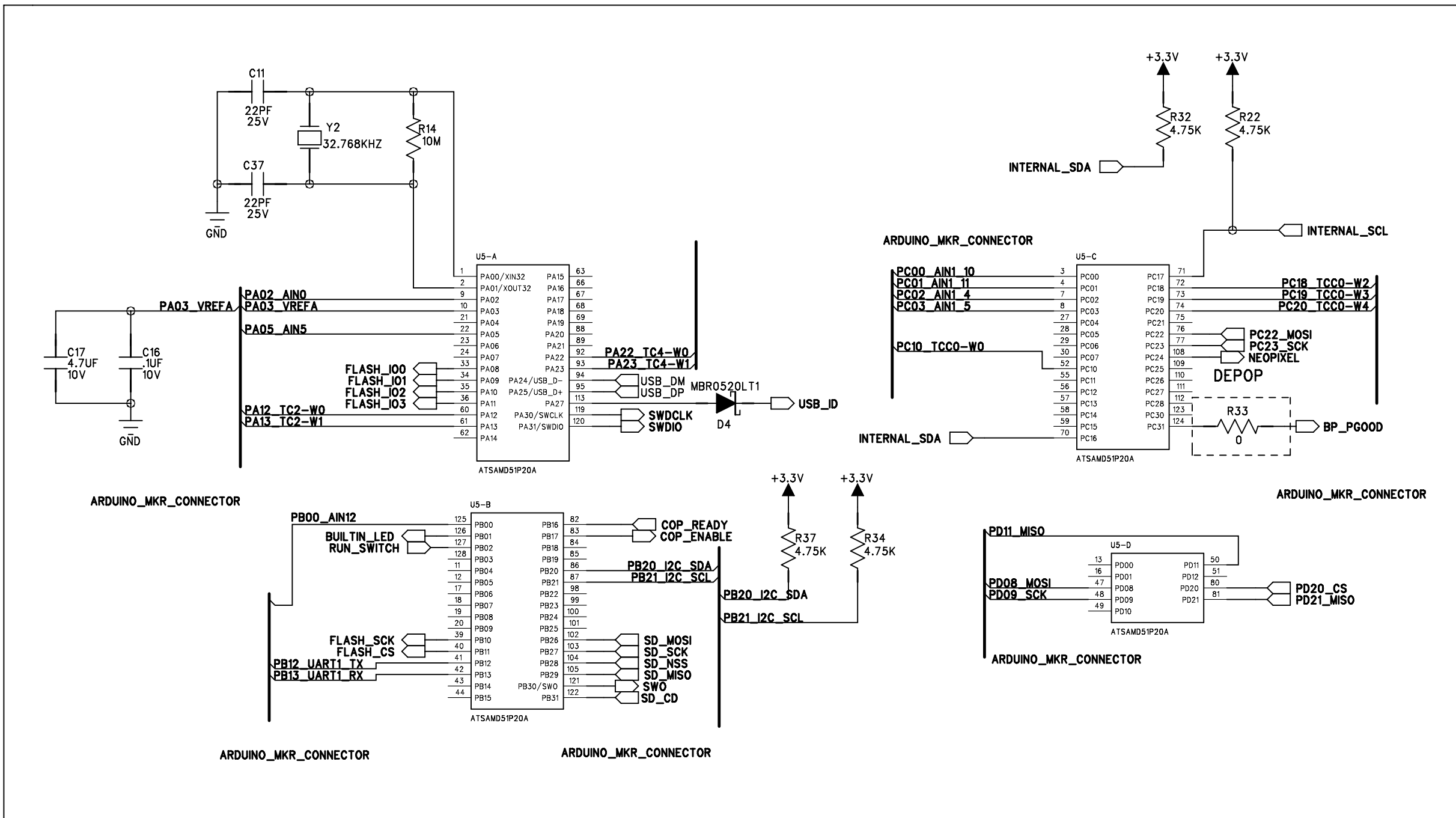
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LTR	DESCRIPTION	BY	DATE	CHECK	APPRVL

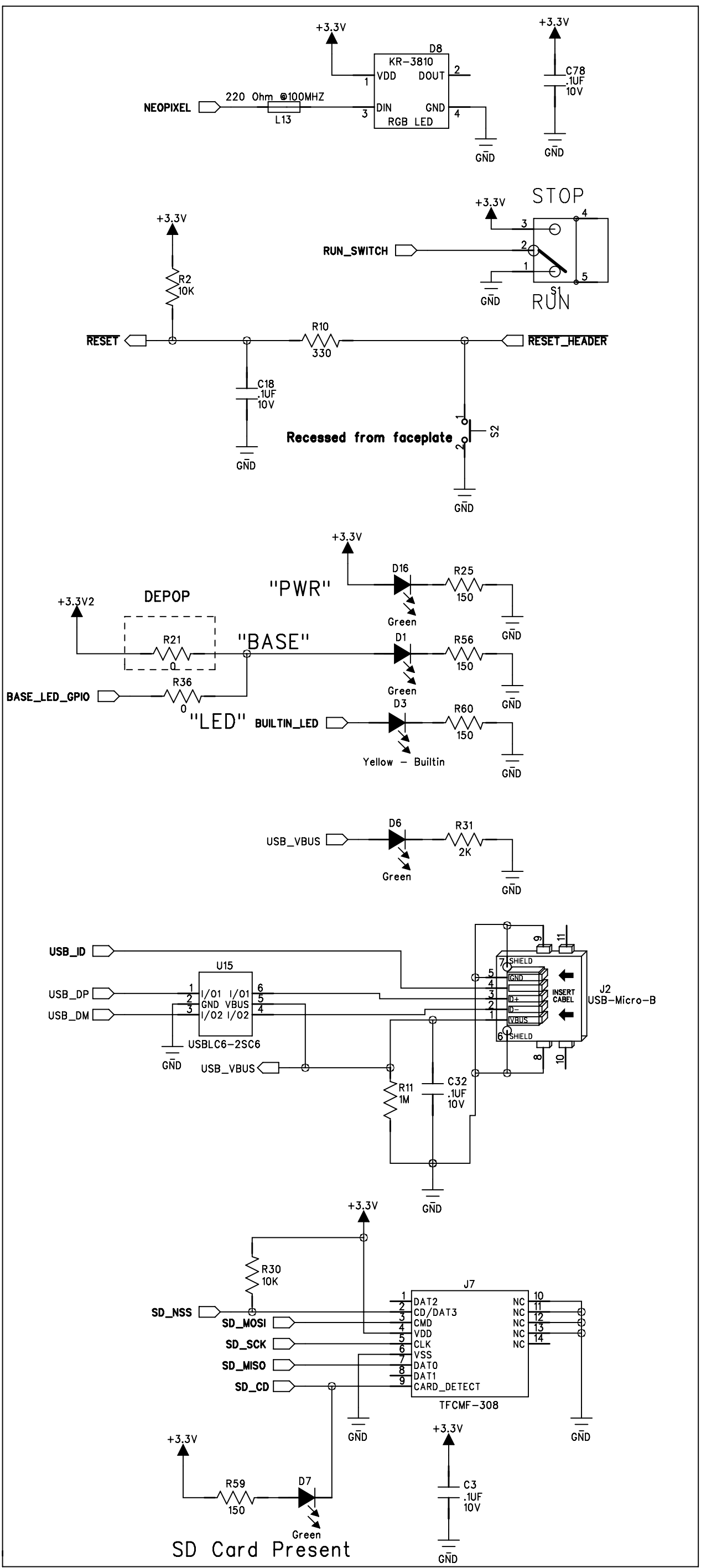
Header



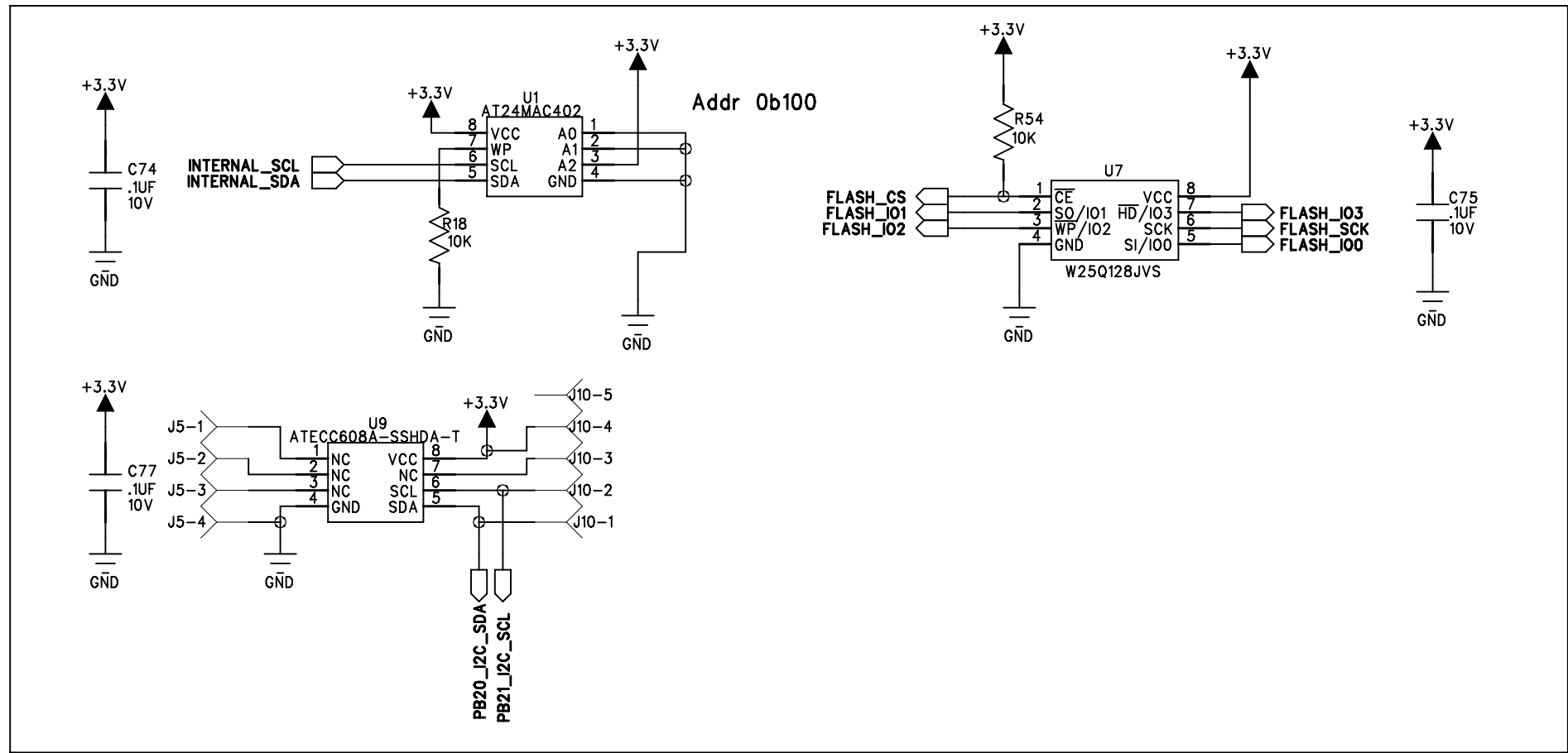
SAMD51 GPIO



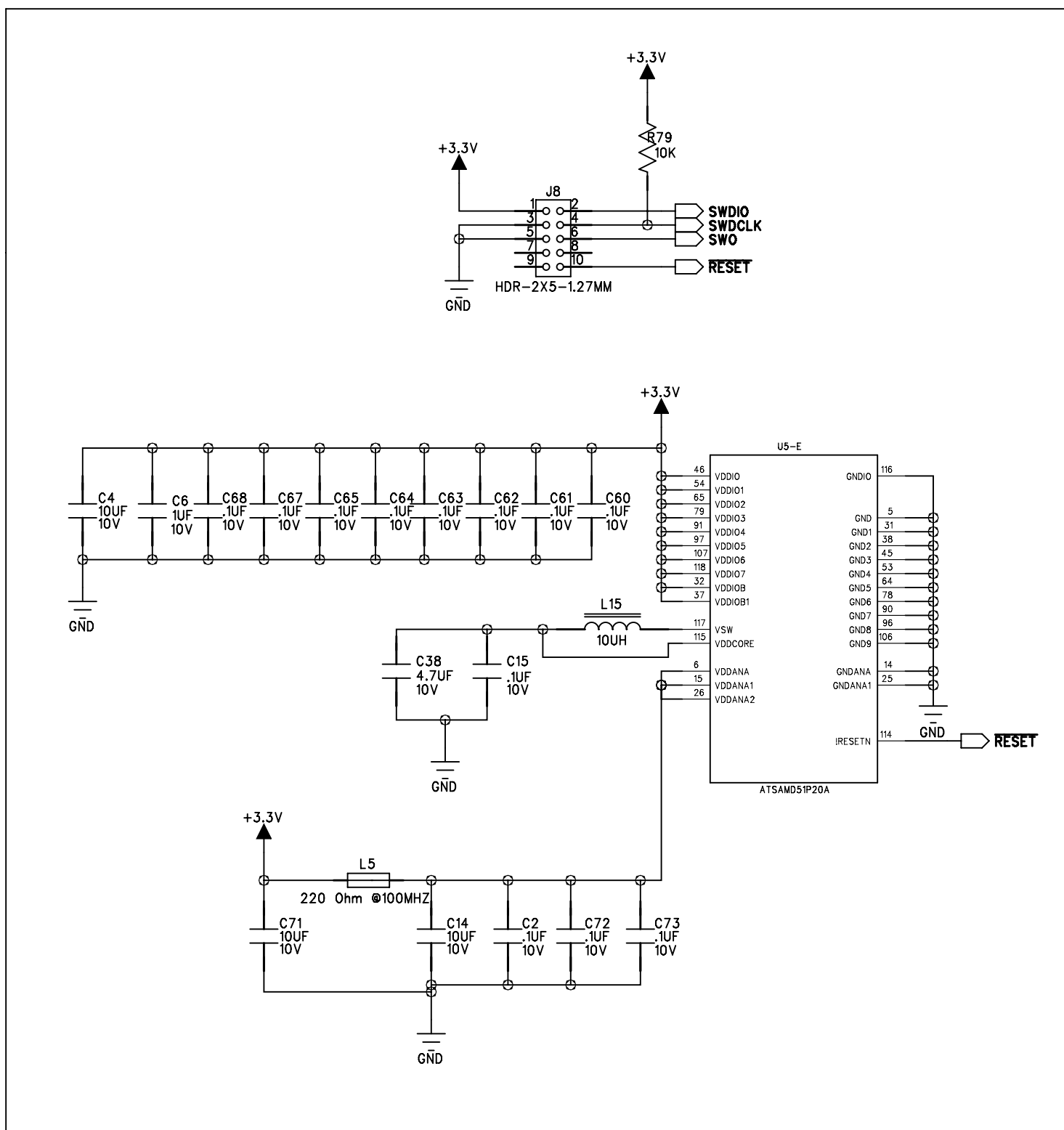
Front Interfaces



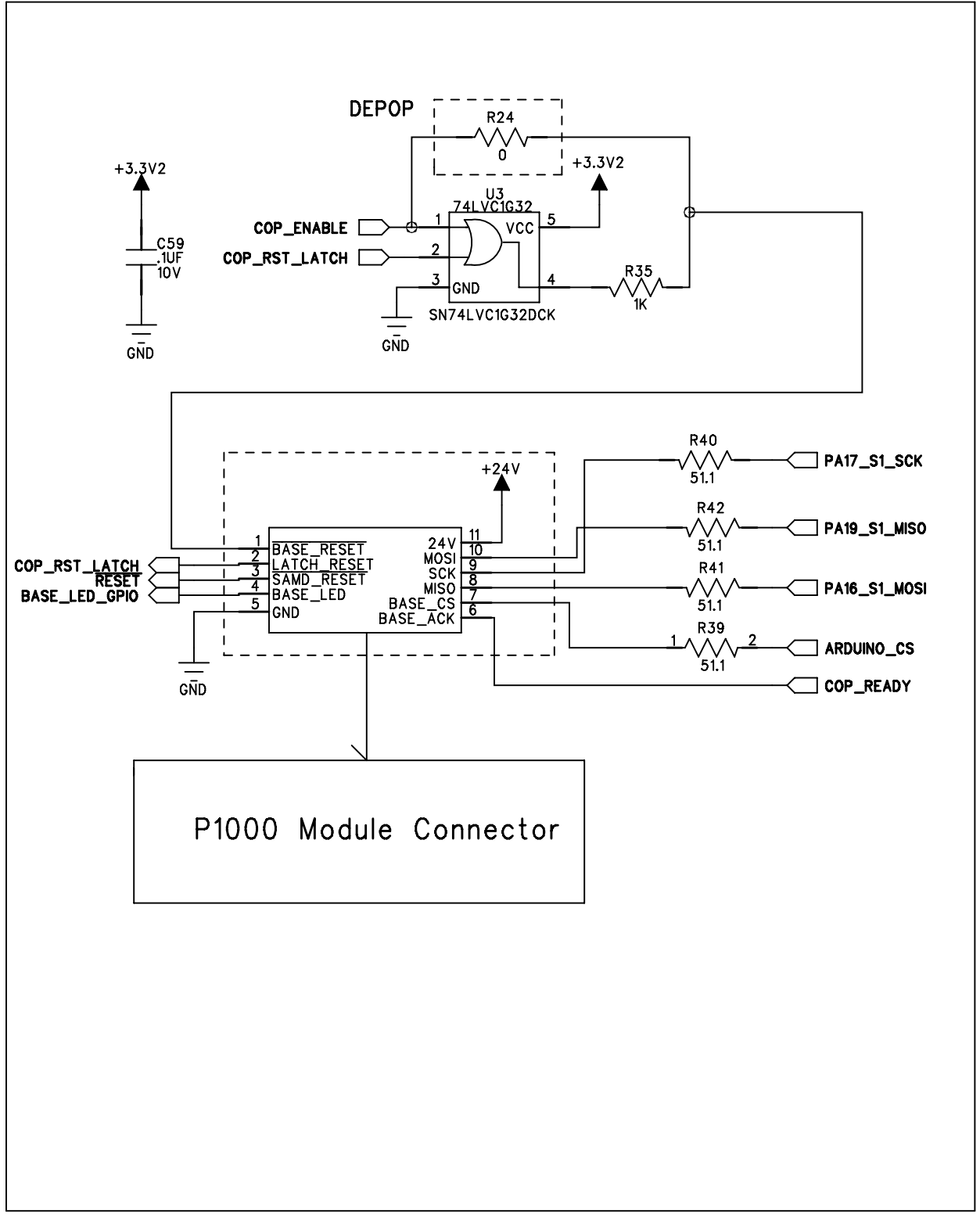
Internal Devices



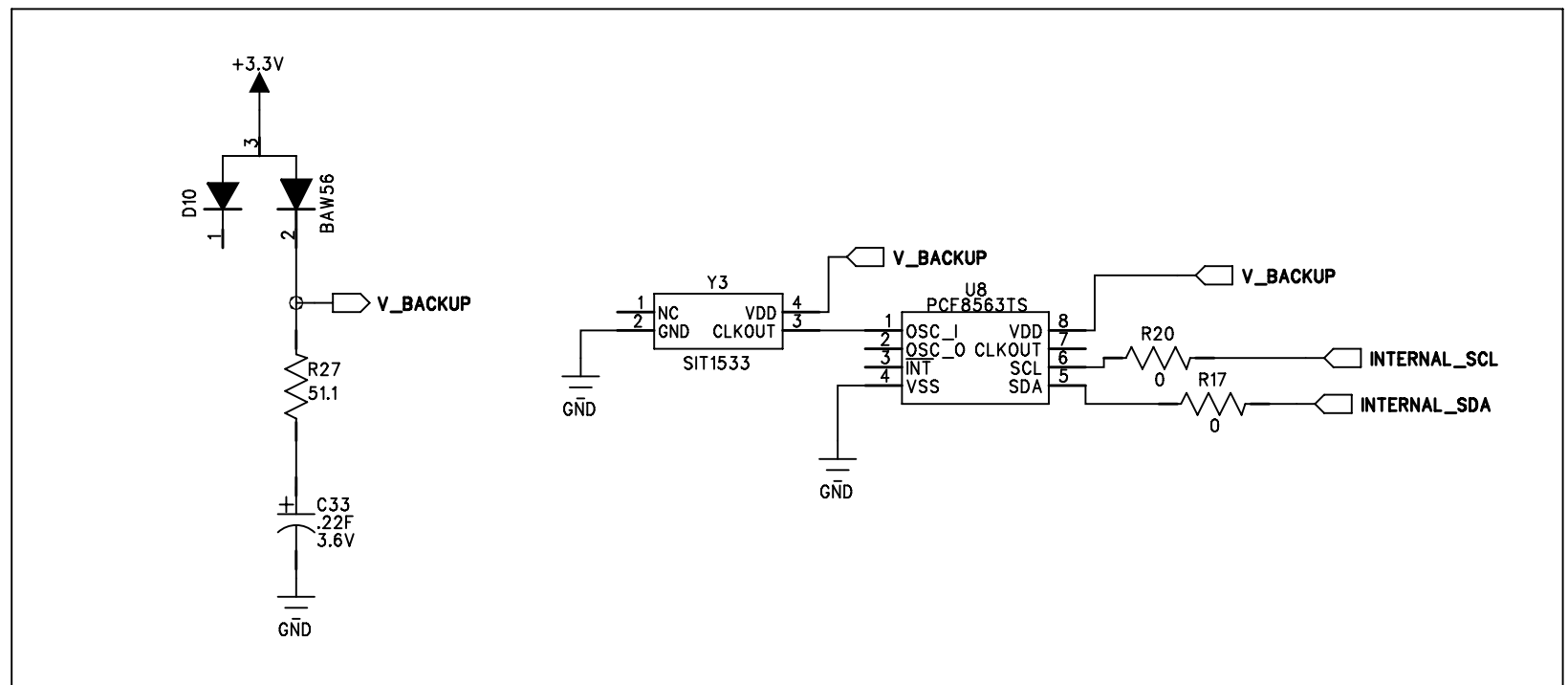
SAMD51 Decouple and Program



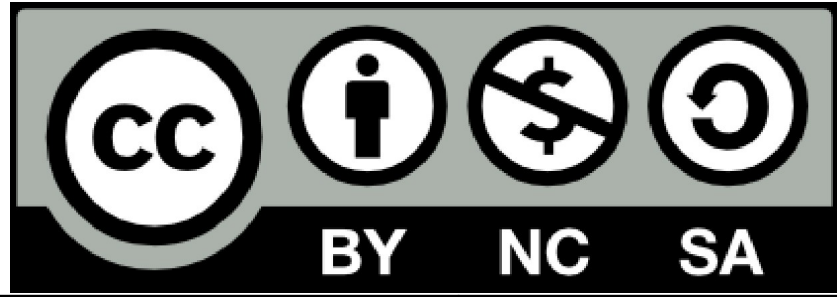
Base Controller and Supporting Circuitry



RTC + Supercap



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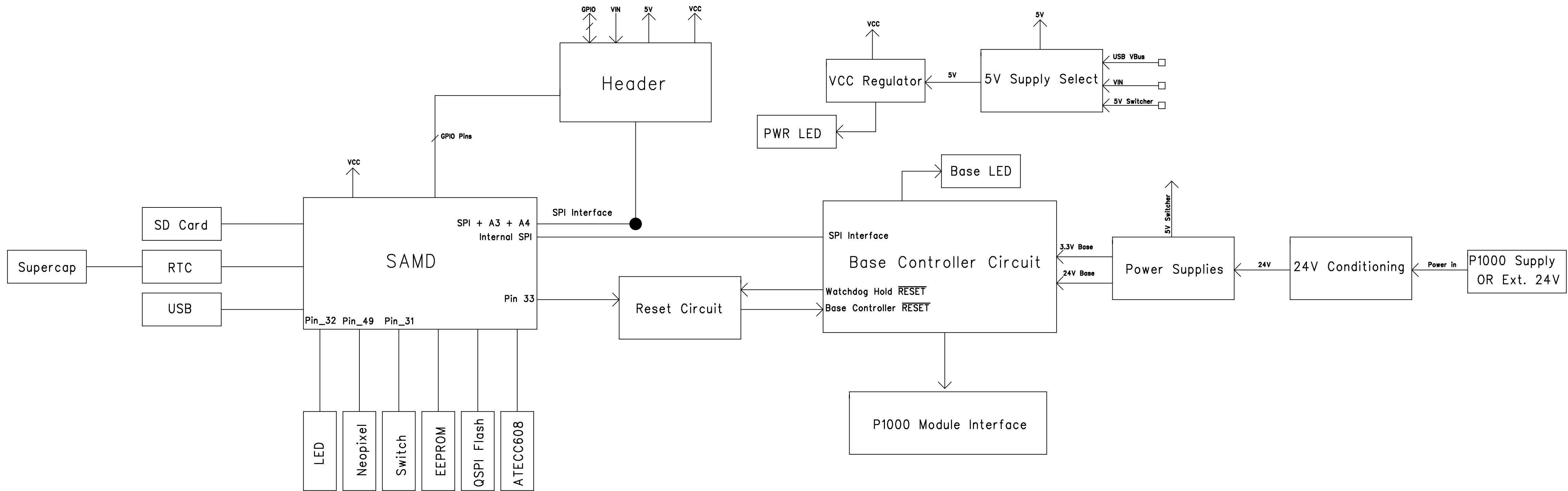


		ORIGINATOR: A. CUMMICK	DATE:	Arduino Compatible P1 CPU				
		REVISED:	DATE:					
		REVISED:	DATE:					
NEXT ASSY	USED ON	REVISED:	DATE:	SIZE: D	PRODUCT CODE: P1AM-200	DRAWING NO:	REV: A	
APPLICATION		CHECKED:	DATE:	SCALE: NONE			SHEET:	

COMPANY: **FACTS engineering** 8049 Photonics Drive
New Port Richey, FL 34655

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		A. CUMMICK	DATE:
		REVISOR:	DATE:
		REVISOR:	DATE:
		CHECKED:	DATE:

COMPANY: FACTS engineering		8049 Photonics Drive New Port Richey, FL 34655	
TITLE:		Arduino Compatible P1 CPU	
SIZE: D	PRODUCT CODE:	DRAWING NO:	REV: A
SCALE: NONE	SHEET: 1 OF 2		