

74LS02

Quad 2-Input NOR Gate

General Description

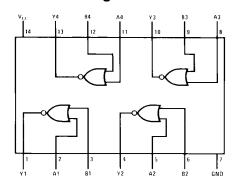
This device contains four independent gates each of which performs the logic NOR function.

Ordering Code:

Order Number	Number Package Number Package Description		
DM74LS02M	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-120, 0.150 Narrow	
DM74LS02SJ	M14D	14-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide	
DM74LS02N	N14A	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide	

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

Connection Diagram



Function Table

Inputs		Output		
Α	В	Y		
L	L	Н		
L	Н	L		
н	L	L		
Н	н	L		

 $Y = \overline{A + B}$

H = HIGH Logic Level L = LOW Logic Level



Absolute Maximum Ratings(Note 1)

Supply Voltage 7V Input Voltage 7V Operating Free Air Temperature Range $0^{\circ}\text{C to } +70^{\circ}\text{C}$

Operating Free Air Temperature Range $0^{\circ}\text{C to } +70^{\circ}\text{C}$ Storage Temperature Range $-65^{\circ}\text{C to } +150^{\circ}\text{C}$

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	Min	Nom	Max	Units
V _{CC}	Supply Voltage	4.75	5	5.25	V
V _{IH}	HIGH Level Input Voltage	2			V
V _{IL}	LOW Level Input Voltage			0.8	V
I _{OH}	HIGH Level Output Current			-0.4	mA
I _{OL}	LOW Level Output Current			8	mA
T _A	Free Air Operating Temperature	0		70	°C

Electrical Characteristics

over recommended operating free air temperature range (unless otherwise noted)

V _{OH} HIGH Level V _{CC} = Min, I _{OH} = Max, 2, 7, 3, 4	-1.5	V
		٧
Output Voltage $V_{IL} = Max$		
	0.5	٧
$I_{OL} = 4 \text{ mA}, V_{CC} = \text{Min}$ 0.25	0.4	
I _I Input Current @ Max Input Voltage V _{CC} = Max, V _I = 7V	0.1	mA
I _{IH} HIGH Level Input Current V _{CC} = Max, V _I = 2.7V	20	μА
$I_{\rm IL}$ LOW Level Input Current $V_{\rm CC}$ = Max, $V_{\rm I}$ = 0.4V	-0.40	mA
I _{OS} Short Circuit Output Current V _{CC} = Max (Note 3) −20 −20	-100	mA
I _{CCH} Supply Current with Outputs HIGH V _{CC} = Max 1.6	3.2	mA
I _{CCL} Supply Current with Outputs LOW V _{CC} = Max 2.8	5.4	mA

Note 2: All typicals are at $V_{CC} = 5V$, $T_A = 25$ °C.

Note 3: Not more than one output should be shorted at a time, and the duration should not exceed one second.

Switching Characteristics

at $V_{CC} = 5V$ and $T_A = 25^{\circ}C$

	Parameter	$R_L = 2 k\Omega$				
Symbol		C _L = 15 pF		C _L = 50 pF		Units
		Min	Max	Min	Max	
	Propagation Delay Time		13		18	ns
	LOW-to-HIGH Level Output					
	Propagation Delay Time		10		15	nc
	HIGH-to-LOW Level Output		'0		15	ns