

# 74LS08 Quad 2-Input AND Gates

### **General Description**

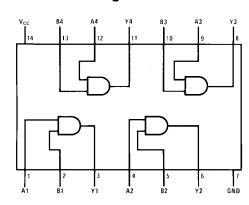
This device contains four independent gates each of which performs the logic AND function.

## **Ordering Code:**

Order Number	Package Number	Package Description			
DM74LS08M	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-120, 0.150 Narrow			
DM74LS08SJ	M14D	14-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide			
DM74LS08N	N14A	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide			

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

### **Connection Diagram**



### **Function Table**

In	Output	
Α	В	Y
L	L	L
L	н	L
н	L	L
н	Н	Н

 $\mathbf{Y} = \mathbf{A}\mathbf{B}$ 

H = HIGH Logic Level L = LOW Logic Level



# S08 147MC

### **Absolute Maximum Ratings**(Note 1)

Supply Voltage 7V Input Voltage 7V Operating Free Air Temperature Range 0°C to +70°C

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

### **Recommended Operating Conditions**

Symbol	Parameter	Min	Nom	Max	Units
V <sub>CC</sub>	Supply Voltage	4.75	5	5.25	٧
V <sub>IH</sub>	HIGH Level Input Voltage	2			V
V <sub>IL</sub>	LOW Level Input Voltage			0.8	٧
I <sub>OH</sub>	HIGH Level Output Current			-0.4	mA
I <sub>OL</sub>	LOW Level Output Current			8	mA
T <sub>A</sub>	Free Air Operating Temperature	0		70	°C

-65°C to +150°C

#### **Electrical Characteristics**

Storage Temperature Range

over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ (Note 2)	Max	Units
VI	Input Clamp Voltage	V <sub>CC</sub> = Min, I <sub>I</sub> = -18 mA			-1.5	٧
V <sub>OH</sub>	HIGH Level	V <sub>CC</sub> = Min, I <sub>OH</sub> = Max,	2.7	3.4		V
	Output Voltage	V <sub>IH</sub> = Min				
V <sub>OL</sub>	LOW Level	V <sub>CC</sub> = Min, I <sub>OL</sub> = Max,		0.35	0.5	
	Output Voltage	V <sub>IL</sub> = Max				V
		$I_{OL} = 4 \text{ mA}, V_{CC} = \text{Min}$		0.25	0.4	
l <sub>l</sub>	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 7V$			0.1	mA
I <sub>IH</sub>	HIGH Level Input Current	$V_{CC} = Max, V_I = 2.7V$			20	μΑ
I <sub>IL</sub>	LOW Level Input Current	$V_{CC} = Max, V_I = 0.4V$			-0.36	mA
Ios	Short Circuit Output Current	V <sub>CC</sub> = Max (Note 3)	-20		-100	mA
ссн	Supply Current with Outputs HIGH	V <sub>CC</sub> = Max		2.4	4.8	mA
I <sub>CCL</sub>	Supply Current with Outputs LOW	V <sub>CC</sub> = Max		4.4	8.8	mA

### **Switching Characteristics**

at  $V_{CC}$  = 5V and  $T_A$  = 25°C

	Parameter	$R_L = 2 k\Omega$				
Symbol		C <sub>L</sub> = 15 pF		C <sub>L</sub> = 50 pF		Units
		Min	Max	Min	Max	
t <sub>PLH</sub>	Propagation Delay Time LOW-to-HIGH Level Output	4	13	6	18	ns
t <sub>PHL</sub>	Propagation Delay Time HIGH-to-LOW Level Output	3	11	5	18	ns

Note 2: All typicals are at  $V_{CC} = 5V$ ,  $T_A = 25$ °C.

Note 3: Not more than one output should be shorted at a time, and the duration should not exceed one second.