Sheet1

Qword (6	4) Dword (3	2) Word (16)	Byte (8)		uses	calle saved
			low	high		
rax	eax	ax	al	ah	return val	no
rbx	ebx	bx	bl	bh	-	yes
rcx	ecx	CX	cl	ch	4 th func arg	no
rdx	edx	dx	dl	dh	3 rd func arg	no
rsi	esi	si	sil		2 nd func arg	no
rdi	edi	di	dil		1st func arg	no
r8	r8d	r8w	r8b		5 th func arg	no
r9	r9d	r9w	r9b		6 th func arg	no
r10	r10d	r10w	r10b		-	no
r11	r11d	r11w	r11b		-	no
r12	r12d	r12w	r12b		-	yes
r13	r13d	r13w	r13b		-	yes
r14	r14d	r14w	r14b		=	yes
r15	r15d	r15w	r15b		-	yes
rsp	esp	sp	spl		stack pointer	
rbp	ebp	bp	bpl		base pointer	
rip	eip	ip			instruction pointe	er
rflags	eflags				Flags & cond	

			SIMD			
avx 512	ymm	xmm	mmx	uses	uses (mmx)	
zmm0	ymm0	xmm0	mmx0	1st arg, or ret	Integer / fixed point	
zmm1	ymm1	xmm1	mmx1	2 nd arg	arg / general	
zmm2	ymm2	xmm2	mmx2	3 rd arg	arg / general	
zmm3	ymm3	xmm3	mmx3	4 th arg	arg / general	
zmm4	ymm4	xmm4	mmx4	5 th arg	arg / general	
zmm5	ymm5	xmm5	mmx5	6 th arg	arg / general	
zmm6	ymm6	xmm6	mmx6	7 th arg	arg / general	
zmm7	ymm7	xmm7	mmx7	8 th arg	arg / general	
zmm8	ymm8	xmm8		general use		
zmm9	ymm9	xmm9		general use		
zmm10	ymm10	xmm10	general use			
zmm11	ymm11	xmm11	general use			
zmm12	ymm12	xmm12	general use			
zmm13	ymm13	xmm13	general use			
zmm14	ymm14	xmm14	general use			
zmm15	ymm15	xmm15	general use			

eflags register

