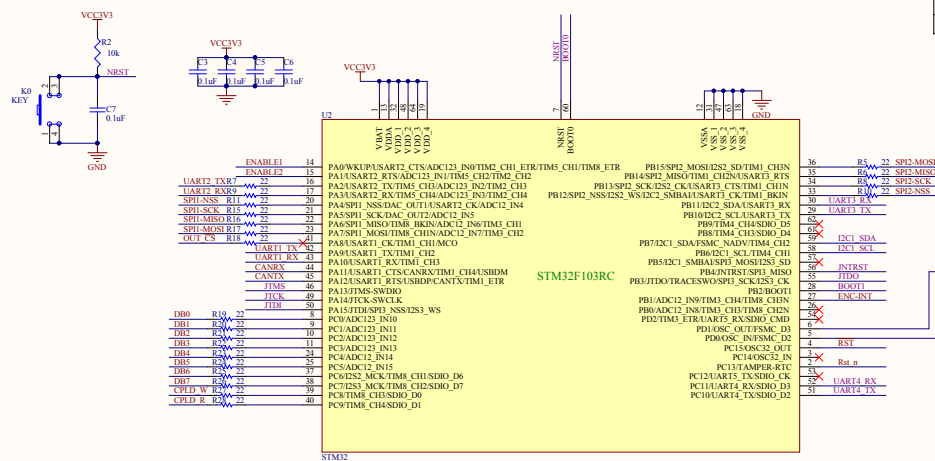
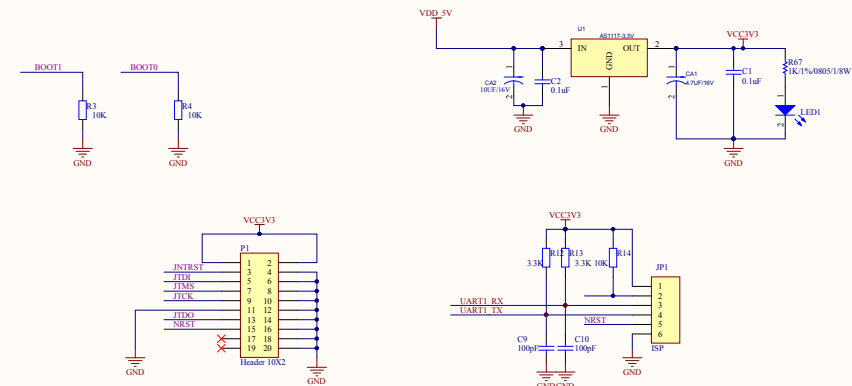


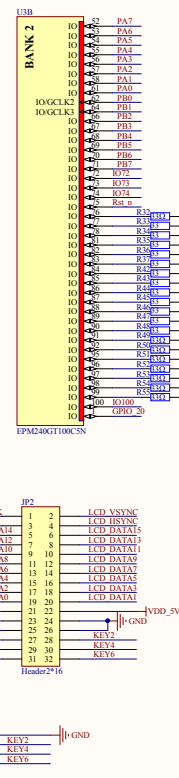
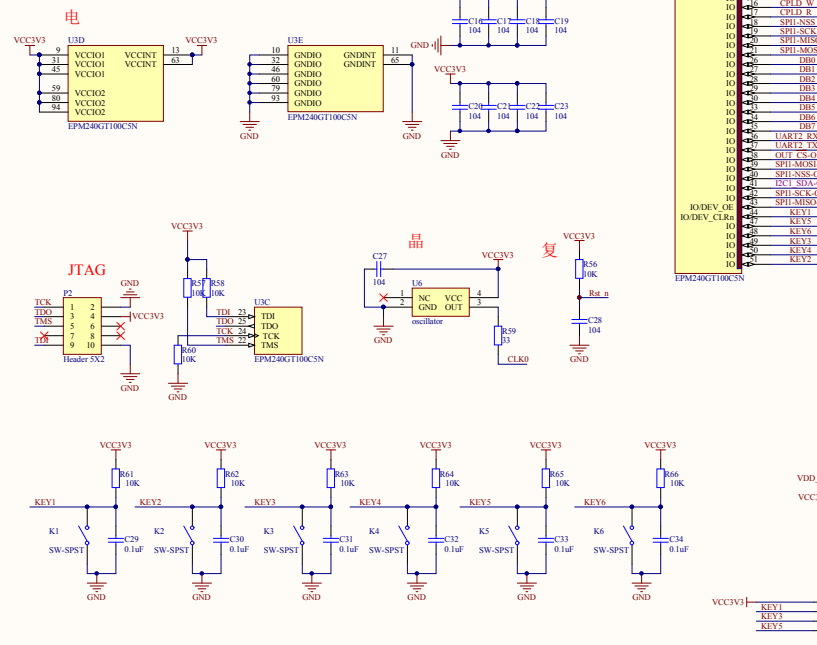
CPU



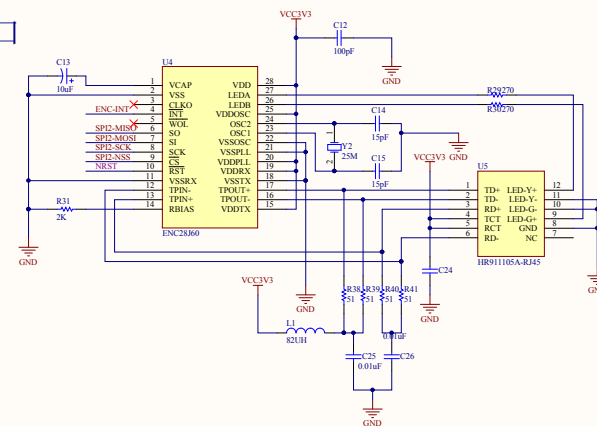
BOOT0	BOOT1	
X	0	FLASH
0	1	ISP
1	1	SRAM



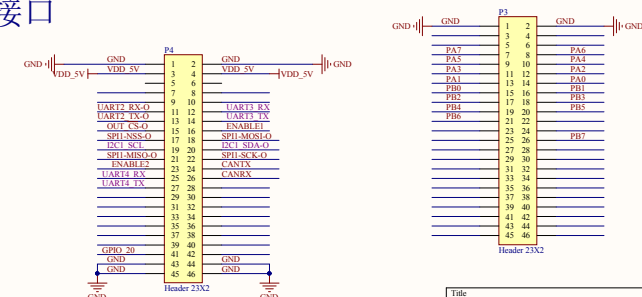
CPLD



网线接口



排针接口



Title		
Size A2	Number	Revision 4.0
Date: 2014/10/28	Sheet of Drawing: 1004-106, 2, 6, 1, 10, 175 Mill Br S	Download

