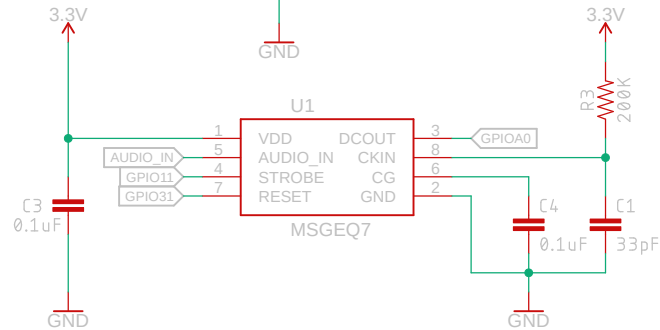


The diagram illustrates a 3.3V audio input buffer circuit. It features two input pins, J1 and J2, which are connected to a network of resistors R1 and R2 (both 22K) and a capacitor C2 (0.01uF). The network is powered by a 3.3V supply and connected to an AUDIO_IN pin.



Pin diagram of the ATmega328P microcontroller. The diagram shows a 28-pin package with pins numbered 1 to 28. Pins 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16 are on the left. Pins 17, 18, 19, 20, 21, 22, 23, 24, 25, 26, 27, 28 are on the right. The pins are labeled: 1: RESET, 2: 3V, 3: AREF, 4: GND, 5: GPIOA0, 6: GPIOA1, 7: GPIOA2, 8: GPIOA3, 9: GPIOA4, 10: GPIOA5, 11: GPIOACK, 12: GPIOMOSI, 13: GPIOMISO, 14: GPIOXRX, 15: GPIOXTX, 16: NC, 17: GPIOA4, 18: GPIOA5, 19: GPIOA6, 20: GPIOA7, 21: GPIOA8, 22: GPIOA9, 23: GPIOA10, 24: GPIOA11, 25: USB, 26: EN, 27: VBAT, 28: GPIOA12. The diagram also shows connections for 3.3V, 5V, GND, and GPIOA0.

Sheet: 1/1