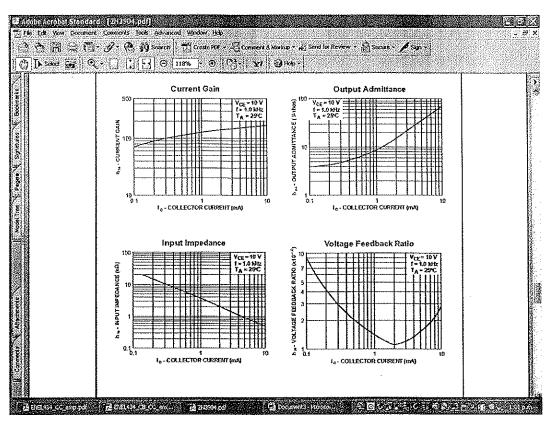
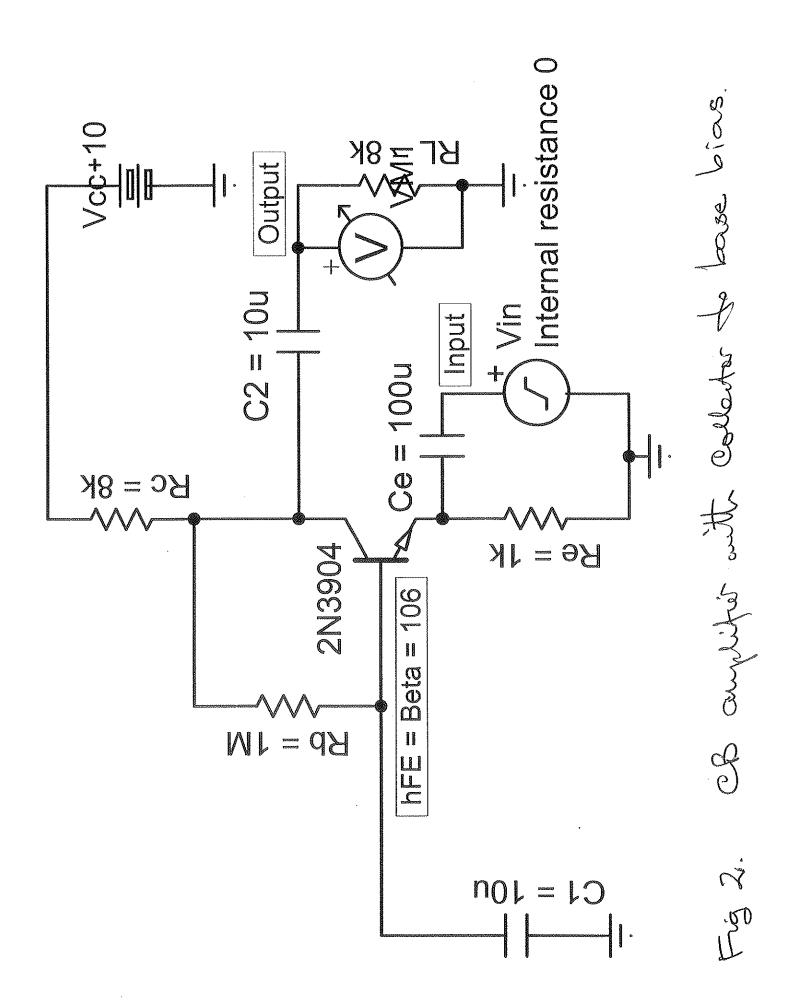
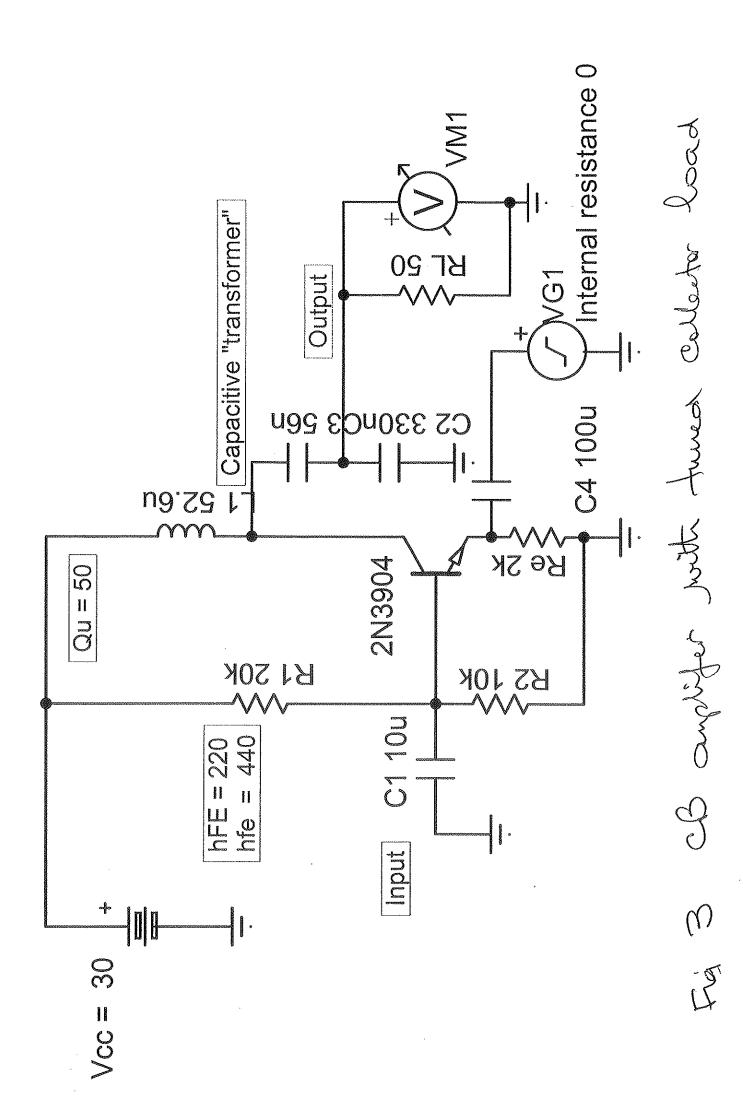
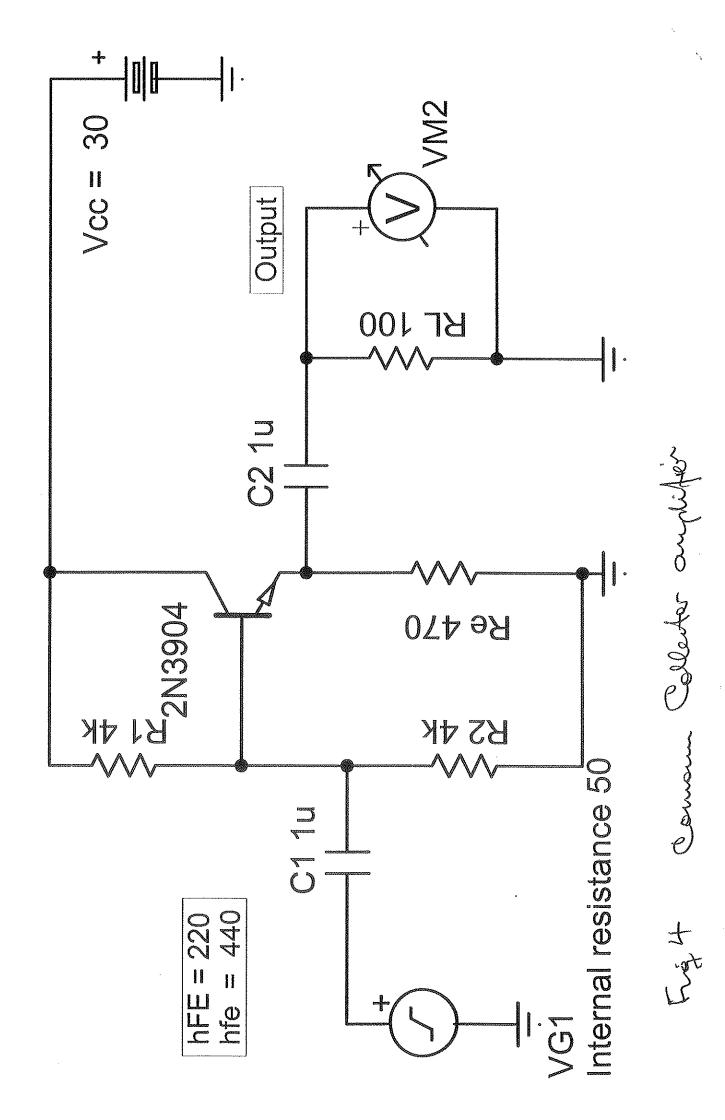
## NEL 434 Electronics II



2N3904 H parambers.







Kerrien step by step design O Fria approse So [w.] 2 Disent Rooil (eg Rp.). (3) (4) Change // to series. (i) Collect to getter (in () all the resistances (Check hoe) (5) Frid peak gan at for Colub for chasent changes ) 55 gain vs large signal gam. CE VS VB. Marie Carlo 1984. BJT. Para stapping Temp.

Review Juality factor 0 Re Xie fi ¥ 30B go BW - > Perhán Œ

Su = 50 Not realistic but weth convenint Qu = Junloaded. Reclister.

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