

LUCAS ELISEI

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EDUCATION

SEPT 2018 to JUNE 2020	Master of Science in Engineering, Embedded Systems, HES-SO , Lausanne
SEPT 2019 to FEB 2020	Master thesis: “High Performance Overlays for FPGA”
FEB 2019 to JUNE 2019	Semester project: “FPGA-based Accelerator for Machine Learning Inference”
SEPT 2015 to JULY 2018	Bachelor of Engineering in Embedded Systems, HEIG-VD , Yverdon Thesis: “Just-in-Time Recompilation and Optimization of Compiled Binaries”
SEPT 2013 to JULY 2015	Propedeutics, Swiss Federal Institute of Technology (EPFL) , Lausanne
JUNE 2013	Scientific <i>Baccalauréat</i> (mathematics speciality), mention “Bien” with physics taught in English.

PROJECTS

STEREO	Flutter library for playing music from a file.
COMMUSICA	Java application for sharing music during an event.
GOODGES	iOS extension for jailbroken devices. Radically changes the way notification badges are displayed on the homescreen.
IMHOF	Draw topographic map of cities from OpenStreetMap data.
PAGERANK	Recommendation algorithm.

WORK EXPERIENCE

- SUMMER 2019 Summer Internship at REDS INSTITUTE, Yverdon
Deployment of a Deep Neural Network model on FPGA in a Software-Defined Radio system.
- SUMMER 2017 Interim at NESTLÉ, Orbe
Quality controls on Dolce Gusto products.
- SUMMER 2016 Summer Internship at ITECOR, Vevey
Development of a mobile application for iOS, Android and Windows platforms. Acknowledged for my professionalism, my integration in a international environment and my English communication skills.
- SUMMER 2015 Summer Internship at Y-PARC, Yverdon
Development of a web application whose aim is to create and manage multiple-choice tests. Acknowledged for my positive attitude, my engagement and my organization.
- FEB 2015 Interim at NESTLÉ, Orbe
Life-cycle tests on Nescafé *Dolce Gusto* machines for sale.
- SUMMER 2013 Interim at NESTLÉ, Orbe
Quality controls on Nescafé and Nespresso products.

LANGUAGES

FRENCH	Mothertongue
ITALIAN	Mothertongue
ENGLISH	Fluent (C1 Level)

SKILLS

Computer skills

Basic	EXCEL, POWERPOINT, JAVASCRIPT
Intermediate	ASSEMBLY, C++, JAVA, L ^A T _E X, SYSTEMVERILOG, QUARTUS, VIVADO, PYTHON
Advanced	C, DART, GIT, SOC/FPGA, VHDL

Soft skills Autonomous, ability to adapt, organized and quick learner.

INTERESTS AND ACTIVITIES

Technology, Open-Source, Programming
Music, Travelling