

SAMIT BARAI

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ACADEMIC QUALIFICATIONS :

Year	Degree/Certificate	Institute / School , City	Year / Standard	CGPA/Percentage
2007 - 2011	B-Tech, Electronics and Electrical Communications Engineering.	Indian Institute of Technology, Kharagpur.	4 th year of Undergraduate course	7.62/10 at the end of 6 semesters.
2007	West Bengal Council of Higher Secondary Education.	Hijli High School, Kharagpur.	XII (High School Finals)	82.8 %
2005	West Bengal Board of Secondary Education.	Hijli High School, Kharagpur.	X (School Finals)	91.5 %

INTERNSHIP :

UNIVERSITY OF SHERBROOKE, CANADA	SUMMER INTERNSHIP May'10 – July'10
	<ul style="list-style-type: none">Designed an Analog Chip which would act as a Magnetic Sensor (Using the principle of Hall Effect) using 350nm CMOS technology in Cadence.Prior to the design of the Magnetic chip, the Hall voltage was measured using an OPAMP, which was designed using the specifications of the sensor chip for efficient measurement.The designs were approved for fabrication by the Canadian Micro-electronics Corporation (CMC), and is under process for fabrication.

PROJECTS :

IIT KHARAGPUR	B.TECH PROJECT August'10 - present
	<ul style="list-style-type: none">Presently working on the device characterization of Optoelectronic HEMT in ATLAS (SILVACO) under the guidance of Dr. Dhruves Biswas , IIT Kharagpur.Did a considerable progress on the learning of Athina needed for the device characterization.
RRCAT INDORE	SUMMER COURSE May'08 – June'08
	<ul style="list-style-type: none">Did a Summer course on VLSI technology working in analog artist environment CADENCE in designing of an OPAMP using 180nm CMOS technology.Awarded a "Certificate of excellence" by Advanced VLSI Design Laboratory, IIT Kharagpur.
	WINTER PROJECT December'09
	<ul style="list-style-type: none">Did a comparative study of Semiconductor LASER diode and its manufacturing and testing at RRCAT, Indore under the guidance of Dr. Suparna Pal.Gathered experience in Clean rooms and had exposure to actual fabrication of the LASER.

KEY ACADEMIC ACHIEVEMENTS :

- Secured All India Rank of 3896 from among 320,000 candidates from all over India in **IIT JEE 2007**.
- Was among the 100 selected candidates from all over India for the **Indian Statistical Institute, Kolkata, 2007**.
- Secured an overall rank of 395 in West Bengal Joint Entrance Examination (**WBJEE 2007**).
- Topped in standards X and XI and awarded the '**Best Student**' in school for 2 consecutive years 2006-2007.
- District 3rd in 10th board (WBBSE) exams in 2005 and awarded "**Outstanding Student of the year**" in school.

EXTRA ACADEMIC ACTIVITIES :

CULTURAL ACTIVITIES	<ul style="list-style-type: none">Represented the Rajendra Prasad Hall of residence in Inter-Hall Sketching event in 2009.Secured 1st position in Intra-Hall Freshers Sketching event in 2008.Member of the "Design Team" for the event Illumination of the Hall in 2008 and 2009.Member of the fine arts society of IIT Kharagpur (SPECTRA).Completed a course (up to 5th year with distinction in 3rd and 4th year) in Painting under Pracheen Kala Kendra, Chandigarh.
SOCIAL AND COMMUNITY WORK	<ul style="list-style-type: none">Member of the Gopali Youth Welfare Society (GYWS), a West Bengal Based NGO aimed at ensuring socio-economic welfare for underprivileged and weaker sections of the society.Volunteered in the "Study programme" for underprivileged young aspirants of JEE under National Service Scheme (NSS) in 2008.Successfully completed Annual Training Camp of National Cadet Corps, 3 Bengal Air SQN. at Pingla (Dec '07).Volunteered in Spring Fest '08, the annual Cultural Fest of IIT Kharagpur.
COMPUTER SKILLS	<ul style="list-style-type: none">Operating systems : Windows, Linux (Fedora, Ubuntu, RedHat), SOLARIS.Programming Languages known : C/C++ , HTML, CSS, Javascript (preliminary), Verilog, 8051.Software / Simulation tools : Cadence, MATLAB, Xilinx-ISE, SIVACO package, PSPICE, AUTOCAD.