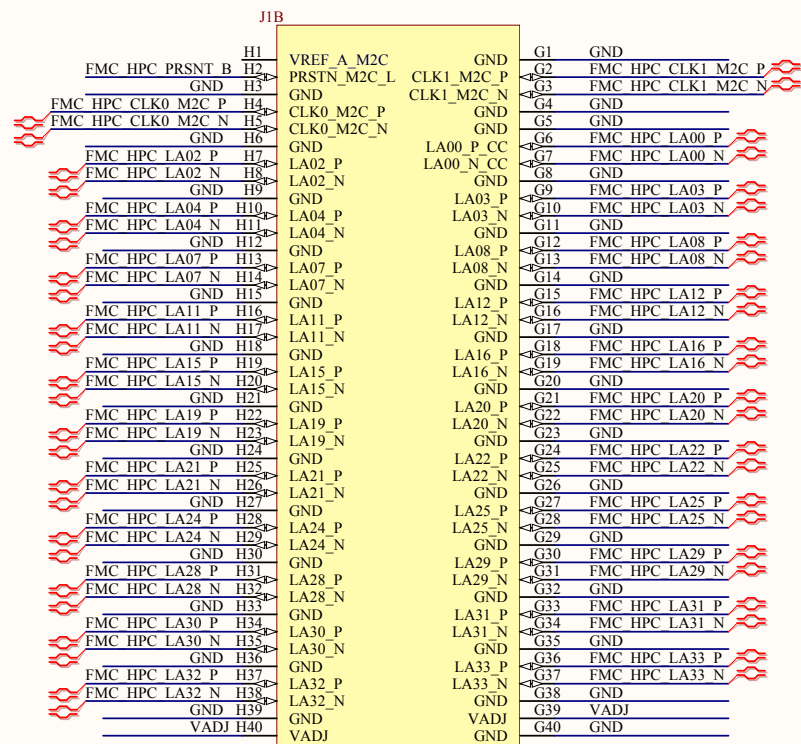


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Doc#

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Engineer EG

Author DL

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J1D

PGOOD_ALL	D1	PG_C2M	GND	C1	GND
GND	D2	GND	DP0_C2M_P	C2	GND
GND	D3	GND	DP0_C2M_N	C3	GND
	D4	GBT_CLK0_M2C_P	GND	C4	GND
	D5	GBT_CLK0_M2C_N	GND	C5	GND
GND	D6	GND	DP0_M2C_P	C6	GND
GND	D7	GND	DP0_M2C_N	C7	GND
FMC_HPC_LA01_P	D8	LA01_P_CC	GND	C8	GND
FMC_HPC_LA01_N	D9	LA01_N_CC	GND	C9	GND
GND	D10	GND	LA06_P	C10	FMC_HPC_LA06_P
FMC_HPC_LA05_P	D11	LA05_P	LA06_N	C11	FMC_HPC_LA06_N
FMC_HPC_LA05_N	D12	LA05_N	GND	C12	GND
GND	D13	GND	LA10_P	C13	GND
FMC_HPC_LA09_P	D14	LA09_P	LA10_P	C14	FMC_HPC_LA10_P
FMC_HPC_LA09_N	D15	LA09_N	LA10_N	C15	FMC_HPC_LA10_N
GND	D16	GND	GND	C16	GND
FMC_HPC_LA13_P	D17	LA13_P	GND	C17	GND
FMC_HPC_LA13_N	D18	LA13_N	LA14_P	C18	FMC_HPC_LA14_P
GND	D19	GND	LA14_N	C19	FMC_HPC_LA14_N
FMC_HPC_LA17_P	D20	LA17_P_CC	GND	C20	GND
FMC_HPC_LA17_N	D21	LA17_N_CC	GND	C21	GND
GND	D22	GND	LA18_P_CC	C22	FMC_HPC_LA18_P
FMC_HPC_LA23_P	D23	LA23_P	LA18_N_CC	C23	FMC_HPC_LA18_N
FMC_HPC_LA23_N	D24	LA23_N	GND	C24	GND
GND	D25	GND	GND	C25	GND
FMC_HPC_LA26_P	D26	LA26_P	GND	C26	FMC_HPC_LA27_P
FMC_HPC_LA26_N	D27	LA26_N	LA27_P	C27	FMC_HPC_LA27_N
GND	D28	GND	LA27_N	C28	GND
FMC_HPC_TCK	D29	TCK	GND	C29	GND
TDO-FPGA/TDI_FMC	D30	TDI	SCL	C30	FMC_HPC_SCL
FMC_HPC_TDO	D31	TDO	SDA	C31	FMC_HPC_SDA
VCC3V3	D32	3P3VAUX	GND	C32	GND
TMS	D33	TMS	GND	C33	GND
GND	D34	TRST_L	GA0	C34	GND
VCC3V3	D35	GA1	12P0V	C35	VCC12V0
GND	D36	3P3V	GND	C36	GND
VCC3V3	D37	GND	12P0V	C37	VCC12V0
GND	D38	3P3V	GND	C38	GND
VCC3V3	D39	GND	3P3V	C39	VCC3V3
VCC3V3	D40	3P3V	GND	C40	GND

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GND	B1	CLK_DIR	GND	A1	GND
GND	B2	GND	DP1_M2C_P	A2	GND
GND	B3	GND	DP1_M2C_N	A3	GND
	B4	DP9_M2C_P	GND	A4	GND
	B5	DP9_M2C_N	GND	A5	GND
GND	B6	GND	DP2_M2C_P	A6	GND
GND	B7	GND	DP2_M2C_N	A7	GND
	B8	DP8_M2C_P	GND	A8	GND
	B9	DP8_M2C_N	GND	A9	GND
GND	B10	GND	DP3_M2C_P	A10	GND
GND	B11	GND	DP3_M2C_N	A11	GND
	B12	DP7_M2C_P	GND	A12	GND
	B13	DP7_M2C_N	GND	A13	GND
GND	B14	GND	DP4_M2C_P	A14	GND
GND	B15	GND	DP4_M2C_N	A15	GND
	B16	DP6_M2C_P	GND	A16	GND
	B17	DP6_M2C_N	GND	A17	GND
GND	B18	GND	DP5_M2C_P	A18	GND
GND	B19	GND	DP5_M2C_N	A19	GND
	B20	GBTCLK1_M2C_P	GND	A20	GND
	B21	GBTCLK1_M2C_N	GND	A21	GND
GND	B22	GND	DP1_C2M_P	A22	GND
GND	B23	GND	DP1_C2M_N	A23	GND
	B24	DP9_C2M_P	GND	A24	GND
	B25	DP9_C2M_N	GND	A25	GND
GND	B26	GND	DP2_C2M_P	A26	GND
GND	B27	GND	DP2_C2M_N	A27	GND
	B28	DP8_C2M_P	GND	A28	GND
	B29	DP8_C2M_N	GND	A29	GND
GND	B30	GND	DP3_C2M_P	A30	GND
GND	B31	GND	DP3_C2M_N	A31	GND
	B32	DP7_C2M_P	GND	A32	GND
	B33	DP7_C2M_N	GND	A33	GND
GND	B34	GND	DP4_C2M_P	A34	GND
GND	B35	GND	DP4_C2M_N	A35	GND
	B36	DP6_C2M_P	GND	A36	GND
	B37	DP6_C2M_N	GND	A37	GND
GND	B38	GND	DP5_C2M_P	A38	GND
GND	B39	GND	DP5_C2M_N	A39	GND
	B40	RES0	GND	A40	GND

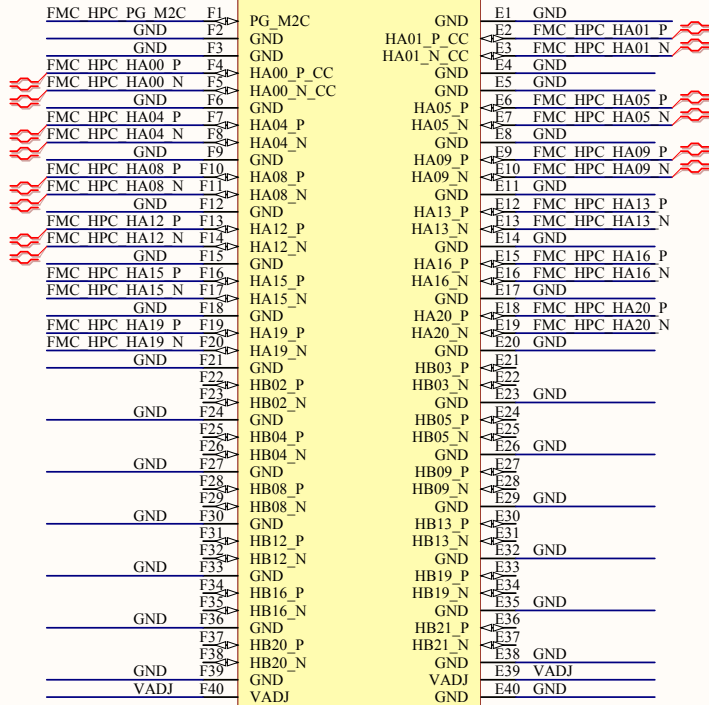
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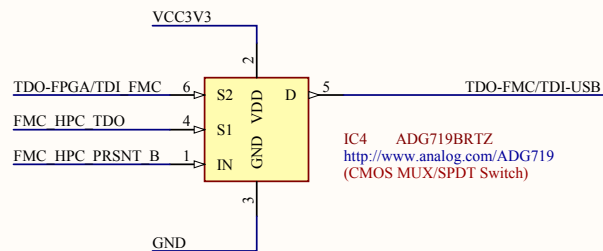
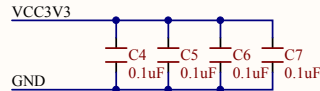
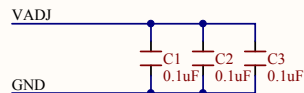
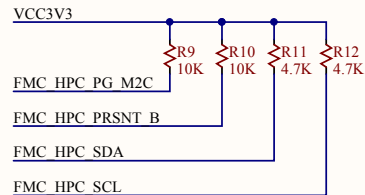
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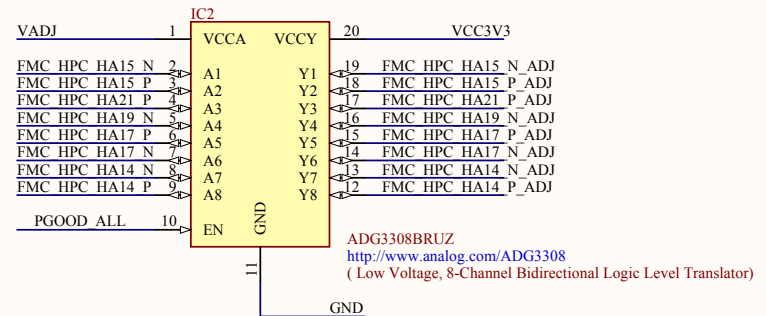
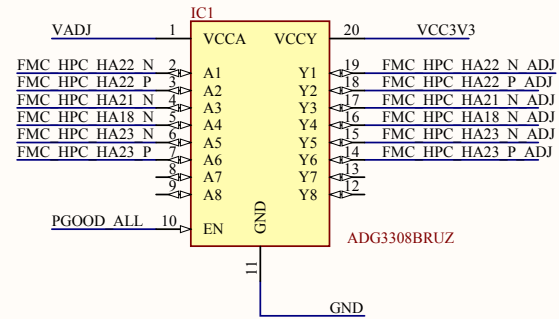
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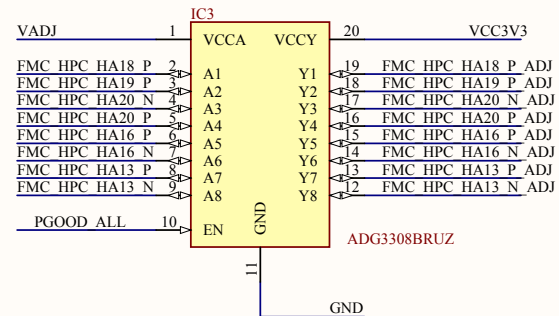
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IC4 ADG719BRTZ  
<http://www.analog.com/ADG719>  
 (CMOS MUX/SPDT Switch)



<http://www.analog.com/ADG3308>  
 ( Low Voltage, 8-Channel Bidirectional Logic Level Translator)



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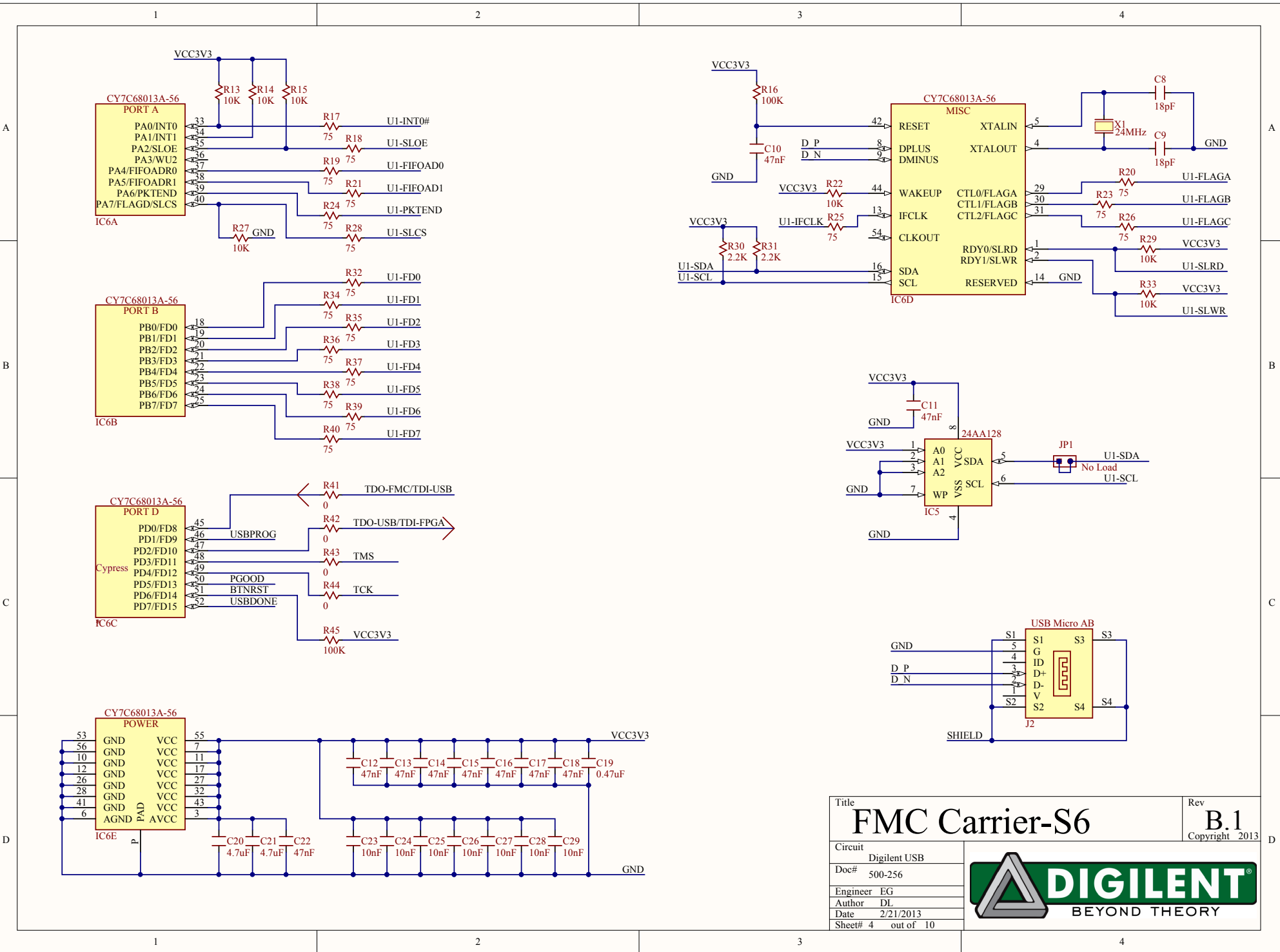
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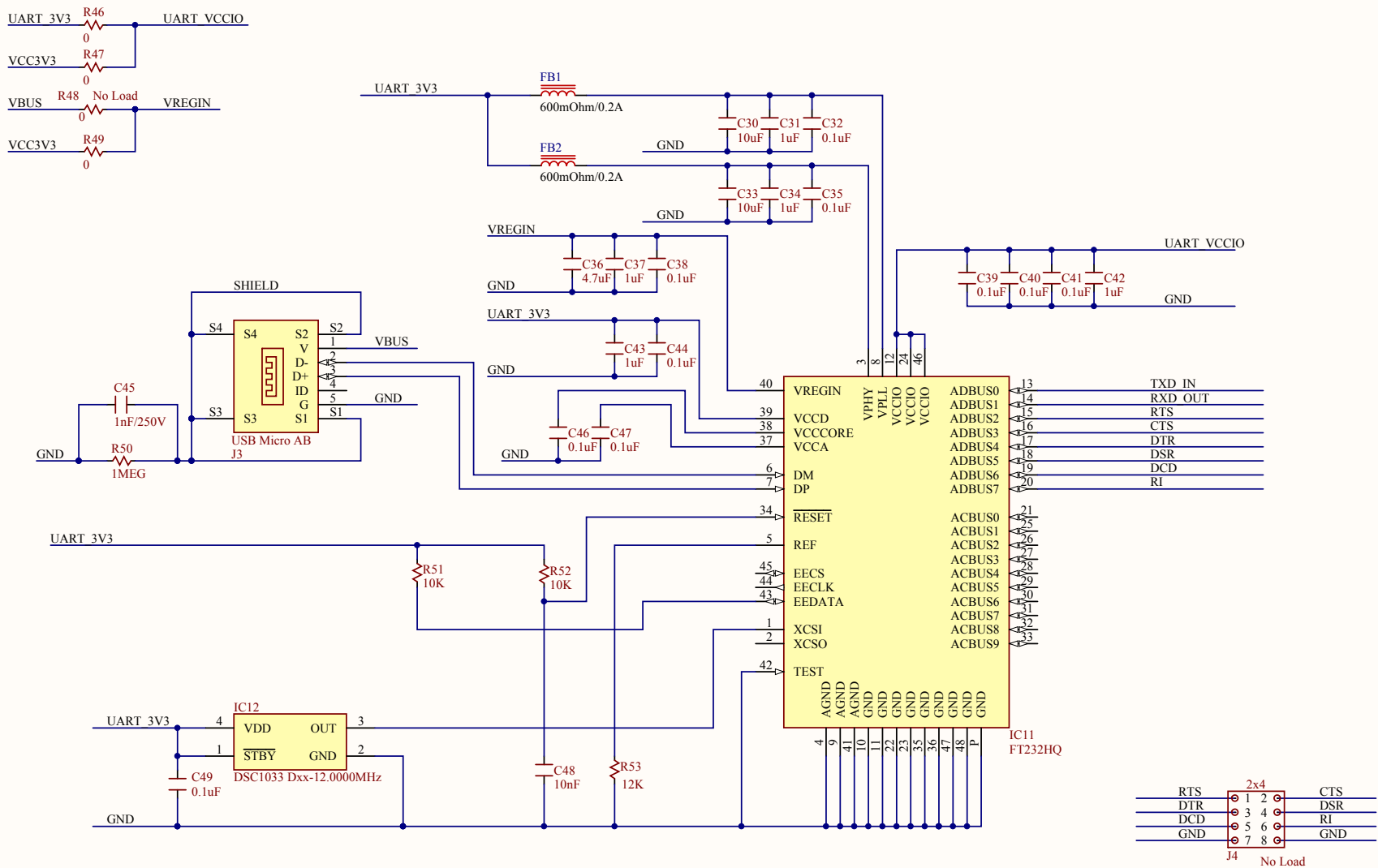
Author DL

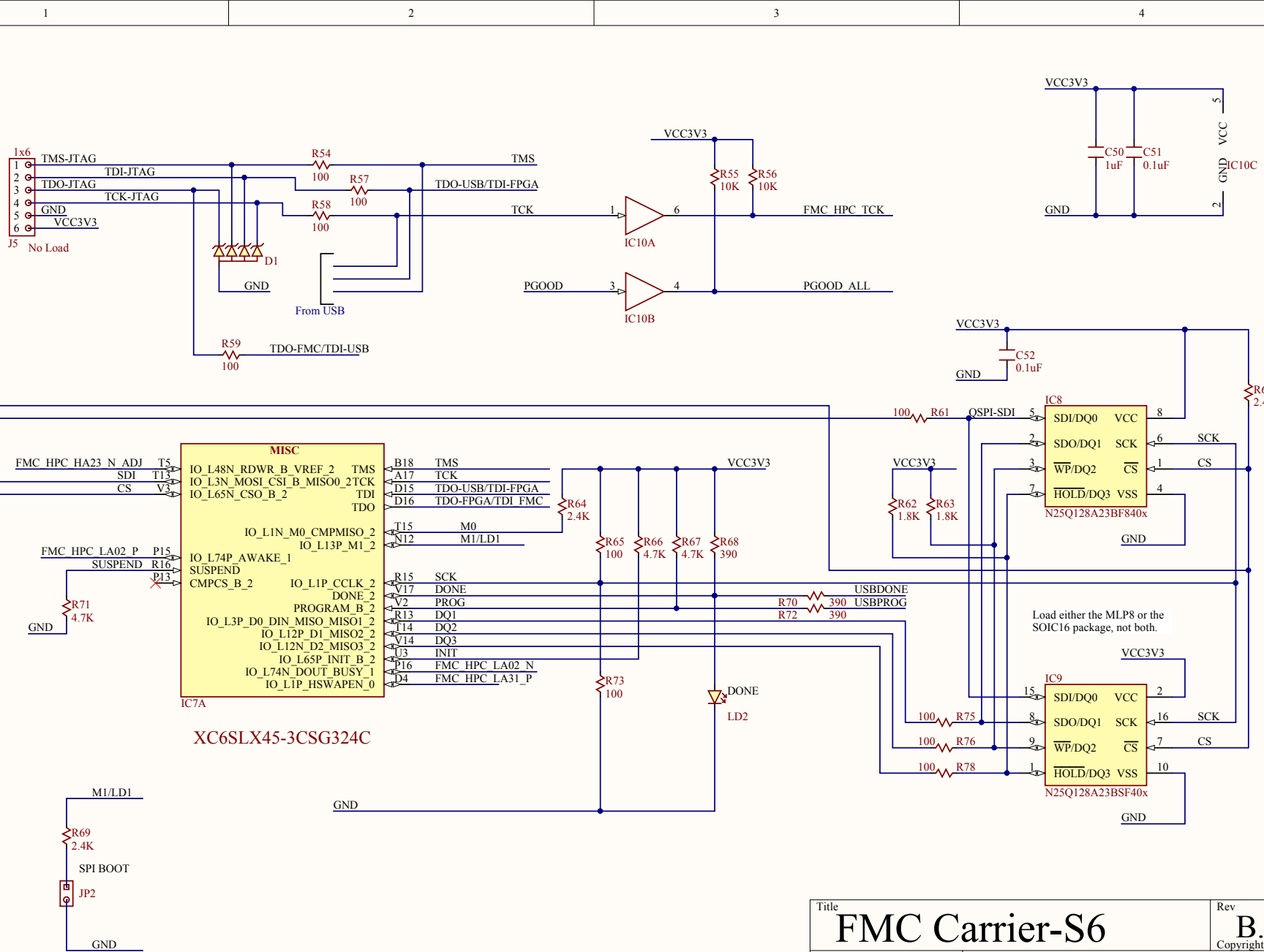
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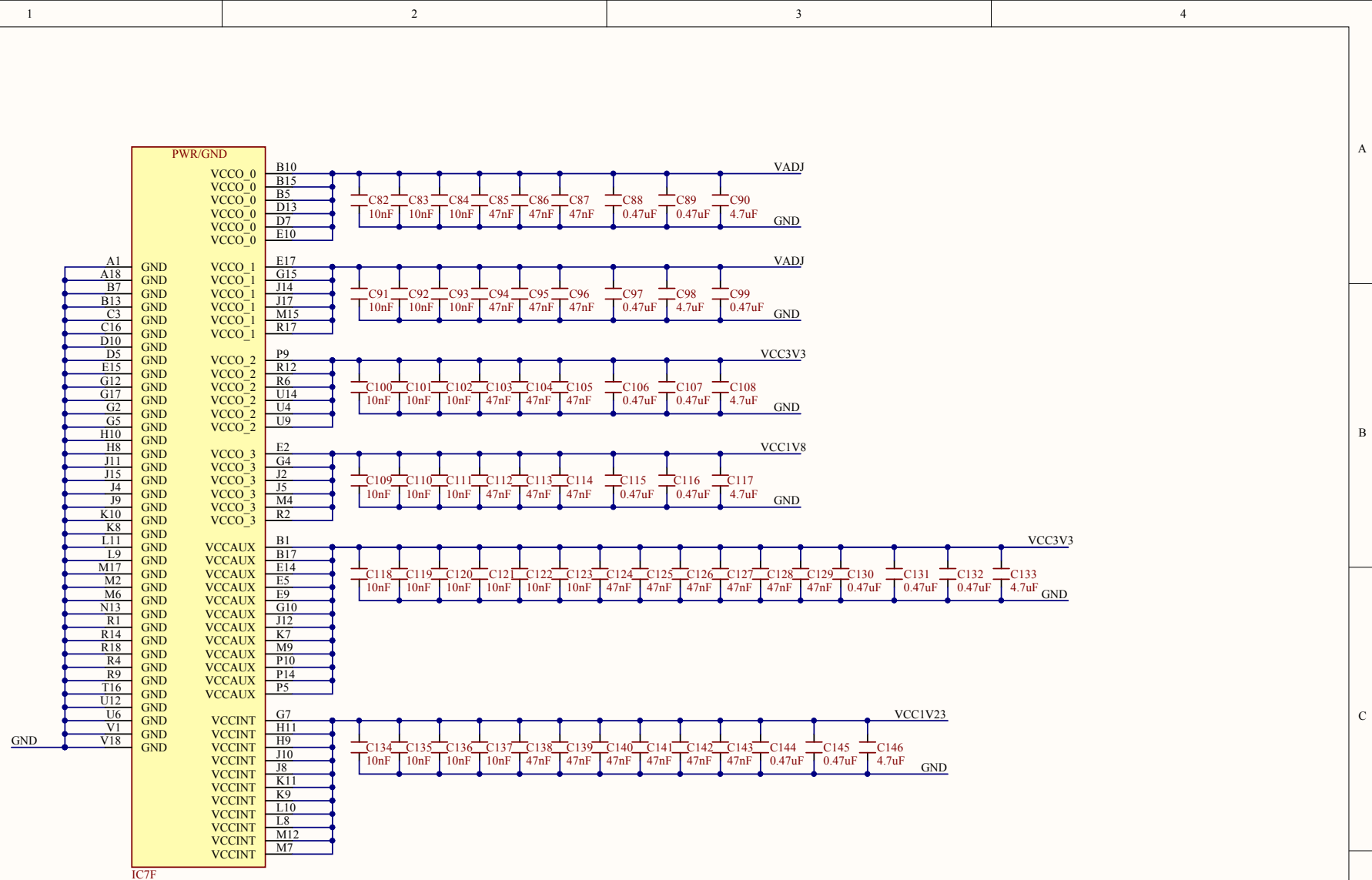












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