

V1.2

MCU U1A {5,11} KEY1 > FSMC_NBL0 {6,11} PA0-WKUP/USART2 CTS/ADC123 IN0/TIM2 CH1 ETR/TIM5 CH1/TIM8 ETR TIM4 ETR/FSMC NBL0/PE0 35 142 {11} GPIO1 {7,11} USART2 TX PA1/USART2_RTS/ADC123_IN1/TIM5_CH2/TIM2_CH2 FSMC_NBL1/PE1 FSMC_NBL1 {6,11} FSMC_A23 {6,11} PA2/USART2 TX/TIM5 CH3/ADC123 IN2/TIM2 CH3 TRACECK/FSMC A23/PE2 37 {7,11} USART2_RX TRACED0/FSMC_A19/PE3 KEY4 {5,11} PA3/USART2_RX/TIM5_CH4/ADC123_IN3/TIM2_CH4 40 {5,11} SPI1_CS0 PA4/SPI1_NSS/USART2_CK/DAC_OUT1/ADC12_IN4 PA5/SPI1_SCK/DAC_OUT2/ADC12_IN5 TRACED1/FSMC A20/PE4 USB CTRL [8,11] 41 {5,11} SPI1 SCK AP3426 INT (5,11) TRACED2/FSMC_A21/PE5 {5,11} SPI1 MISO PA6/SPI1 MISO/TIM8 BKIN/ADC12 IN6/TIM3 CH1/TIM1 BKIN TRACED3/FSMC A22/PE6 TP GPIO2 (6,11) 43 (5,11) SPI1 MOSI FSMC_D4 {6,11} PA7/SPI1 MOSI/TIM8 CH1N/ADC12 IN7/TIM3 CH2/TIM1 CH1N FSMC D4/TIM1 ETR/PE7 100 {5,11} BUZZER FSMC_D5/TIM1_CH1N/PE8 FSMC D5 {6,11} PA8/USART1_CK/TIM1_CH1/MCO 101 {7.11} USART1 TX PA9/USART1 TX/TIM1 CH2 FSMC D6 (6,11) FSMC D6/TIM1 CH1/PE9 102 {7,11} USART1_RX FSMC_D7/TIM1_CH2N/PE10 PA10/USARTT_RX/TIMT_CH3 FSMC D7 {6,11} FSMC_D8/TIM1_CH2/PE11 65 {8,11} USB DM FSMC_D8 {6,11} PA11/USART1 CTS/USBDM/CAN RX/TIM1 CH4 104 FSMC_D9/TIM1_CH3N/PE12 66 {8,11} USB DP FSMC D9 (6.11) PA12/USART1_RTS/USBDP/CAN_TX/TIM1_ETR {3,11} JTMS FSMC_D10/TIM1_CH3/PE13 67 SFSMC_D10 (6,11) PA13/JTMS SWDIO 109 {3,11} JTCK FSMC D11 (6.11) PA14/JTCK SWCLK FSMC D11/TIM1 CH4/PE14 110 {3,11} JTDI \ PA15/JTDI/SPI3_NSS/I2S3_WS/TIM2_CH1_ETR/SPI1_NSS FSMC_D12/TIM1_BKIN/PE15 (FSMC D12 (6,11) SFSMC_A0 {6,11} SFSMC_A1 {6,11} {5,11} LED_R PB0/ADC12_IN8/TIM3_CH3/TIM8_CH2N/TIM1_CH2N FSMC_A0/PF0 47 11 {5,11} LED G PB1/ADC12_IN9/TIM3_CH4/TIM8_CH3N/TIM1_CH3N FSMC A1/PF1 48 12 13 {3.11} BOOT1 PB2/BOOT1 FSMC A2/PF2 FSMC A2 {6.11} 133 {3,11} JTDO PB3/JTDO/SPI3_SCK/I2S3_CK/TRACESWO/TIM2_CH2/SPI1_SCK FSMC_A3 {6,11} FSMC A3/PF3 134 14 CAMERA WEN FSMC A4 (6,11) {3,11} JNTRST PB4/JNTRST/SPI3 MISO/TIM3 CH1/SPI1 MISO FSMC A4/PF4 135 PB5/I2C1_SMBA/SPI3_MOSI/I2S3_SD/TIM3_CH2/SPI1_MOSI FSMC_A5 {6,11} {5,11} LED_B FSMC A5/PF5 136 18 {5,11} I2C1 SCL TP GPIO5 (6,11) ADC3 IN4/FSMC NIORD/PF6 PB6/I2C1 SCL/TIM4 CH1/USART1 TX 137 (5,11) I2C1 SDA PB7/I2C1 SDA/FSMC NADV/TIM4 CH2/USART1 RX ADC3 IN5/FSMC NREG/PF7 TP GPIO4 (6.11) 139 TP GPIO3 (6,11) {8,11} CAN_RX PB8/TIM4 CH3/SDIO_D4/I2C1_SCL/CAN_RX ADC3_IN6/FSMC_NIOWR/PF8 {8,11} CAN TX PB9/TIM4_CH4/SDIO_D5/I2C1_SDA/CAN_TX ADC3_IN7/FSMC_CD/PF9 LCD GPIO (6,11) 69 TP_GPIO1 {6,11} {7,11} USART3_TX PB10/I2C2 SCL/USART3 TX/TIM2 CH3 ADC3 IN8/FSMC INTR/PF10 70 49 {7.11} USART3 RX FSMC NIOS16/PF11 CLCD RST (6.11) PB11/I2C2 SDA/USART3 RX/TIM2 CH4 {9,11} SPI2 CS0 FSMC A6 (6.11) PB12/SPI2 NSS/I2S2 WS/I2C2 SMBA/USART3 CK/TIM1 BKIN FSMC A6/PF12 53 CAMERA_OE FSMC_A7 (6,11) 74 {9,10,11} SPI2 SCK PB13/SPI2_SCK/I2S2_CK/USART3_CTS/TIM1_CH1N FSMC_A7/PF13 54 CAMERA_VSYNC FSMC_A8 (6,11) {9,10,11} SPI2 MISO PB14/SPI2 MISO/TIM1 CH2N/USART3 RTS FSMC A8/PF14 76 {9,10,11} SPI2_MOSI \ FSMC_A9 (6,11) FSMC_A9/PF15 PB15/SPI2_MOSI/I2S2_SD/TIM1_CH3N {5,11} ADC > PC0/ADC123 IN10 FSMC A10/PG0 27 57 CAMERA D1 {5,11} ICM20608 INT PC1/ADC123 IN11 FSMC A11/PG1 FSMC A11 (6,11) 87 CAMERA D2 {11} GPIO2 FSMC_A12 {6,11} PC2/ADC123 IN12 FSMC A12/PG2 29 88 CAMERA D3 {11} GPIO3 PC3/ADC123_IN13 SMC A13 (6,11) FSMC_A13/PG3 44 89 CAMERA D4 FSMC_A14 {6,11} {5,11} SPI1 CS1 PC4/ADC12 IN14 FSMC A14/PG4 45 90 camera d5 {8,11} RS485 RE FSMC A15 (6,11) PC5/ADC12_IN15 FSMC_A15/PG5 96 (6,11) LCD PWM FSMC INT2/PG6 CAMERA D6 {11} PC6/I2S2 MCK/TIM8 CH1/SDIO D6/TIM3 CH1 {5,11} IRDA CAMERA D7 {11} PC7/I2S3_MCK/TIM8_CH2/SDIO_D7/TIM3_CH2 FSMC_INT3/PG7 {6,11} SDIO_D0 PC8/TIM8 CH3/SDIO D0/TIM3 CH3 PG8 ENC28J60_WOL {9,11} 99 124 {6,11} SDIO D1 FSMC_NE2/FSMC_NCE3/PG9 VS1053B XREST {10,11} PC9/TIM8 CH4/SDIO D1/TIM3 CH4 111 125 (6,11) SDIO D2 PC10/UART4_TX/SDIO_D2/USART3_TX FSMC_NCE4_1/FSMC_NE3/PG10 FSMC_NE3 {6,11} 112 126 ENC28J60 INT (9,11) {6,11} SDIO D3 PC11/UART4 RX/SDIO D3/USART3 RX FSMC NCE4 2/PG11 127 {6,11} SDIO CLK FSMC_NE4/PG12 PC12/UART5_TX/SDIO_CK/USART3_CK FSMC NE4 (6,11) 128 8002A_EN {10,11 {5,11} KEY3 PC13/TAMPER-RTC FSMC A24/PG13 129 PG14 {11} {3} OSC32 IN PC14/OSC32 IN FSMC A25/PG14 132 (3) OSC32_OUT > PC15/OSC32 OUT PG15 {6,11} FSMC_D2 > {6,11} FSMC_D3 > PD0/FSMC_D2/CAN_RX 115 PD1/FSMC D3/CAN TX 116 {6,11} SDIO_CMD PD2/TIM3 ETR/UART5 RX/SDIO CMD 117 (NRST {3,11}) {10,11} SPI2_CS1 {6.11} FSMC_NOE PD3/FSMC CLK/USART2 CTS NRS1 118 138 **⟨** BOOT0 {3,11} PD4/FSMC NOE/USART2 RTS BOOT0 {6,11} FSMC_NWE PD5/FSMC NWE/USART2 TX 122 {10.11} VS1053B XDCS $\langle \langle$ OSC IN {3} PD6/FSMC NWAIT/USART2 RX OSC IN 123 24 √Sosc_out (3) {10,11} VS1053B DREQ PD7/FSMC_NE1/FSMC_NCE2/USART2_CK PD8/FSMC_D13/USART3_TX OSC_OUT 77 {6,11} FSMC D13 106 {6,11} FSMC D14 PD9/FSMC D14/USART3 RX NC 79 {6,11} FSMC_D15 PD10/FSMC D15/USART3 CK 80 (6.11) FSMC A16 PD11/FSMC A16/USART3 CTS 81 (6,11) FSMC_A17 深圳百问网科技有限公司 PD12/FSMC A17/TIM4 CH1/USART3 RTS/ 82 (6,11) FSMC A18 PD13/FSMC A18/TIM4 CH2 Shenzhen 100ask Technology Co., Lt (6,11) FSMC D0 PD14/FSMC D0/TIM4 CH3 86 {6,11} FSMC_D1 > PD15/FSMC D1/TIM4 CH4 www.100ask.net | https://100ask.taobao.com STM32F103ZET6 100ASK STM32F103 Document Number: Rev: 02_MCU Saturday, January 16, 2021

















