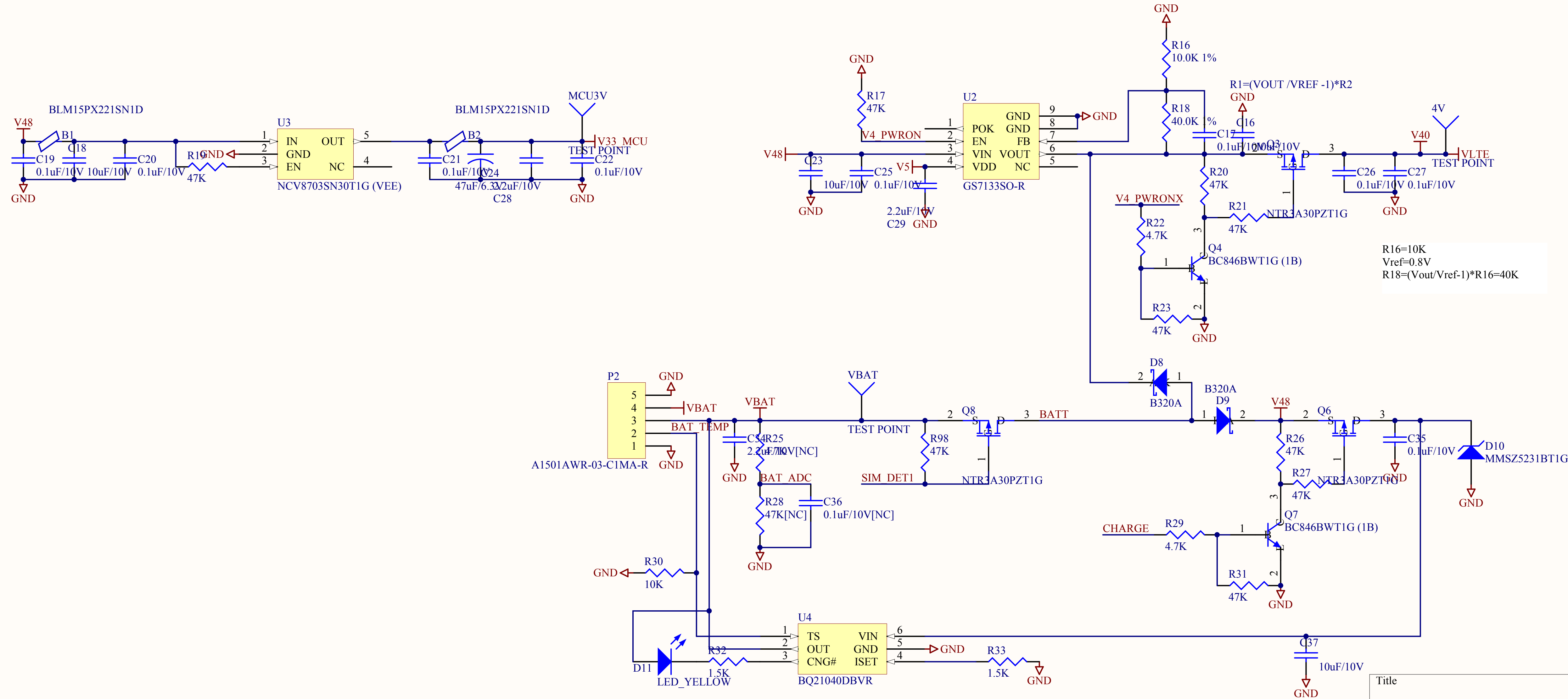
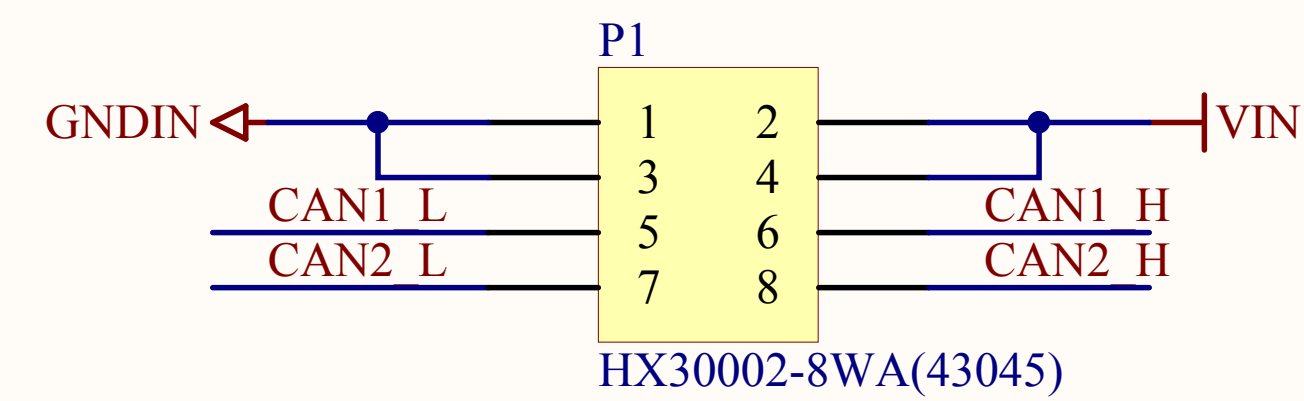
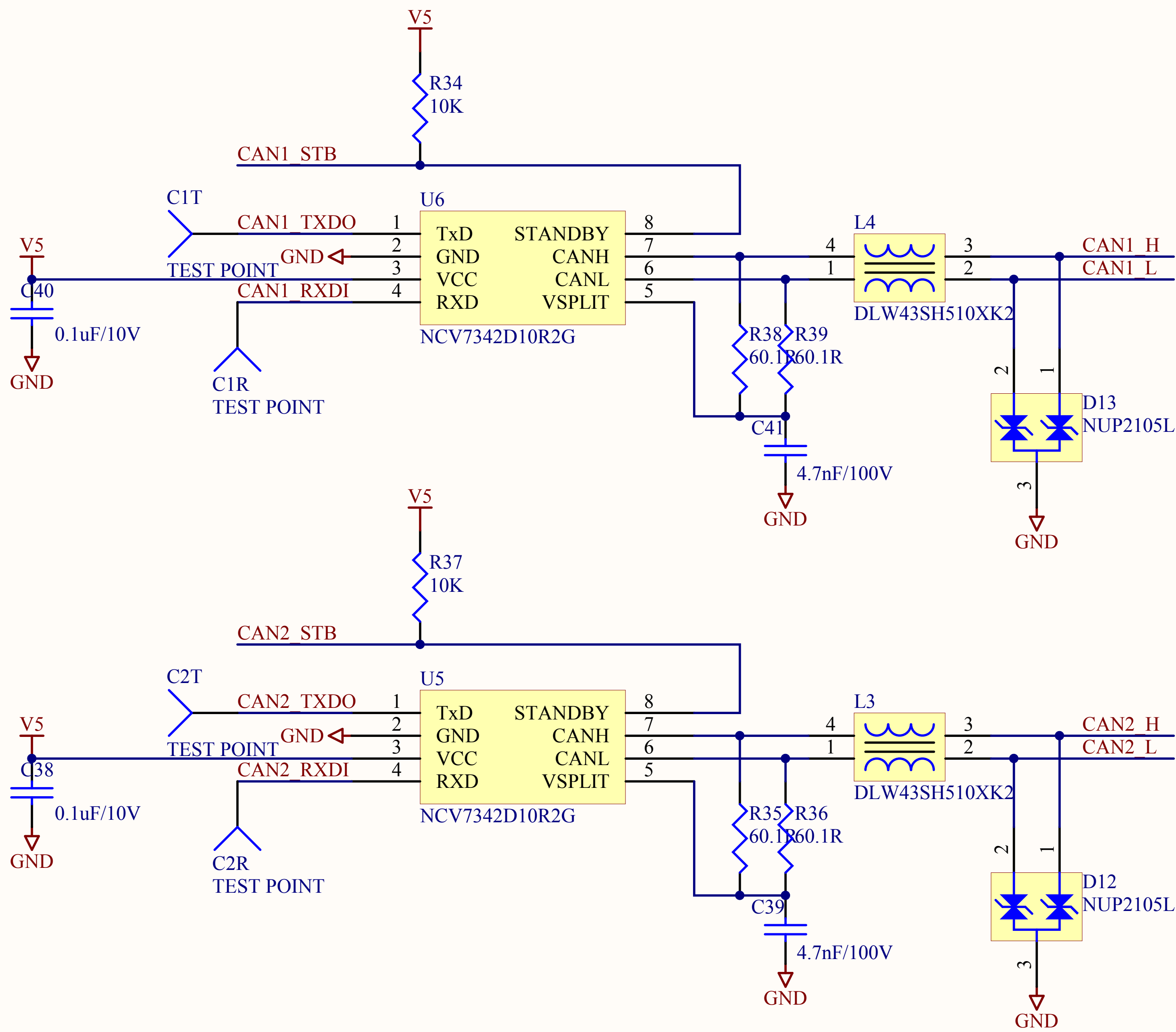


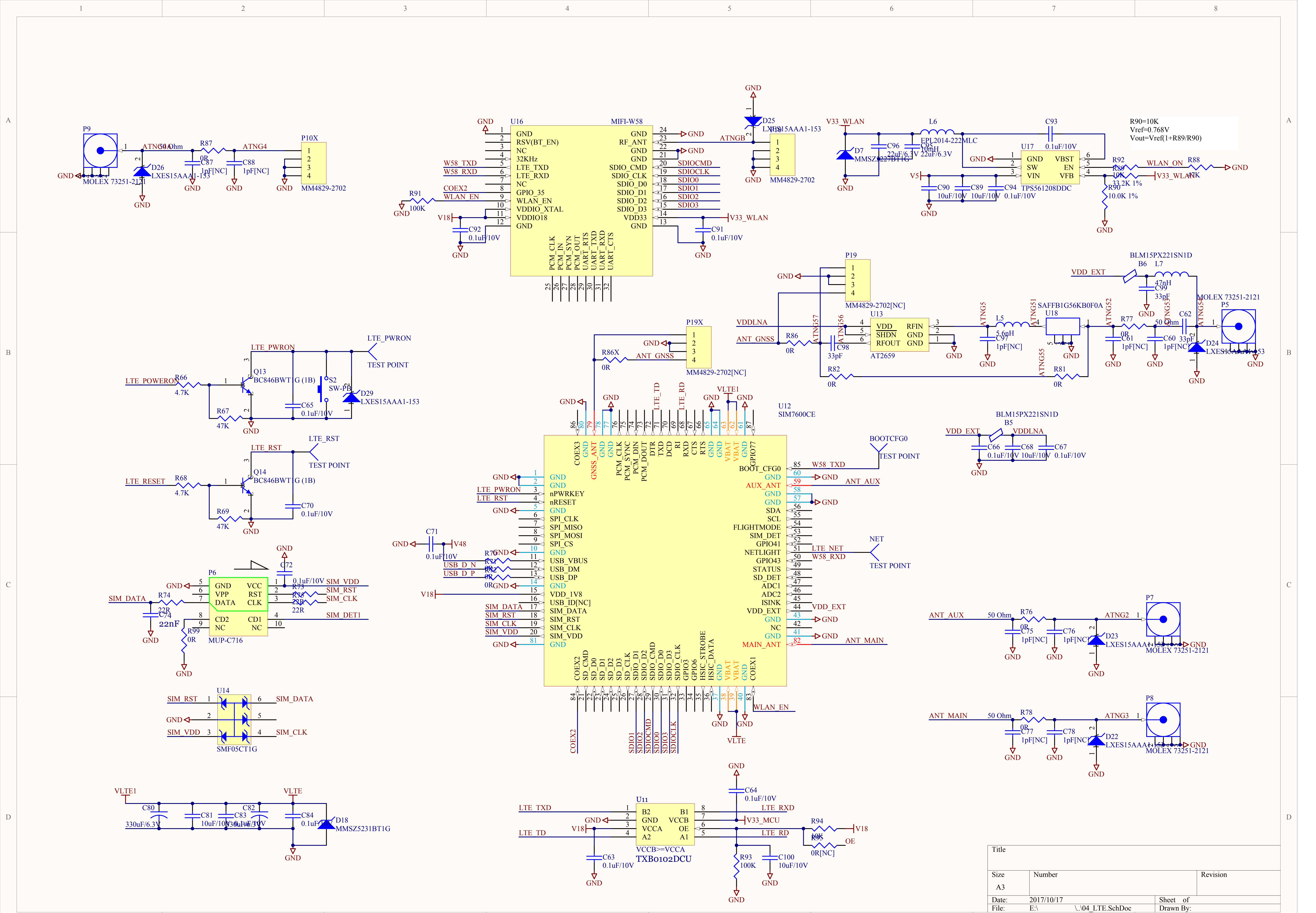
$C_{in} > 3\mu F$
 $C_{out} > 2 \times \Delta I_{out} / f_{sw} / \Delta I_{out}$
 $C_6 = 1 / (R_3 \times f_{sw} \times P_I)$
 $C_8 = 1 / (2 \times P_I \times R_3 \times 1.04 \text{ KHz})$
 $V_{start} = 6.0V, V_{stop} = 5.5V$
 $R_1 = (V_{start} - V_{stop}) / I_{hys}$
 $R_4 = V_{ena} / ((V_{start} - V_{ena}) / R_1 + 1)$
 $R_2 = R_6 \times (V_{out} - 0.8V) / 0.8V = 52.5K$
 $R_6 = 10K$
 $R_3 = (2 \times P_I \times 49KHz \times 47uF \times 3 / 10.5A/V) \times (5 / 0.8 / 310uA/V)$
 $R_5 = 206033 / f_{sw} (KHz) \times 1.0888$
 $f_{sw} = 500KHz$



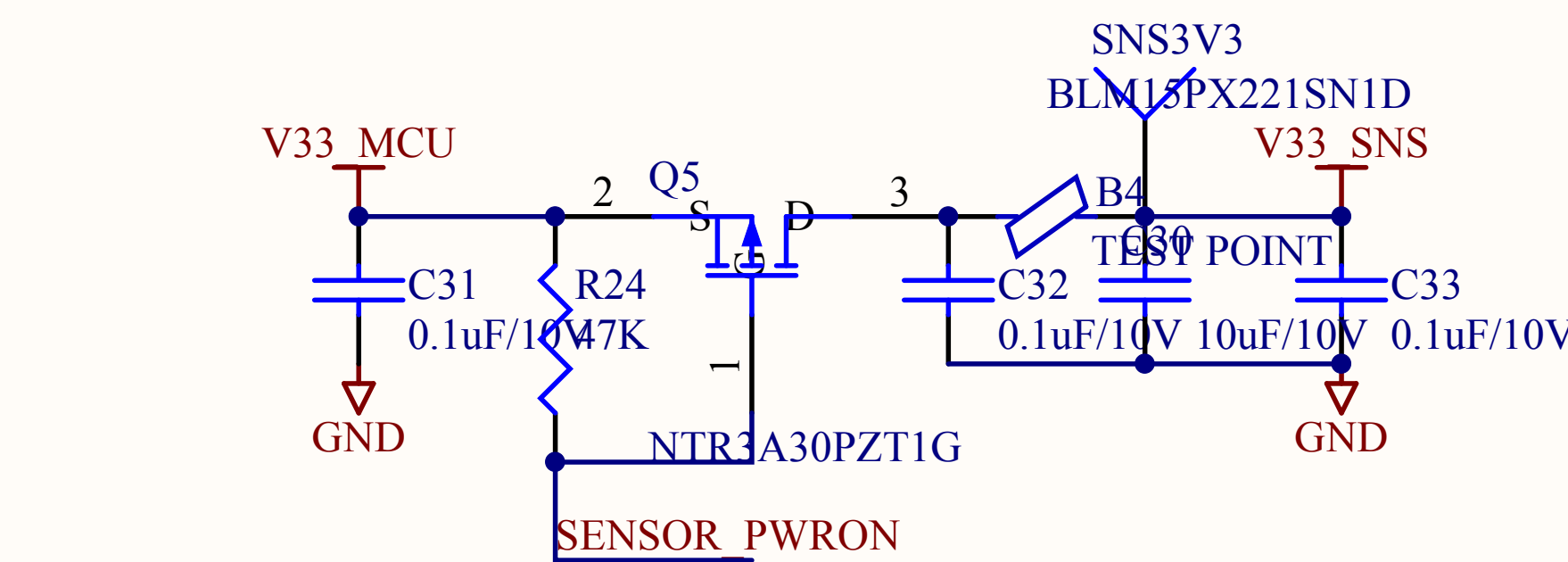
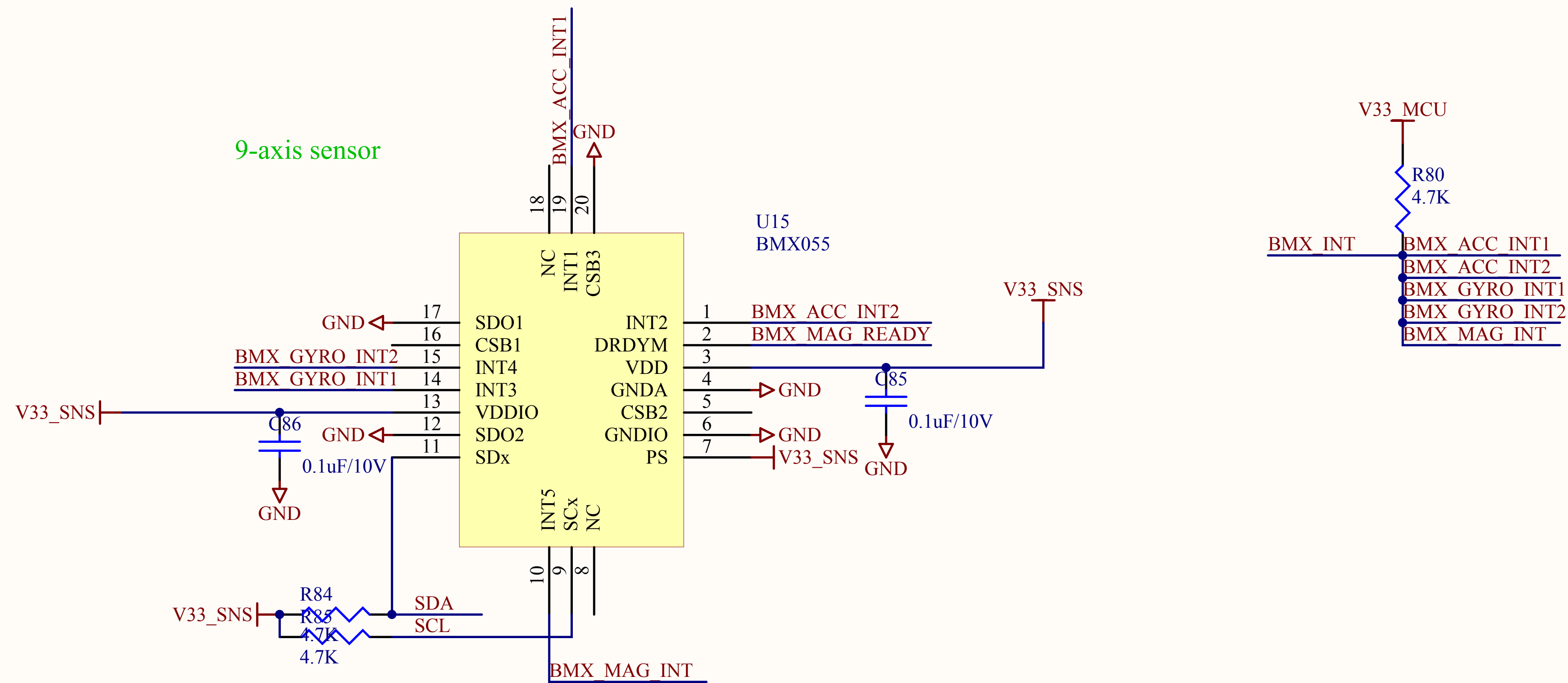
Title		
Size	Number	Revision
A3		
Date:	2017/10/17	Sheet of
File:	E:\ \01_POWER.SchDoc	Drawn By:



Title			
Size	Number		Revision
A4			
Date:	2017/10/17		Sheet of
File:	E:\ \.02_INTF.SchDoc		Drawn By:



Title		
Size	Number	Revision
A3		
Date:	2017/10/17	Sheet of
File:	E:\...04_LTE.SchDoc	Drawn By:



Supply Current in Active Mode (average)

IDD,lp,m Low power preset Nominal VDD supplies TA=25° C, ODR=10Hz 170 μ A

IDD,rg,m Regular preset Nominal VDD supplies TA=25° C, ODR=10Hz 0.5 mA

IDD,eh,m Enhanced regular preset Nominal VDD supplies TA=25° C, ODR=10Hz 0.8 mA

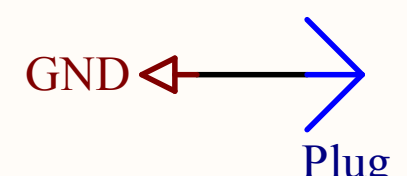
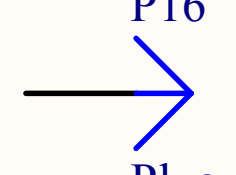
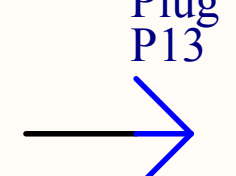
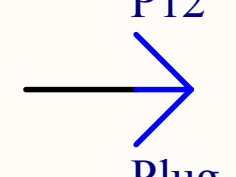
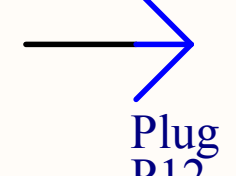
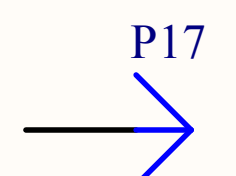
IDD,ha,m High accuracy preset Nominal VDD supplies TA=25° C, ODR=20Hz 4.9 mA

Supply Current in Suspend Mode $I_{DDsm,m}$ Nominal VDD/VDDIO supplies, $T_A=25^\circ\text{C}$ 1 μA

Peak supply current in Active Mode I_{DDpk,m} In measurement phase Nominal VDD supplies
TA=25° C 18 mA

Peak logic supply current in active mode IDDIOpk,m Only during measurement phase
Nominal VDDIO supplies 210 μ A

Title		
Size A4	Number	Revision
Date:	2017/10/17	Sheet of
File:	E:\...05 SENSOR.SchDoc	Drawn By:



Title		
Size A4	Number	Revision
Date:	2017/10/17	Sheet of
File:	E:\.....\..\AUTO_DATA2.SchDoc	Drawn By:

