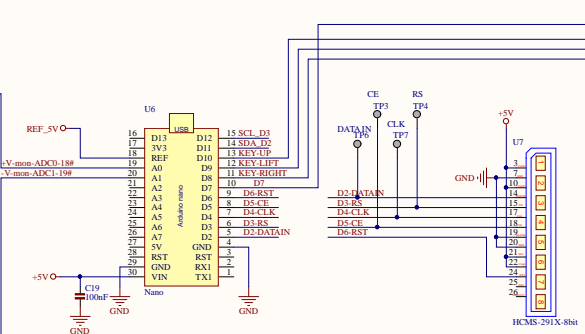
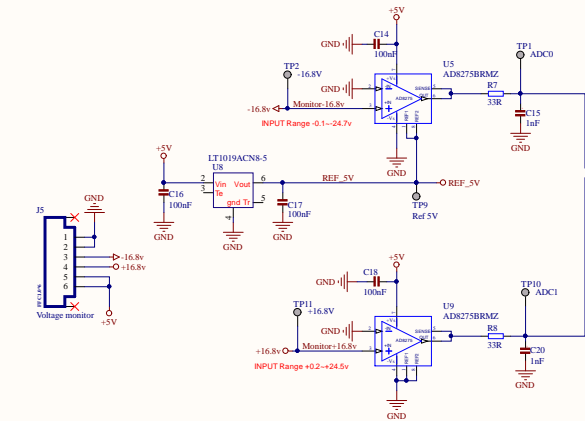
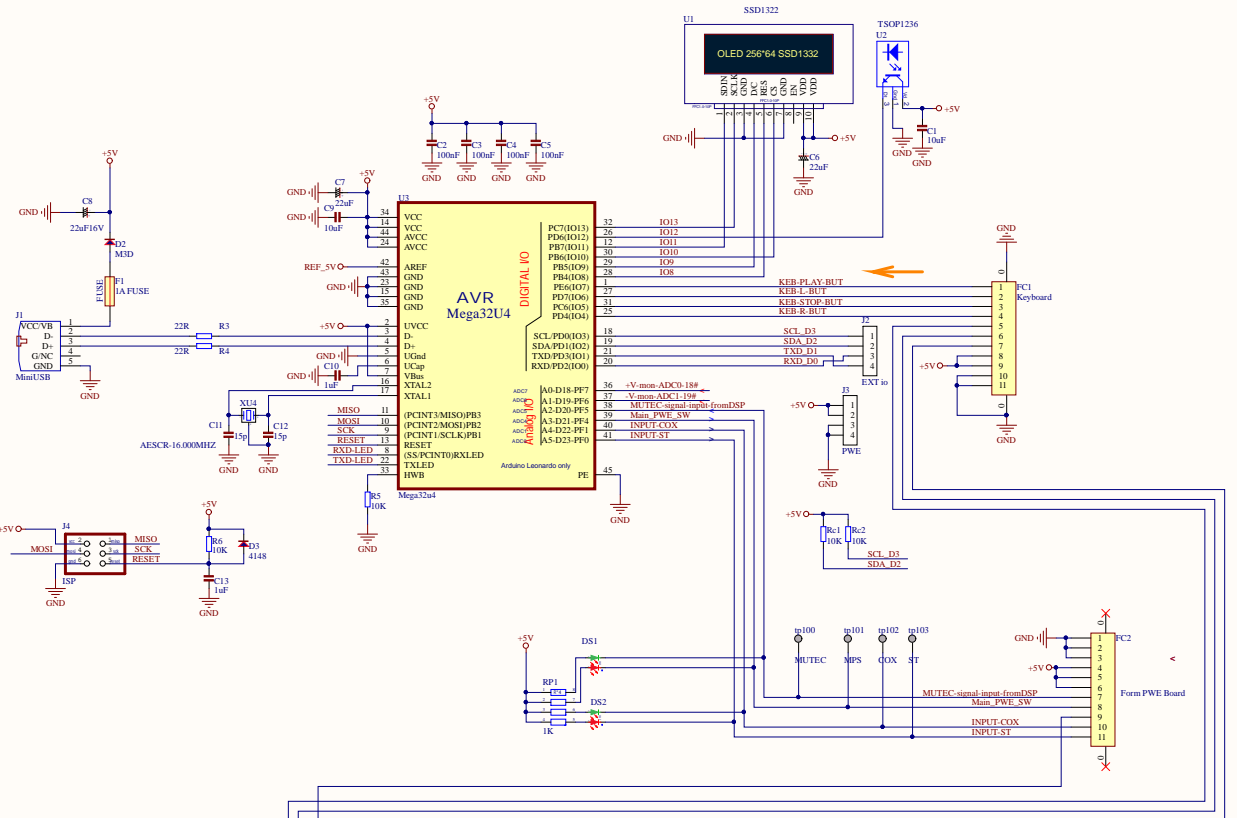
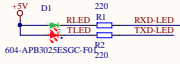
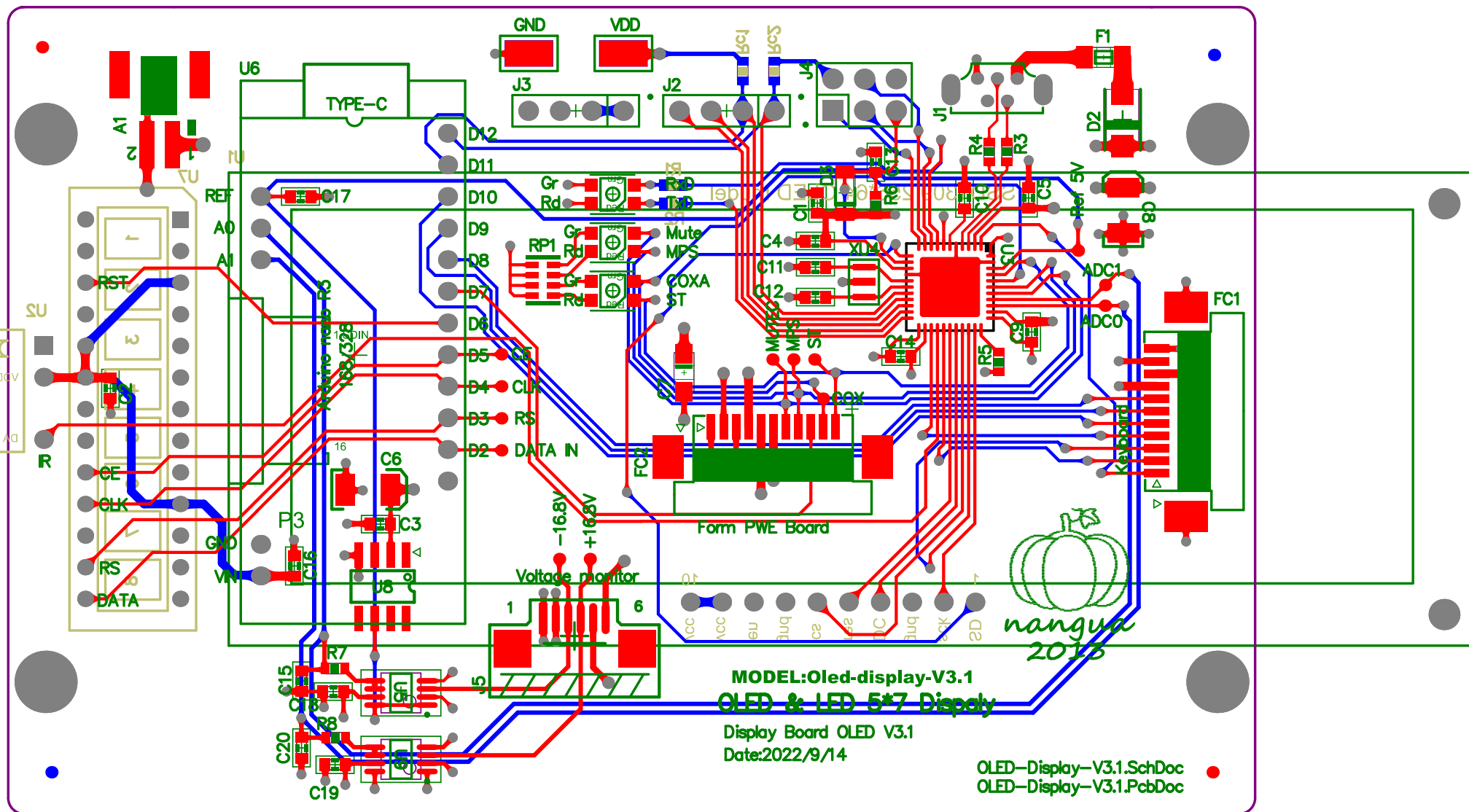


FID1 FID2 FID3 FID4 FID5 FID6 FID7 FID8  
Fiducial Fiducial Fiducial FiducialFiducial Fiducial Fiducial Fiducial

URG2\_SSD1322\_NHD\_256X64\_1\_4V0\_5V\_SPI ulg2(URG2\_R0, P'clock=713, P'data=711, P'cs=710, P'dc=719, P'res=716); if Enable\_URG2\_18BIT in ulg2.h



Title		
Size	Number	Revision
A2		
Date:	2022/9/19	Sheet 4 of 4
File:	Y:\设计文件\未\oled display-v3.1.Sch2\Brown B...	



Boards