Fanny Zamora

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Education

INSA Lyon - France ⬀   *Sept 2022 – Sept 2025*
Department of Electrical Engineering, Microelectronics specialization
- **Example project:** Design of an analog circuit and transfer onto a wafer in a cleanroom environment.

University of Toulon - France ⬀   *Sept 2020 – Sept 2022*
DUT in Electrical Engineering and Industrial Computing
- **Example project:** Development of a wattmeter prototype with its own website.

Baccalauréat SSI, specialization in Mathematics, European section   *Sept 2020*

Experiences - Electrical Engineering

ASML ⬀   *Veldhoven, Netherlands*
Embedded Logic and Firmware Intern on the Acquisiton module in EUV system: *March 2025 – Sept 2025*
- Implemented a **Ping Pong method on FPGA** within a laser system control loop, **optimizing inter-module interface bandwidth** up to 100 kHz. Developed **Python scripts** for **validation, verification and reporting**.
- Created a **measurement card** to assess jitter, latency, and other timing parameters, enabling **precise characterization** of acquisition modules in a high frequency setting. Developed **Python scripts** for **validation** through hardware interfaces and **reporting**.

Alstom ⬀   *Villeurbanne, France*
Apprentice Industrial Test Engineer: *Sept 2022 – Feb 2025*
- Started the **development of a tooling board** to **improve the quality and accuracy of tests** for a railway control board, **doubling the number of boards repaired per year**.
- **Standardized an automated self-test** procedure for **60 test benches** using TestStand.
- Created a **LabVIEW application** to support manufacturing processes, saving engineers up to **15 working days per year**.

Orange Wholesale ⬀   *Toulon, France*
Apprentice After-Sales Advisor – B2B: *Sept 2020 – Sept 2022*
- Handled **calls and technical inquiries** from partner operators, ensuring **efficient issue resolution and customer satisfaction**.

Additional Information

Skills
- Electronics (Simulation on Spice or Cadence, PCB Design in Kicad, prototyping...), Microelectronics, Test Automation, Power Electronics
- Programming : Python3, VHDL, TestStand, LabView, C++
  - Learning : pyTest, CI/CD
- Development/Requirement Tools: Git, Bitbucket, Jira, Polarion

Qualities
- Autonomous and quick to learn, solution-oriented, curious, effective communicator, well-organized

Languages
- French - Native   English - Fluent

Interests
- Karate (for 13 years, black belt), Volleyball, Astronomy, Reading

References

Maxime Chauvier - Test Authority (Alstom)
- Email: maxime.chauvier@alstomgroup.com ⬀

Paul Buijsman - Electronics Architect (ASML)
- Email: paul.buijsman@asml.com ⬀