Net Length (mm)	FPGA Pin		JA1 FPGA Signal	FPGA Pin	Net Length (mm)
		VIO_OUT (+3.3V) 1	9		
	C19	VCCO_35 3		G14	36.462
67.528	E18	IO_L5P_T0_AD9P_35 5		D20	67.516
67.517	E19	IO_L5N_T0_AD9N_35 7		D19	67.527
		GND 9			
67.521	F16		12 IO_L1N_T0_AD0N_35	B20	67.682
67.524	F17	IO_L6N_T0_VREF_35	14 IO_L1P_T0_AD0P_35 16 GND	C20	67.533
67.516	E17	IO_L3P_T0_DQS_AD1P_35		A20	67.530
67.531	D18	IO_L3N_T0_DQS_AD1N_35	20 IO_L2P_T0_AD8P_35 22 GND	B19	67.521
67.137	F19	IO L15P T2 DQS AD12P 35 23	24 IO L18N T2 AD13N 35	G20	67.143
67.155	F20	IO_L15N_T2_DQS_AD12N_35 25	26 IO_L18P_T2_AD13P_35	G19	67.150
		GND 27	28 GND		
67.155	J20	IO_L17P_T2_AD5P_35	30 IO_L16N_T2_35	G18	67.152
67.139	H20	IO_L17N_T2_AD5N_35 31		G17	67.148
		GND 33	34 GND		
67.156	J18	IO_L14P_T2_AD4P_SRCC_35 35		H17	67.149
67.133	H18	IO_L14N_T2_AD4N_SRCC_35 37	38 IO_L13P_T2_MRCC_35	Н16	67.134
		SMB_I2C_SCL0 <mark>39</mark>	40 SMB_I2C_SDA0		
Net Length (mm)	FPGA Pin		JA2 FPGA Signal JA2 SMB nINT	FPGA Pin	Net Length (mm)
Net Length (mm)	FPGA Pin	_	JA2_SMB_nINT	FPGA Pin	Net Length (mm)
- , ,	C19	VIO_OUT (+3.3V) 1 VCCO_35 3	JA2_SMB_nINT 4 IO_25_35		21.780
Net Length (mm)  37.541 37.550		VIO_OUT (+3.3V) 1 VCCO_35 3 IO_L22P_T3_AD7P_35 5	JA2_SMB_nINT 4 IO_25_35 6 IO_L24N_T3_AD15N_35	J15	
37.541	C19 L14	VIO_OUT (+3.3V) 1 VCCO_35 3	JA2_SMB_nINT 4 IO_25_35 6 IO_L24N_T3_AD15N_35 8 IO_L24P_T3_AD15P_35	J15 J16	21.780 37.545
37.541	C19 L14	VIO_OUT (+3.3V) 1 VCCO_35 3 IO_L22P_T3_AD7P_35 5 IO_L22N_T3_AD7N_35 7	JA2_SMB_nINT 4 IO_25_35 6 IO_L24N_T3_AD15N_35 8 IO_L24P_T3_AD15P_35 10 GND	J15 J16	21.780 37.545
37.541 37.550	C19 L14 L15	VIO_OUT (+3.3V) 1 VCCO_35 3 IO_L22P_T3_AD7P_35 5 IO_L22N_T3_AD7N_35 7 GND 9	JA2_SMB_nINT 4 IO_25_35 6 IO_L24N_T3_AD15N_35 8 IO_L24P_T3_AD15P_35 10 GND 12 IO_L19N_T3_VREF_35	J15 J16 K16	21.780 37.545 37.542
37.541 37.550 37.547	C19 L14 L15 M14	VIO_OUT (+3.3V) 1 VCCO_35 3 IO_L22P_T3_AD7P_35 5 IO_L22N_T3_AD7N_35 7 GND 9 IO_L23P_T3_35 11 IO_L23N_T3_35 11	JA2_SMB_nINT 4 IO_25_35 6 IO_L24N_T3_AD15N_35 8 IO_L24P_T3_AD15P_35 10 GND 12 IO_L19N_T3_VREF_35	J15 J16 K16	21.780 37.545 37.542 37.543
37.541 37.550 37.547	C19 L14 L15 M14	VIO_OUT (+3.3V) 1 VCCO_35 3 IO_L22P_T3_AD7P_35 5 IO_L22N_T3_AD7N_35 7 GND 9 IO_L23P_T3_35 11 IO_L23N_T3_35 13 GND 15	JA2_SMB_nINT 4 IO_25_35 6 IO_L24N_T3_AD15N_35 8 IO_L24P_T3_AD15P_35 10 GND 12 IO_L19N_T3_VREF_35 14 IO_L19P_T3_35	J15 J16 K16	21.780 37.545 37.542 37.543
37.541 37.550 37.547 37.543	C19 L14 L15 M14 M15	VIO_OUT (+3.3V) 1 VCCO_35 3 IO_L22P_T3_AD7P_35 5 IO_L22N_T3_AD7N_35 7 GND 9 IO_L23P_T3_35 11 IO_L23N_T3_35 13 GND 15	JA2_SMB_nINT 4	J15 J16 K16 G15 H15	21.780 37.545 37.542 37.543 37.550
37.541 37.550 37.547 37.543 37.563	C19 L14 L15 M14 M15	VIO_OUT (+3.3V) 1	JA2_SMB_nINT 4	J15 J16 K16 G15 H15	21.780 37.545 37.542 37.543 37.550
37.541 37.550 37.547 37.543 37.563	C19 L14 L15 M14 M15	VIO_OUT (+3.3V) 1	JA2_SMB_nINT 4	J15 J16 K16 G15 H15	21.780 37.545 37.542 37.543 37.550
37.541 37.550 37.547 37.543 37.563 37.569	C19 L14 L15 M14 M15 N15 N16	VIO_OUT (+3.3V) 1	JA2_SMB_nINT 4	J15 J16 K16 G15 H15 J14 K14	21.780 37.545 37.542 37.543 37.550 37.557 37.546
37.541 37.550 37.547 37.543 37.563 37.569 65.700	C19 L14 L15 M14 M15 N15 N16	VIO_OUT (+3.3V) 1	JA2_SMB_nINT 4	J15 J16 K16 G15 H15 J14 K14	21.780 37.545 37.542 37.543 37.550 37.557 37.546
37.541 37.550 37.547 37.543 37.563 37.569 65.700 65.693	C19 L14 L15 M14 M15 N15 N16	VIO_OUT (+3.3V) 1 VCCO_35 3 IO_L22P_T3_AD7P_35 5 IO_L22N_T3_AD7N_35 7 GND 9 IO_L23P_T3_35 11 IO_L23N_T3_35 13 GND 15 IO_L21P_T3_DQS_AD14P_35 17 IO_L21N_T3_DQS_AD14N_35 GND 21 IO_L9P_T1_DQS_AD3P_35 23 IO_L9N_T1_DQS_AD3N_35 25 GND 27 IO_L8P_T1_AD10P_35 29	JA2_SMB_nINT 4	J15 J16 K16 G15 H15 J14 K14	21.780 37.545 37.542 37.543 37.550 37.557 37.546
37.541 37.550 37.547 37.543 37.563 37.549 65.700 65.693	C19 L14 L15 M14 M15 N15 N16 L19 L20	VIO_OUT (+3.3V) 1	JA2_SMB_nINT 4	J15 J16 K16 G15 H15 J14 K14 J19 K19	21.780 37.545 37.542 37.543 37.550 37.557 37.546 65.691 65.689
37.541 37.550 37.547 37.543 37.563 37.549 65.700 65.693 65.710 65.703	C19 L14 L15 M14 M15 N15 N16 L19 L20 M17 M18	VIO_OUT (+3.3V) 1	JA2_SMB_nINT 4	J15 J16 K16 G15 H15 J14 K14 J19 K19	21.780 37.545 37.542 37.543 37.550 37.557 37.546 65.691 65.689 65.704 65.714
37.541 37.550 37.547 37.543 37.563 37.549 65.700 65.693 65.710 65.703	C19 L14 L15 M14 M15 N15 N16 L19 L20 M17 M18 L16	VIO_OUT (+3.3V) 1	JA2_SMB_nINT 4	J15 J16 K16 G15 H15 J14 K14 J19 K19 M20 M19	21.780 37.545 37.542 37.543 37.550 37.557 37.546 65.691 65.689 65.704 65.714
37.541 37.550 37.547 37.543 37.563 37.549 65.700 65.693 65.710 65.703	C19 L14 L15 M14 M15 N15 N16 L19 L20 M17 M18	VIO_OUT (+3.3V) 1	JA2_SMB_nINT 4	J15 J16 K16 G15 H15 J14 K14 J19 K19	21.780 37.545 37.542 37.543 37.550 37.557 37.546 65.691 65.689 65.704 65.714

Net Length (mm)	FPGA Pin	FPGA Signal	JB1 FPGA Signal	FPGA Pin	Net Length (mm)
. ,		VIO OUT (+3.3V) 1	JB1 SMB nINT		<b>5</b> , ,
	N19	VCCO_34 3	3 4 IO_25_34	Т19	56.313
65.260	T11	IO_L1P_T0_34_5	6 IO_L2N_T0_34	U12	65.247
65.256	T10	IO_L1N_T0_34 7	8 IO_L2P_T0_34	T12	65.252
		GND 9	9 10 GND		
65.261	P14	IO_L6P_T0_34	1 12 IO_L4N_T0_34	W13	65.232
65.264	R14	IO_L6N_T0_VREF_34 1		V12	65.257
		GND 1			
65.256	U13	IO_L3P_T0_DQS_PUDC_B_34		T15	65.271
65.264	V13	IO_L3N_T0_DQS_34 19		T14	65.267
		GND 2			
37.095	T16	IO_L9P_T1_DQS_34		Y17	37.097
37.107	U17	IO_L9N_T1_DQS_34 2		Y16	37.103
		GND 2			
37.095	W14		9 30 IO_L10N_T1_34	W15	37.096
37.109	Y14	IO_L8N_T1_34_3	1 32 IO_L10P_T1_34	V15	37.100
			3 34 GND		
37.090	U14		5 36 IO_L12N_T1_MRCC_34	U19	37.104
37.107	U15	IO_L11N_T1_SRCC_34_3		U18	37.088
		SMB_12C_SCL2	9 40 SMB_I2C_SDA2		
Net Length	FPGA Pin		JB2 FPGA Signal	FPGA Pin	Net Length (mm)
Net Length	FPGA Pin	VIO_OUT (+3.3V) 1	JB2_SMB_nINT	FPGA Pin	Net Length (mm)
Net Length	N19	VIO_OUT (+3.3V) 1 VCCO_34 3	JB2_SMB_nINT JB2_SMB_nINT	R19	Net Length (mm) 65.687
43.088	N19 N17	VIO_OUT (+3.3V) 1 VCCO_34 3 IO_L23P_T3_34 5	JB2_SMB_nINT JB2_SMB_nINT JB2_SMB_nINT JB2_SMB_nINT JB2_SMB_nINT JB2_SMB_nINT JB2_SMB_nINT	R19 P16	_ , ,
-	N19	VIO_OUT (+3.3V) 1 VCCO_34 3 IO_L23P_T3_34 IO_L23N_T3_34 7	JB2_SMB_nINT JD_0_34 JO_124N_T3_34 JO_124P_T3_34	R19	65.687
43.088 43.092	N19 N17 P18	VIO_OUT (+3.3V) 1 VCCO_34 3 IO_L23P_T3_34 5 IO_L23N_T3_34 7 GND 9	JB2_SMB_nINT JB2_SMB_nINT JD2_SMB_nINT JD3_SMB_nINT JD3_S	R19 P16 P15	65.687 43.091 43.091
43.088 43.092 43.087	N19 N17 P18 T17	VIO_OUT (+3.3V) 1 VCCO_34 3 IO_L23P_T3_34 5 IO_L23N_T3_34 7 GND 9 IO_L20P_T3_34 1	JB2_SMB_nINT JB2_SMB_nINT JD2_SMB_nINT JD3_SMB_nINT JD3_S	R19 P16 P15 R17	65.687 43.091 43.091
43.088 43.092	N19 N17 P18	VIO_OUT (+3.3V) 1 VCCO_34 3 IO_L23P_T3_34 5 IO_L23N_T3_34 7 GND 9 IO_L20P_T3_34 1 IO_L20N_T3_34 1	JB2_SMB_nINT JB2_SMB_nINT JI0_0_34 JI0_L24N_T3_34 JI0_L24P_T3_34 JI0_L19N_T3_VREF_34 JI0_L19P_T3_34	R19 P16 P15	65.687 43.091 43.091
43.088 43.092 43.087 43.090	N19 N17 P18 T17 R18	VIO_OUT (+3.3V) 1 VCCO_34 3 IO_L23P_T3_34 5 IO_L23N_T3_34 7 GND 9 IO_L20P_T3_34 11 GND 120N_T3_34 11	JB2_SMB_nINT JB2_S	R19 P16 P15 R17 R16	65.687 43.091 43.091 43.092 43.090
43.088 43.092 43.087 43.090	N19 N17 P18 T17 R18	VIO_OUT (+3.3V) 1 VCCO_34 3 IO_L23P_T3_34 5 IO_L23N_T3_34 7 GND 9 IO_L20P_T3_34 11 GND 11 IO_L21P_T3_DQS_34 11	JB2_SMB_NINT JB2_SMB_NINT JB2_SMB_NINT JB3_4	R19 P16 P15 R17 R16	65.687 43.091 43.091 43.092 43.090
43.088 43.092 43.087 43.090	N19 N17 P18 T17 R18	VIO_OUT (+3.3V) 1 VCCO_34 3 IO_L23P_T3_34 5 IO_L23N_T3_34 7 GND 9 IO_L20P_T3_34 10 L20N_T3_34 GND 11 IO_L21P_T3_DQS_34 10 L21N_T3_DQS_34 11	JB2_SMB_NINT JB2_SMB_NINT JB2_SMB_NINT JB3_4	R19 P16 P15 R17 R16	65.687 43.091 43.091 43.092 43.090
43.088 43.092 43.087 43.090 43.083 43.086	N19 N17 P18 T17 R18 V17 V18	VIO_OUT (+3.3V) 1	JB2_SMB_NINT JB2_S	R19 P16 P15 R17 R16 W19 W18	65.687 43.091 43.091 43.092 43.090 43.097 43.083
43.088 43.092 43.087 43.090 43.083 43.086	N19 N17 P18 T17 R18 V17 V18	VIO_OUT (+3.3V) 1	JB2_SMB_NINT JB2_S	R19 P16 P15 R17 R16 W19 W18	65.687 43.091 43.091 43.092 43.090 43.097 43.083
43.088 43.092 43.087 43.090 43.083 43.086	N19 N17 P18 T17 R18 V17 V18	VIO_OUT (+3.3V) 1 VCCO_34 3 IO_L23P_T3_34 5 IO_L23N_T3_34 7 GND 9 IO_L20P_T3_34 11 IO_L20N_T3_34 GND 11 IO_L21P_T3_DQS_34 11 IO_L21N_T3_DQS_34 GND 2 IO_L15P_T2_DQS_34 12 IO_L15P_T2_DQS_34 12	JB2_SMB_NINT JB2_SMB_NINT JB2_SMB_NINT JB3_4	R19 P16 P15 R17 R16 W19 W18	65.687 43.091 43.091 43.092 43.090 43.097 43.083
43.088 43.092 43.087 43.090 43.083 43.086 25.380 25.392	N19 N17 P18 T17 R18 V17 V18 T20 U20	VIO_OUT (+3.3V) 1	JB2_SMB_NINT JB2_SMB_NINT JB2_SMB_NINT JB3_4	R19 P16 P15 R17 R16 W19 W18	65.687 43.091 43.091 43.092 43.090 43.097 43.083 25.376 25.388
43.088 43.092 43.087 43.090 43.083 43.086 25.380 25.392	N19 N17 P18 T17 R18 V17 V18 T20 U20	VIO_OUT (+3.3V) 1	JB2_SMB_NINT JB2_SMB_NINT JB2_SMB_NINT JB2_SMB_NINT JB3_4	R19 P16 P15 R17 R16 W19 W18 W16 V16	65.687 43.091 43.091 43.092 43.090 43.097 43.083 25.376 25.388
43.088 43.092 43.087 43.090 43.083 43.086 25.380 25.392	N19 N17 P18 T17 R18 V17 V18 T20 U20	VIO_OUT (+3.3V) 1 VCCO_34 3 IO_L23P_T3_34 5 IO_L23N_T3_34 7 GND 9 IO_L20P_T3_34 1 IO_L20N_T3_34 1 GND 11 IO_L21P_T3_DQS_34 1 IO_L21N_T3_DQS_34 1 IO_L15P_T2_DQS_34 20 IO_L15P_T2_DQS_34 20 IO_L16P_T2_34 30 IO_L16P_T2_34 30	JB2_SMB_NINT  JB	R19 P16 P15 R17 R16 W19 W18	65.687 43.091 43.091 43.092 43.090 43.097 43.083 25.376 25.388
43.088 43.092 43.087 43.090 43.083 43.086 25.380 25.392 25.392 25.378	N19 N17 P18 T17 R18 V17 V18 T20 U20 V20 W20	VIO_OUT (+3.3V) 1	JB2_SMB_NINT JB2_SMB_NINT JB2_SMB_NINT JB3_4	R19 P16 P15 R17 R16 W19 W18 W16 V16 Y19 Y18	65.687 43.091 43.091 43.092 43.090 43.097 43.083 25.376 25.388 25.381
43.088 43.092 43.087 43.090 43.083 43.086 25.380 25.392 25.378 25.378	N19 N17 P18 T17 R18 V17 V18 T20 U20 V20 W20 N20	VIO_OUT (+3.3V) 1	JB2_SMB_NINT  JB	R19 P16 P15 R17 R16 W19 W18 W16 V16 Y19 Y18	65.687 43.091 43.091 43.092 43.090 43.097 43.083 25.376 25.388 25.381 25.379
43.088 43.092 43.087 43.090 43.083 43.086 25.380 25.392 25.392 25.378	N19 N17 P18 T17 R18 V17 V18 T20 U20 V20 W20	VIO_OUT (+3.3V) 1	JB2_SMB_NINT  JB	R19 P16 P15 R17 R16 W19 W18 W16 V16 Y19 Y18	65.687 43.091 43.091 43.092 43.090 43.097 43.083 25.376 25.388 25.381

Net Length (mm)	FPGA Pin	FPGA Signal_	JC1 FPGA Signal	FPGA Pin	Net Length (mm)
		VIO_OUT (+3.3V)	1 2 JC1_SMB_nINT		
	Т8	VCCO_13		V5	100.858
62.825	<b>U</b> 7	IO_L11P_T1_SRCC_13		U10	62.935
62.828	V7	IO_L11N_T1_SRCC_13		Т9	62.897
			9 10 GND		
62.837	Т5		11 12 IO_L20N_T3_13	Y13	62.750
62.835	U5	IO_L19N_T3_VREF_13 GND	13	Y12	62.723
62.823	V11	IO L21P T3 DQS 13	17 18 IO_L22N_T3_13	W6	63.185
62.834	V10		19 20 IO_L22P_T3_13	V6	63.089
		GND	21 22 GND		
60.578	V8	IO_L15P_T2_DQS_13	23 24 IO_L18N_T2_13	Y11	60.516
60.556	W8	IO_L15N_T2_DQS_13	25 26 IO_L18P_T2_13	W11	60.505
			27 28 GND		
60.561	U9		29 30 IO_L16N_T2_13	W9	60.558
60.570	U8	IO_L17N_T2_13	31 32 IO_L16P_T2_13	W10	60.565
			33 34 GND		
60.570	¥9		35 36 IO_L13N_T2_MRCC_13		60.566
60.563	Y8		37 38 IO_L13P_T2_MRCC_13	¥7	60.562
		SMB_I2C_SCL4	39 40 SMB_I2C_SDA4		
Net Length (mm)	FPGA Pin	FPGA Signal_		FPGA Pin	Net Length (mm)
Net Length (mm)	FPGA Pin	VIO_OUT (+3.3V)	1 2 PS_MIO52_501	C10	Net Length (mm)
· · · ·	A13	VIO_OUT (+3.3V) VCCO_MIO1_501 (+1.8V)	1 2 PS_MIO52_501 3 4 PS_MIO53_501	C10 C11	
79.274	A13 A19	VIO_OUT (+3.3V) VCCO_MIO1_501 (+1.8V) PS_MIO16_501	1 2 PS_MIO52_501 3 4 PS_MIO53_501 5 6 PS_MIO19_501	C10 C11 D10	79.294
· · · ·	A13	VIO_OUT (+3.3V) VCCO_MIO1_501 (+1.8V) PS_MIO16_501 PS_MIO17_501	1 2 PS_MIO52_501 3 4 PS_MIO53_501 5 6 PS_MIO19_501 7 8 PS_MIO18_501	C10 C11	
79.274 79.314	A13 A19 E14	VIO_OUT (+3.3V) VCCO_MIO1_501 (+1.8V) PS_MIO16_501 PS_MIO17_501 GND	1 2 PS_MIO52_501 3 4 PS_MIO53_501 5 6 PS_MIO19_501 7 8 PS_MIO18_501 9 10 GND	C10 C11 D10 B18	79.294 79.283
79.274 79.314 79.277	A13 A19 E14 A17	VIO_OUT (+3.3V) VCCO_MIO1_501 (+1.8V) PS_MIO16_501 PS_MIO17_501 GND PS_MIO20_501	1 2 PS_MIO52_501 3 4 PS_MIO53_501 5 6 PS_MIO19_501 7 8 PS_MIO18_501 9 10 GND 11 12 PS_MIO23_501	C10 C11 D10 B18	79.294 79.283 79.276
79.274 79.314	A13 A19 E14	VIO_OUT (+3.3V) VCCO_MIO1_501 (+1.8V) PS_MIO16_501 PS_MIO17_501 GND PS_MIO20_501 PS_MIO21_501	1 2 PS_MIO52_501 3 4 PS_MIO53_501 5 6 PS_MIO19_501 7 8 PS_MIO18_501 9 10 GND 11 12 PS_MIO23_501 13 14 PS_MIO22_501	C10 C11 D10 B18	79.294 79.283
79.274 79.314 79.277 79.287	A13 A19 E14 A17 F14	VIO_OUT (+3.3V)  VCCO_MIO1_501 (+1.8V)  PS_MIO16_501  PS_MIO17_501  GND  PS_MIO20_501  PS_MIO21_501  GND	1 2 PS_MIO52_501 3 4 PS_MIO53_501 5 6 PS_MIO19_501 7 8 PS_MIO18_501 9 10 GND 11 12 PS_MIO23_501 13 14 PS_MIO22_501 15 16 GND	C10 C11 D10 B18 D11 B17	79.294 79.283 79.276 79.455
79.274 79.314 79.277 79.287	A13 A19 E14 A17 F14	VIO_OUT (+3.3V)  VCCO_MIO1_501 (+1.8V)  PS_MIO16_501  PS_MIO17_501  GND  PS_MIO20_501  PS_MIO21_501  GND  PS_MIO24_501	1 2 PS_MIO52_501 3 4 PS_MIO53_501 5 6 PS_MIO19_501 7 8 PS_MIO18_501 9 10 GND 11 12 PS_MIO23_501 13 14 PS_MIO22_501 15 16 GND 17 18 PS_MIO27_501	C10 C11 D10 B18 D11 B17	79.294 79.283 79.276 79.455
79.274 79.314 79.277 79.287	A13 A19 E14 A17 F14	VIO_OUT (+3.3V)  VCCO_MIO1_501 (+1.8V)  PS_MIO16_501  PS_MIO17_501  GND  PS_MIO20_501  PS_MIO21_501  GND  PS_MIO24_501  PS_MIO25_501	1 2 PS_MIO52_501 3 4 PS_MIO53_501 5 6 PS_MIO19_501 7 8 PS_MIO18_501 9 10 GND 11 12 PS_MIO23_501 13 14 PS_MIO22_501 15 16 GND 17 18 PS_MIO27_501 19 20 PS_MIO26_501	C10 C11 D10 B18 D11 B17	79.294 79.283 79.276 79.455
79.274 79.314 79.277 79.287 79.284 79.292	A13 A19 E14 A17 F14 A16 F15	VIO_OUT (+3.3V)  VCCO_MIO1_501 (+1.8V)  PS_MIO16_501  PS_MIO17_501  GND  PS_MIO20_501  PS_MIO21_501  GND  PS_MIO24_501  PS_MIO25_501  GND  OND	1 2 PS_MIO52_501 3 4 PS_MIO53_501 5 6 PS_MIO19_501 7 8 PS_MIO18_501 9 10 GND 11 12 PS_MIO23_501 13 14 PS_MIO22_501 15 16 GND 17 18 PS_MIO27_501 19 20 PS_MIO26_501 21 22 GND	C10 C11 D10 B18 D11 B17	79.294 79.283 79.276 79.455 79.265 79.289
79.274 79.314 79.277 79.287 79.284 79.292	A13 A19 E14 A17 F14 A16 F15	VIO_OUT (+3.3V)  VCCO_MIO1_501 (+1.8V)  PS_MIO16_501  PS_MIO17_501  GND  PS_MIO20_501  PS_MIO21_501  GND  PS_MIO24_501  PS_MIO25_501  GND  PS_MIO28_501	1 2 PS_MIO52_501 3 4 PS_MIO53_501 5 6 PS_MIO19_501 7 8 PS_MIO18_501 9 10 GND 11 12 PS_MIO23_501 13 14 PS_MIO22_501 15 16 GND 17 18 PS_MIO27_501 19 20 PS_MIO26_501 21 22 GND 23 24 PS_MIO31_501	C10 C11 D10 B18 D11 B17 D13 A15	79.294 79.283 79.276 79.455 79.265 79.289
79.274 79.314 79.277 79.287 79.284 79.292	A13 A19 E14 A17 F14 A16 F15	VIO_OUT (+3.3V)  VCCO_MIO1_501 (+1.8V)  PS_MIO16_501  PS_MIO17_501  GND  PS_MIO20_501  PS_MIO21_501  GND  PS_MIO24_501  PS_MIO25_501  GND  PS_MIO28_501  PS_MIO29_501	1 2 PS_MIO52_501 3 4 PS_MIO53_501 5 6 PS_MIO19_501 7 8 PS_MIO18_501 9 10 GND 11 12 PS_MIO23_501 13 14 PS_MIO22_501 15 16 GND 17 18 PS_MIO27_501 19 20 PS_MIO26_501 21 22 GND 23 24 PS_MIO31_501 25 26 PS_MIO30_501	C10 C11 D10 B18 D11 B17	79.294 79.283 79.276 79.455 79.265 79.289
79.274 79.314 79.277 79.287 79.284 79.292 63.747 63.800	A13 A19 E14 A17 F14 A16 F15 C16 C13	VIO_OUT (+3.3V)  VCCO_MIO1_501 (+1.8V)  PS_MIO16_501  PS_MIO17_501  GND  PS_MIO20_501  PS_MIO21_501  GND  PS_MIO24_501  PS_MIO25_501  GND  PS_MIO28_501  PS_MIO29_501  GND	1 2 PS_MIO52_501 3 4 PS_MIO53_501 5 6 PS_MIO19_501 7 8 PS_MIO18_501 9 10 GND 11 12 PS_MIO23_501 13 14 PS_MIO22_501 15 16 GND 17 18 PS_MIO27_501 19 20 PS_MIO26_501 21 22 GND 23 24 PS_MIO31_501 25 26 PS_MIO30_501 27 28 GND	C10 C11 D10 B18 D11 B17 D13 A15	79.294 79.283 79.276 79.455 79.265 79.289 63.679 63.781
79.274 79.314 79.277 79.287 79.284 79.292 63.747 63.800	A13 A19 E14 A17 F14 A16 F15 C16 C13	VIO_OUT (+3.3V)  VCCO_MIO1_501 (+1.8V)  PS_MIO16_501  PS_MIO17_501  GND  PS_MIO20_501  PS_MIO21_501  GND  PS_MIO24_501  PS_MIO25_501  GND  PS_MIO28_501  PS_MIO29_501  GND  PS_MIO29_501  GND  PS_MIO29_501  GND  PS_MIO32_501	1 2 PS_MIO52_501 3 4 PS_MIO53_501 5 6 PS_MIO19_501 7 8 PS_MIO18_501 9 10 GND 11 12 PS_MIO23_501 13 14 PS_MIO22_501 15 16 GND 17 18 PS_MIO27_501 19 20 PS_MIO26_501 21 22 GND 23 24 PS_MIO31_501 25 26 PS_MIO30_501 27 28 GND 29 30 PS_MIO35_501	C10 C11 D10 B18 D11 B17 D13 A15 E16 C15	79.294 79.283 79.276 79.455 79.265 79.289 63.679 63.781
79.274 79.314 79.277 79.287 79.284 79.292 63.747 63.800	A13 A19 E14 A17 F14 A16 F15 C16 C13	VIO_OUT (+3.3V)  VCCO_MIO1_501 (+1.8V)  PS_MIO16_501  PS_MIO17_501  GND  PS_MIO20_501  PS_MIO21_501  GND  PS_MIO24_501  PS_MIO25_501  GND  PS_MIO28_501  PS_MIO29_501  GND  PS_MIO32_501  PS_MIO32_501  PS_MIO33_501	1 2 PS_MIO52_501 3 4 PS_MIO53_501 5 6 PS_MIO19_501 7 8 PS_MIO18_501 9 10 GND 11 12 PS_MIO23_501 13 14 PS_MIO22_501 15 16 GND 17 18 PS_MIO27_501 19 20 PS_MIO26_501 21 22 GND 23 24 PS_MIO31_501 25 26 PS_MIO30_501 27 28 GND	C10 C11 D10 B18 D11 B17 D13 A15	79.294 79.283 79.276 79.455 79.265 79.289 63.679 63.781
79.274 79.314 79.277 79.287 79.284 79.292 63.747 63.800	A13 A19 E14 A17 F14 A16 F15 C16 C13	VIO_OUT (+3.3V)  VCCO_MIO1_501 (+1.8V)  PS_MIO16_501  PS_MIO17_501  GND  PS_MIO20_501  PS_MIO21_501  PS_MIO24_501  PS_MIO25_501  GND  PS_MIO28_501  PS_MIO29_501  PS_MIO32_501  PS_MIO32_501  PS_MIO32_501  PS_MIO33_501  GND	1 2 PS_MIO52_501 3 4 PS_MIO53_501 5 6 PS_MIO19_501 7 8 PS_MIO18_501 9 10 GND 11 12 PS_MIO23_501 13 14 PS_MIO22_501 15 16 GND 17 18 PS_MIO27_501 19 20 PS_MIO26_501 21 22 GND 23 24 PS_MIO31_501 25 26 PS_MIO30_501 27 28 GND 29 30 PS_MIO35_501 31 32 PS_MIO34_501	C10 C11 D10 B18 D11 B17 D13 A15 E16 C15	79.294 79.283 79.276 79.455 79.265 79.289 63.679 63.781
79.274 79.314 79.277 79.287 79.284 79.292 63.747 63.800 63.687 63.700	A13 A19 E14 A17 F14 A16 F15 C16 C13 A14 D15	VIO_OUT (+3.3V)  VCCO_MIO1_501 (+1.8V)  PS_MIO16_501  PS_MIO17_501  GND  PS_MIO20_501  PS_MIO21_501  GND  PS_MIO24_501  PS_MIO25_501  GND  PS_MIO28_501  PS_MIO29_501  GND  PS_MIO32_501  PS_MIO32_501  PS_MIO33_501  GND  PS_MIO36_501	1 2 PS_MIO52_501 3 4 PS_MIO53_501 5 6 PS_MIO19_501 7 8 PS_MIO18_501 9 10 GND 11 12 PS_MIO23_501 13 14 PS_MIO22_501 15 16 GND 17 18 PS_MIO27_501 19 20 PS_MIO26_501 21 22 GND 23 24 PS_MIO31_501 25 26 PS_MIO30_501 27 28 GND 29 30 PS_MIO35_501 31 32 PS_MIO34_501 33 34 GND	C10 C11 D10 B18 D11 B17 D13 A15 E16 C15	79.294 79.283 79.276 79.455 79.265 79.289 63.679 63.781 63.747 63.680

Net Length (mm)	FPGA Pin	FPGA Signal	J	T2	FPGA Signal	FPGA Pin	Net	Length	(mm)
	K9	VP_0	1	2	VN_0	L10			
		DAC_OUT1/ADC_IN4	3	4	DAC_OUT2/ADC_IN5				
		GND	5	6	GND				
		SMB_I2C_SCL7	7	8	SMB_I2C_SDA7				
		GND	9	10	GND				
		BT_AUD_CLK	11	12	BT_AUD_FSYNC				
		BT_AUD_OUT							
		MCU_SWCLK/PA13	15	16	MCU_SWDIO/PA14				
		MCU_nRST	17	18	MCU_VDDIO2 (+3.3V)				
		<pre>Zynq nRST(PS_SRST_B_501)</pre>			GND				
	F9	TCK_0	21	22	TDI_0	G6			
	J6	TMS_0				F6			
	K6	VCCO_0 (+3.3V)	25	26	PGND				
		GND	27	28	GND				
		VIN (+3.7-17V)	29	30	VIN (+3.7-17V)				