

Net Length (mm)	FPGA Pin	FPGA Signal	JA1	FPGA Signal	FPGA Pin	Net Length (mm)
		VIO_OUT (+3.3V)	1	2		
	C19	VCCO_35	3	4	G14	36.462
67.528	E18	IO_L5P_T0_AD9P_35	5	6	D20	67.516
67.517	E19	IO_L5N_T0_AD9N_35	7	8	D19	67.527
		GND	9	10		
67.521	F16	IO_L6P_T0_35	11	12	B20	67.682
67.524	F17	IO_L6N_T0_VREF_35	13	14	C20	67.533
		GND	15	16		
67.516	E17	IO_L3P_T0_DQS_AD1P_35	17	18	A20	67.530
67.531	D18	IO_L3N_T0_DQS_AD1N_35	19	20	B19	67.521
		GND	21	22		
67.137	F19	IO_L15P_T2_DQS_AD12P_35	23	24	G20	67.143
67.155	F20	IO_L15N_T2_DQS_AD12N_35	25	26	G19	67.150
		GND	27	28		
67.155	J20	IO_L17P_T2_AD5P_35	29	30	G18	67.152
67.139	H20	IO_L17N_T2_AD5N_35	31	32	G17	67.148
		GND	33	34		
67.156	J18	IO_L14P_T2_AD4P_SRCC_35	35	36	H17	67.149
67.133	H18	IO_L14N_T2_AD4N_SRCC_35	37	38	H16	67.134
		SMB_I2C_SCL0	39	40		

Net Length (mm)	FPGA Pin	FPGA Signal	JA2	FPGA Signal	FPGA Pin	Net Length (mm)
		VIO_OUT (+3.3V)	1	2		
	C19	VCCO_35	3	4	J15	21.780
37.541	L14	IO_L22P_T3_AD7P_35	5	6	J16	37.545
37.550	L15	IO_L22N_T3_AD7N_35	7	8	K16	37.542
		GND	9	10		
37.547	M14	IO_L23P_T3_35	11	12	G15	37.543
37.543	M15	IO_L23N_T3_35	13	14	H15	37.550
		GND	15	16		
37.563	N15	IO_L21P_T3_DQS_AD14P_35	17	18	J14	37.557
37.549	N16	IO_L21N_T3_DQS_AD14N_35	19	20	K14	37.546
		GND	21	22		
65.700	L19	IO_L9P_T1_DQS_AD3P_35	23	24	J19	65.691
65.693	L20	IO_L9N_T1_DQS_AD3N_35	25	26	K19	65.689
		GND	27	28		
65.710	M17	IO_L8P_T1_AD10P_35	29	30	M20	65.704
65.703	M18	IO_L8N_T1_AD10N_35	31	32	M19	65.714
		GND	33	34		
65.703	L16	IO_L11P_T1_SRCC_35	35	36	K18	65.666
65.688	L17	IO_L11N_T1_SRCC_35	37	38	K17	65.704
		SMB_I2C_SCL1	39	40		

Net Length (mm)	FPGA Pin	FPGA Signal	JB1	FPGA Signal	FPGA Pin	Net Length (mm)
		VIO_OUT (+3.3V)	1	2	JB1_SMB_nINT	
	N19	VCCO_34	3	4	IO_25_34	T19 56.313
65.260	T11	IO_L1P_T0_34	5	6	IO_L2N_T0_34	U12 65.247
65.256	T10	IO_L1N_T0_34	7	8	IO_L2P_T0_34	T12 65.252
		GND	9	10	GND	
65.261	P14	IO_L6P_T0_34	11	12	IO_L4N_T0_34	W13 65.232
65.264	R14	IO_L6N_T0_VREF_34	13	14	IO_L4P_T0_34	V12 65.257
		GND	15	16	GND	
65.256	U13	IO_L3P_T0_DQS_PUDC_B_34	17	18	IO_L5N_T0_34	T15 65.271
65.264	V13	IO_L3N_T0_DQS_34	19	20	IO_L5P_T0_34	T14 65.267
		GND	21	22	GND	
37.095	T16	IO_L9P_T1_DQS_34	23	24	IO_L7N_T1_34	Y17 37.097
37.107	U17	IO_L9N_T1_DQS_34	25	26	IO_L7P_T1_34	Y16 37.103
		GND	27	28	GND	
37.095	W14	IO_L8P_T1_34	29	30	IO_L10N_T1_34	W15 37.096
37.109	Y14	IO_L8N_T1_34	31	32	IO_L10P_T1_34	V15 37.100
		GND	33	34	GND	
37.090	U14	IO_L11P_T1_SRCC_34	35	36	IO_L12N_T1_MRCC_34	U19 37.104
37.107	U15	IO_L11N_T1_SRCC_34	37	38	IO_L12P_T1_MRCC_34	U18 37.088
		SMB_I2C_SCL2	39	40	SMB_I2C_SDA2	

Net Length	FPGA Pin	FPGA Signal	JB2	FPGA Signal	FPGA Pin	Net Length (mm)
		VIO_OUT (+3.3V)	1	2	JB2_SMB_nINT	
	N19	VCCO_34	3	4	IO_0_34	R19 65.687
43.088	N17	IO_L23P_T3_34	5	6	IO_L24N_T3_34	P16 43.091
43.092	P18	IO_L23N_T3_34	7	8	IO_L24P_T3_34	P15 43.091
		GND	9	10	GND	
43.087	T17	IO_L20P_T3_34	11	12	IO_L19N_T3_VREF_34	R17 43.092
43.090	R18	IO_L20N_T3_34	13	14	IO_L19P_T3_34	R16 43.090
		GND	15	16	GND	
43.083	V17	IO_L21P_T3_DQS_34	17	18	IO_L22N_T3_34	W19 43.097
43.086	V18	IO_L21N_T3_DQS_34	19	20	IO_L22P_T3_34	W18 43.083
		GND	21	22	GND	
25.380	T20	IO_L15P_T2_DQS_34	23	24	IO_L18N_T2_34	W16 25.376
25.392	U20	IO_L15N_T2_DQS_34	25	26	IO_L18P_T2_34	V16 25.388
		GND	27	28	GND	
25.392	V20	IO_L16P_T2_34	29	30	IO_L17N_T2_34	Y19 25.381
25.378	W20	IO_L16N_T2_34	31	32	IO_L17P_T2_34	Y18 25.379
		GND	33	34	GND	
25.383	N20	IO_L14P_T2_SRCC_34	35	36	IO_L13N_T2_MRCC_34	P19 25.387
25.381	P20	IO_L14N_T2_SRCC_34	37	38	IO_L13P_T2_MRCC_34	N18 25.381
		SMB_I2C_SCL3	39	40	SMB_I2C_SDA3	

Net Length (mm)	FPGA Pin	FPGA Signal	JC1	FPGA Signal	FPGA Pin	Net Length (mm)
		VIO_OUT (+3.3V)	1 2	JC1_SMB_nINT		
	T8	VCCO_13	3 4	IO_L6N_T0_VREF_13	V5	100.858
62.825	U7	IO_L11P_T1_SRCC_13	5 6	IO_L12N_T1_MRCC_13	U10	62.935
62.828	V7	IO_L11N_T1_SRCC_13	7 8	IO_L12P_T1_MRCC_13	T9	62.897
		GND	9 10	GND		
62.837	T5	IO_L19P_T3_13	11 12	IO_L20N_T3_13	Y13	62.750
62.835	U5	IO_L19N_T3_VREF_13	13 14	IO_L20P_T3_13	Y12	62.723
		GND	15 16	GND		
62.823	V11	IO_L21P_T3_DQS_13	17 18	IO_L22N_T3_13	W6	63.185
62.834	V10	IO_L21N_T3_DQS_13	19 20	IO_L22P_T3_13	V6	63.089
		GND	21 22	GND		
60.578	V8	IO_L15P_T2_DQS_13	23 24	IO_L18N_T2_13	Y11	60.516
60.556	W8	IO_L15N_T2_DQS_13	25 26	IO_L18P_T2_13	W11	60.505
		GND	27 28	GND		
60.561	U9	IO_L17P_T2_13	29 30	IO_L16N_T2_13	W9	60.558
60.570	U8	IO_L17N_T2_13	31 32	IO_L16P_T2_13	W10	60.565
		GND	33 34	GND		
60.570	Y9	IO_L14P_T2_SRCC_13	35 36	IO_L13N_T2_MRCC_13	Y6	60.566
60.563	Y8	IO_L14N_T2_SRCC_13	37 38	IO_L13P_T2_MRCC_13	Y7	60.562
		SMB_I2C_SCL4	39 40	SMB_I2C_SDA4		

Net Length (mm)	FPGA Pin	FPGA Signal	J3	FPGA Signal	FPGA Pin	Net Length (mm)
		VIO_OUT (+3.3V)	1 2	PS_MIO52_501	C10	
	A13	VCCO_MIO1_501 (+1.8V)	3 4	PS_MIO53_501	C11	
79.274	A19	PS_MIO16_501	5 6	PS_MIO19_501	D10	79.294
79.314	E14	PS_MIO17_501	7 8	PS_MIO18_501	B18	79.283
		GND	9 10	GND		
79.277	A17	PS_MIO20_501	11 12	PS_MIO23_501	D11	79.276
79.287	F14	PS_MIO21_501	13 14	PS_MIO22_501	B17	79.455
		GND	15 16	GND		
79.284	A16	PS_MIO24_501	17 18	PS_MIO27_501	D13	79.265
79.292	F15	PS_MIO25_501	19 20	PS_MIO26_501	A15	79.289
		GND	21 22	GND		
63.747	C16	PS_MIO28_501	23 24	PS_MIO31_501	E16	63.679
63.800	C13	PS_MIO29_501	25 26	PS_MIO30_501	C15	63.781
		GND	27 28	GND		
63.687	A14	PS_MIO32_501	29 30	PS_MIO35_501	F12	63.747
63.700	D15	PS_MIO33_501	31 32	PS_MIO34_501	A12	63.680
		GND	33 34	GND		
63.691	A11	PS_MIO36_501	35 36	PS_MIO39_501	C18	63.685
63.687	A10	PS_MIO37_501	37 38	PS_MIO38_501	E13	63.691
		SMB_I2C_SCL5	39 40	SMB_I2C_SDA5		

Net Length (mm)	FPGA Pin	FPGA Signal	J2	FPGA Signal	FPGA Pin	Net Length (mm)
	K9	VP_0	1	VN_0	L10	
		DAC_OUT1/ADC_IN4	3	DAC_OUT2/ADC_IN5		
		GND	5	GND		
		SMB_I2C_SCL7	7	SMB_I2C_SDA7		
		GND	9	GND		
		BT_AUD_CLK	11	BT_AUD_FSYNC		
		BT_AUD_OUT	13	BT_AUD_IN		
		MCU_SWCLK/PA13	15	MCU_SWDIO/PA14		
		MCU_nRST	17	MCU_VDDIO2 (+3.3V)		
		Zynq nRST(PS_SRST_B_501)	19	GND		
F9		TCK_0	21	TDI_0	G6	
J6		TMS_0	23	TDO_0	F6	
K6		VCCO_0 (+3.3V)	25	PGND		
		GND	27	GND		
		VIN (+3.7-17V)	29	VIN (+3.7-17V)		