

NUCLEO-WL5JC

MB1389

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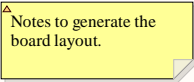
Sheet 8: ST-LINK/V3E-SWD Power Module

U_MB1389_Top
MB1389_Top.SchDoc



Legend

- General comment such as function title, configuration, ...
- Text to be added to silkscreen.
- Warning text.



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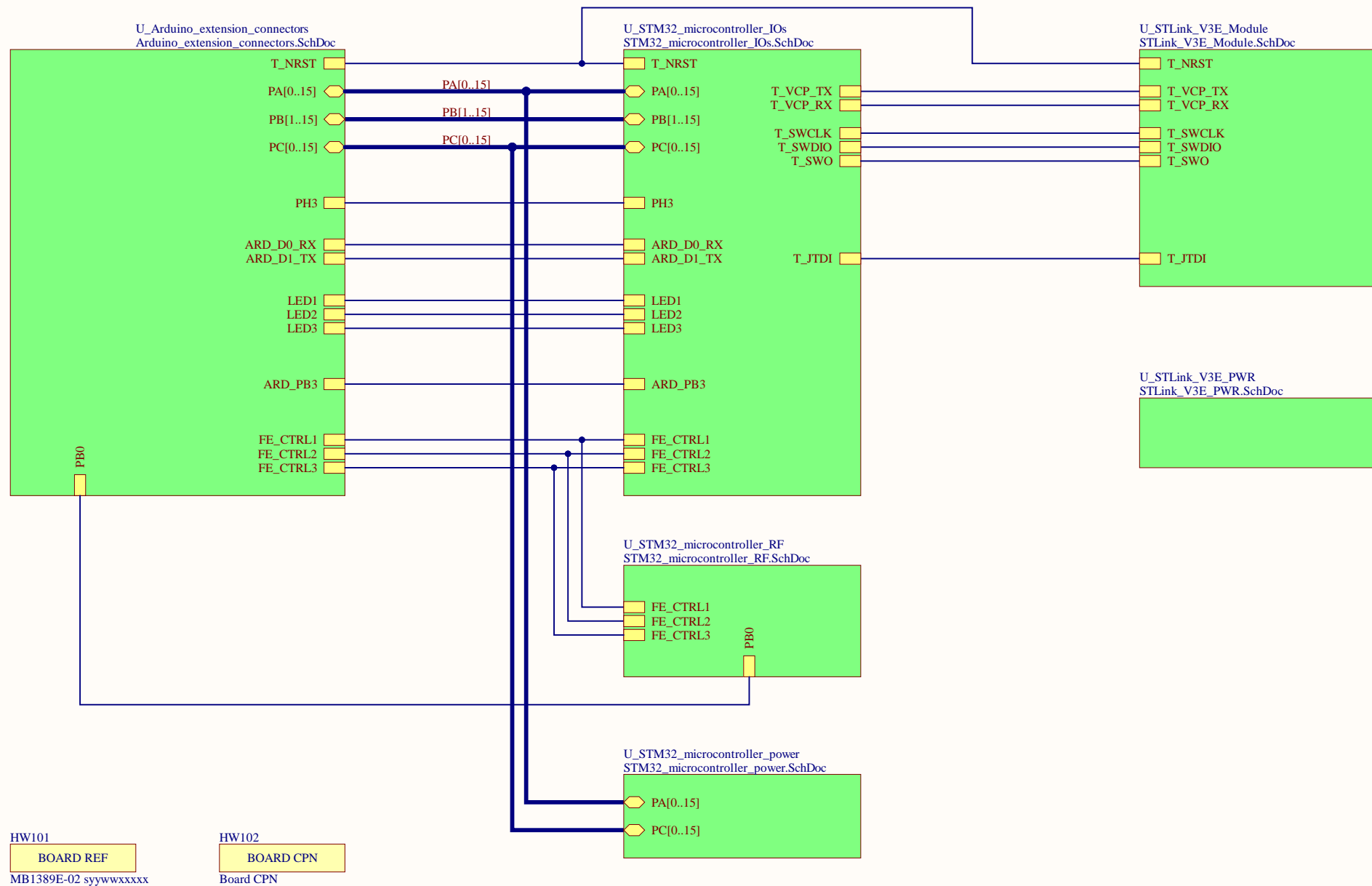
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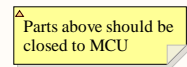
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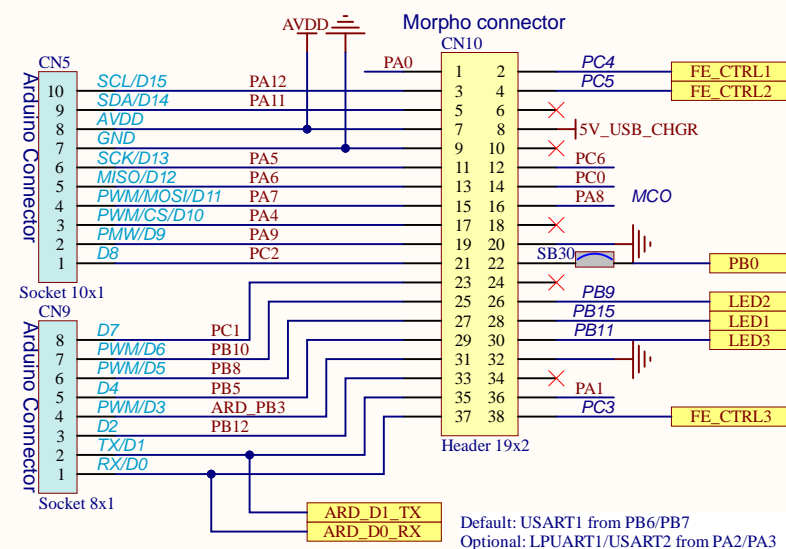
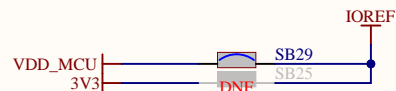
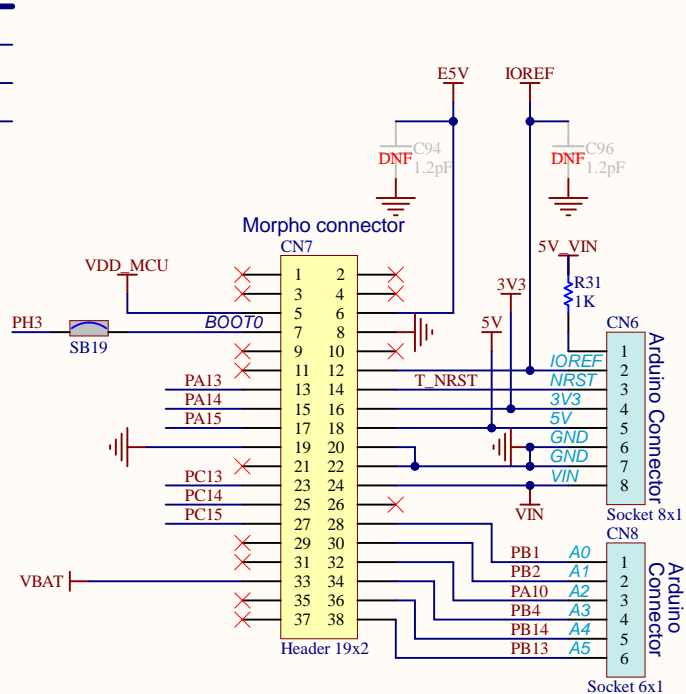
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Title: Project overview		
Project: NUCLEO-WL5JC		
Variant: Low-band		
Revision: E-02		Reference: MB1389
Size: A4	Date: 2020-April-28	Sheet: 1 of 8

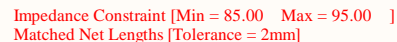






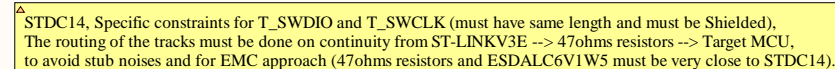


5V_USB_CHGR

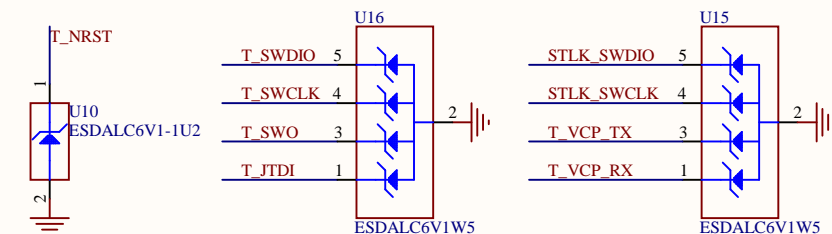


Capacitor C90 close to pin 1 of USB STLK connector.

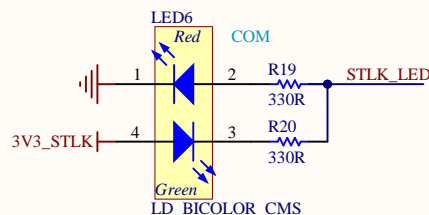
HW11 HW12 HW13 HW14 HW15 HW16



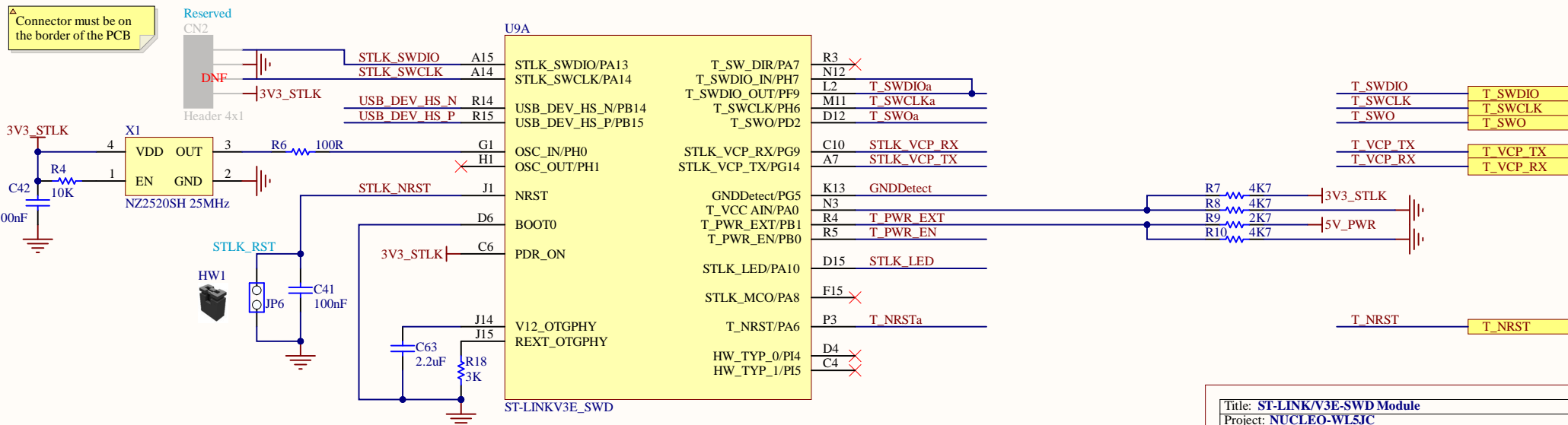
- Pin attributions on ESD can be swapped for layout optimisation



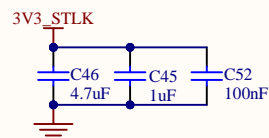
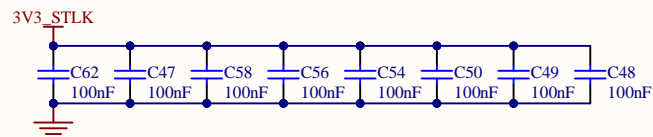
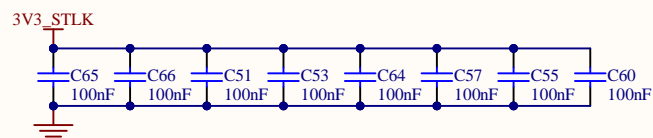
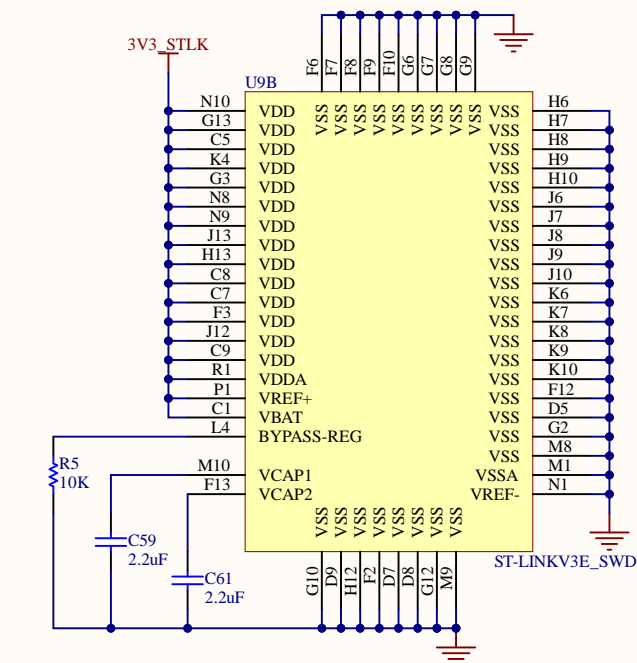
Re



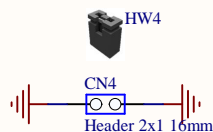
Connector must be on the border of the PCB



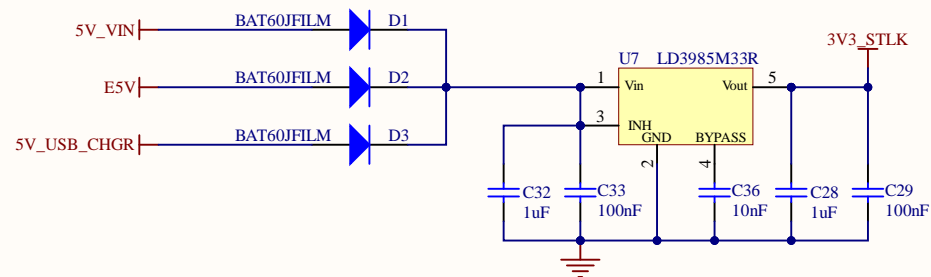
ST-LINKV3E SWD MCU POWER



GND

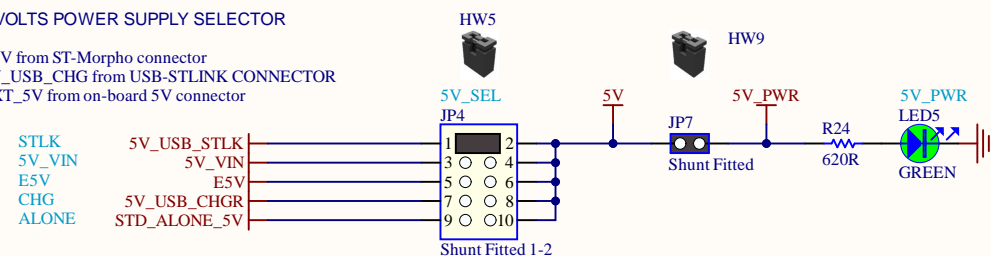


3.3 VOLTS POWER SUPPLY FOR ST-LINK

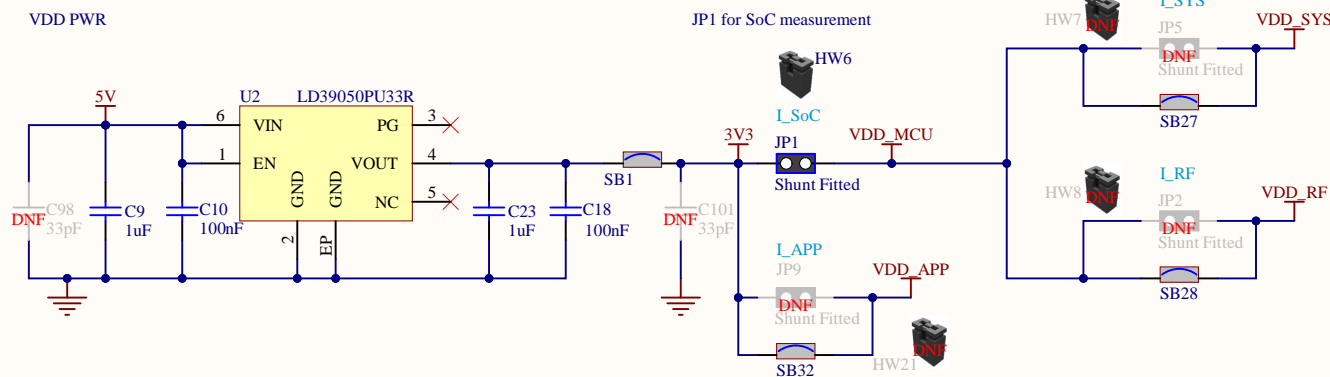


5 VOLTS POWER SUPPLY SELECTOR

E5V from ST-Morpho connector
5V_USB_CHG from USB-STLINK CONNECTOR
EXT_5V from on-board 5V connector



VDD PWR



VIN / 5V PWR

