

## Author details

[Back to results](#) | [< Previous](#) **2 of 22** [Next >](#)[Print](#) | [E-mail](#)

## Roberts, Gordon Walter

McGill University, Department of Electrical &  
Computer Engineering, Montreal, Canada

Author ID: 7403400413

[About Scopus Author Identifier](#) | [View potential author matches](#)Other name formats: Roberts, G. W.  
Roberts, Gordon W.  
Roberts, Gordon  
[View More](#)

Documents: 192

Citations: 2660 total citations by 1994 documents

h-index: 27

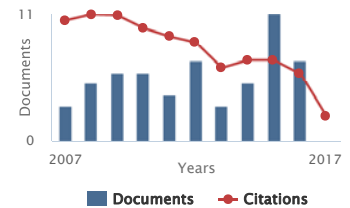
Co-authors: 98

Subject area: Engineering , Materials Science [View More](#)[Analyze author output](#)[View citation overview](#)[View h-graph](#)**192 Documents** | Cited by 1994 documents | 98 co-authors**192 documents** [View all in search results format](#)Sort on: **Date** [Cited by](#) [Export all](#) | [Add all to list](#) | [Set document alert](#) | [Set document feed](#)

A digitally programmable 50-150dB DC gain operational transconductance amplifier in 130nm CMOS	Yang, M., Roberts, G.W.	2016	14th IEEE International NEWCAS Conference, NEWCAS 2016	0
<a href="#">View at Publisher</a> <a href="#">Find it</a>				
Top-down design and synthesis of inherently-stable integrator-based high-order amplifiers	Shoukry, A.M., Roberts, G.W.	2016	14th IEEE International NEWCAS Conference, NEWCAS 2016	0
<a href="#">View at Publisher</a> <a href="#">Find it</a>				
A Jitter Injection Signal Generation and Extraction System for Embedded Test of High-Speed Data I/O	Li, Y., Bielby, S., Chowdhury, A., Roberts, G.W.	2016	Journal of Electronic Testing: Theory and Applications (JETTA)	1
<a href="#">View at Publisher</a> <a href="#">Find it</a>				
Design of high-order type-II delay-locked loops using a Gaussian transfer function approach	Li, Y., Roberts, G.W.	2016	Proceedings - IEEE International Symposium on Circuits and Systems	0
<a href="#">View at Publisher</a> <a href="#">Find it</a>				
Low-cost trimmable manufacturing methods for printable electronics	Gordon, A., Roberts, G.W., Fayomi, C.J.	2016	Proceedings - IEEE International Symposium on Circuits and Systems	0
<a href="#">View at Publisher</a> <a href="#">Find it</a>				
A Top-Down Design Methodology Encompassing Components Variations Due to Wide-Range Operation in Frequency Synthesizer PLLs	Abdelfattah, O., Gal, G., Roberts, G.W., Shih, I., Shih, Y.-C.	2016	IEEE Transactions on Very Large Scale Integration (VLSI) Systems	0
<a href="#">View at Publisher</a> <a href="#">Find it</a>				
Synthesis of High Gain Operational Transconductance Amplifiers for Closed-Loop Operation Using a Generalized Controller-Based Compensation Method	Yang, M., Roberts, G.W.	2016	IEEE Transactions on Circuits and Systems I: Regular Papers	0
<a href="#">View at Publisher</a> <a href="#">Find it</a>				
Quick and easy CMOS amplifier design and optimization	Roberts, G.W.	2015	2015 European Conference on Circuit Theory and Design, ECCTD 2015	1
<a href="#">View at Publisher</a> <a href="#">Find it</a>				
An ultra-low-voltage CMOS process-insensitive self-biased OTA with rail-to-rail input range	Abdelfattah, O., Roberts, G.W., Shih, I., Shih, Y.-C.	2015	IEEE Transactions on Circuits and Systems I: Regular Papers	7
<a href="#">View at Publisher</a> <a href="#">Find it</a>				

Follow this Author

Receive emails when this author publishes new articles

[Get citation alerts](#)[Add to ORCID](#)[Request author detail corrections](#)[Export profile to SciVal](#)

## Author History

Publication range: 1985 - 2016

References: [932](#)

## Source history:

[IEEE Transactions on Circuits and Systems II: Express B](#)[View docu](#)[Electronics Letters](#)[View docu](#)[IEEE Design and Test](#)[View docu](#)[View More](#)[Show Related Affiliations](#)

A 0.6V-supply bandgap reference in 65 nm CMOS	Abdelfattah, O., Shih, I., Roberts, G., Shih, Y.-C.	2015	Conference Proceedings - 13th IEEE International NEW Circuits and Systems Conference, NEWCAS 2015	0
View at Publisher <a href="#">Find it</a> <a href="#">NTU</a>				
A jitter injection signal generation and extraction system for embedded test of high-speed data I/O	Li, Y., Bielby, S., Chowdhury, A., Roberts, G.W.	2015	Proceedings of the 2015 IEEE 20th International Mixed-Signals Test Workshop, IMSTW 2015	0
View at Publisher <a href="#">Find it</a> <a href="#">NTU</a>				
A sub-THz folded substrate integrated waveguide in IBM 130 nm CMOS process	Mahani, M.S., Roberts, G.	2015	2015 Global Symposium on Millimeter-Waves, GSMM 2015	0
View at Publisher <a href="#">Find it</a> <a href="#">NTU</a>				
Guest editors' introduction: Speeding up analog integration and test for mixed-signal SoCs	Parekhji, R., Butler, K., Roberts, G.	2015	IEEE Design and Test	0
View at Publisher <a href="#">Find it</a> <a href="#">NTU</a>				
An embedded probabilistic extraction unit for on-chip jitter measurements	Bielby, S., Roberts, G.W.	2015	Proceedings - IEEE International Symposium on Circuits and Systems	0
View at Publisher <a href="#">Find it</a> <a href="#">NTU</a>				
A 0.35-V bulk-driven self-biased OTA with rail-to-rail input range in 65 nm CMOS	Abdelfattah, O., Roberts, G., Shih, I., Shih, Y.-C.	2015	Proceedings - IEEE International Symposium on Circuits and Systems	1
View at Publisher <a href="#">Find it</a> <a href="#">NTU</a>				
A 0.55-V 1-GHz frequency synthesizer PLL for ultra-low-voltage ultra-low-power applications	Abdelfattah, O., Shih, I., Roberts, G., Shih, Y.-C.	2015	2015 IEEE 6th Latin American Symposium on Circuits and Systems, LASCAS 2015 - Conference Proceedings	0
View at Publisher <a href="#">Find it</a> <a href="#">NTU</a>				
Wide linear range voltage-controlled delay unit for time-mode signal processing	Ziabakhsh, S., Gagnon, G., Roberts, G.W.	2015	Proceedings - IEEE International Symposium on Circuits and Systems	0
View at Publisher <a href="#">Find it</a> <a href="#">NTU</a>				
Automated characterization of SIP MZI-based switches	Hai, M.S., Fard, M.M.P., An, D., (...), Roberts, G.W., Liboiron-Ladouceur, O.	2015	2015 IEEE Optical Interconnects Conference, OI 2015	1
View at Publisher <a href="#">Find it</a> <a href="#">NTU</a>				
Reducing the analog-digital productivity gap using time-mode signal processing	Roberts, G.W.	2014	Proceedings - IEEE International Symposium on Circuits and Systems	2
View at Publisher <a href="#">Find it</a> <a href="#">NTU</a>				
Sub-gate-delay edge-control of a clock signal using DLLs and $\Sigma\Delta$ modulation techniques	Bielby, S., Roberts, G.W.	2014	Canadian Conference on Electrical and Computer Engineering	2
View at Publisher <a href="#">Find it</a> <a href="#">NTU</a>				

Display:  results per page

Page 1

[Back to results](#) | [< Previous](#) **2 of 22** [Next >](#)[Top of page](#)

The data displayed above is compiled exclusively from articles published in the Scopus database. To request corrections to any inaccuracies or provide any further feedback, please [contact us](#) (registration required).  
 The data displayed above is subject to the privacy conditions contained in the [privacy policy](#).

[Scopus API](#)  
[Privacy matters](#)

[Русский язык](#)

---

**ELSEVIER**

[Terms and conditions](#)

[Privacy policy](#)

Copyright © 2017 [Elsevier B.V.](#) All rights reserved. Scopus® is a registered trademark of Elsevier B.V.

Cookies are set by this site. To decline them or learn more, visit our [Cookies page](#).

 RELX Group