Welcome...

prof.dr.ir. B. Nauta (Bram)

Distinguished Professor (Universiteitshoogleraar)

About Me

Bram Nauta was born in 1964 in Hengelo, The Netherlands. In 1987 he received the M.Sc degree (cum laude) in electrical engineering from the University of Twente, Enschede, The Netherlands. In 1991 he received the Ph.D. degree from the same university on the subject of analog CMOS filters for very high frequencies. In 1991 he joined the Mixed-Signal Circuits and Systems Department of Philips Research, Eindhoven the Netherlands. In 1998 he returned to the University of Twente, where he is currently a distinguished professor, heading the IC Design group. Since 2016 he also serves as chair of the EE department at this university. His current research interest is high-speed analog CMOS circuits, software defined radio, cognitive radio and beamforming.

He served as the Editor-in-Chief (2007-2010) of the IEEE Journal of Solid-State Circuits (JSSC), and was the 2013 program chair of the International Solid State Circuits Conference (ISSCC). He is currently the President of the IEEE Solid-State Circuits Society (2018-2019 term).

Also, he served as Associate Editor of IEEE Transactions on Circuits and Systems II (1997-1999), and of JSSC (2001-2006). He was in the Technical Program Committee of the Symposium on VLSI circuits (2009-2013) and is in the steering committee and programme committee of the European Solid State Circuit Conference (ESSCIRC). He served as distinguished lecturer of the IEEE, is co-recipient of the ISSCC 2002 and 2009 "Van Vessem Outstanding Paper Award" and in 2014 he received the 'Simon Stevin Meester' award (500.000€), the largest Dutch national prize for achievements in technical sciences. He is fellow of the IEEE and member of the Royal Netherlands Academy of Arts and Sciences (KNAW)

Short movie (Youtube) : who is Bram Nauta

Short movie (Youtube / Tweakers.net) : The chip designs of Bram Nauta

Expertise

Networks (Circuits)
Electric Potential
Clocks
Bandwidth
Sampling
Electric Power Utilization
Radio Receivers

Transistors

Organizations

- Faculty of Electrical Engineering, Mathematics & Computer Science (EWI), Integrated Circuit Design (ICD)

Ancillary Activities

- Else Kooi Foundation (www.ekp.nl)
  Chair Else Kooi Foundation
- IEEE Solid-State Circuits Society
  President
- Chip Design Works, NL
  Adviseren
- Law Firms
  Expert Witness in Patent/IP Legislation
- Various research funding organisations / institutes
  Reviewer of Scientific programs / projects

Research

Analog and RF Frequency Integrated Ciruit design

N-path filters explained (Youtube movie)

Publications

Recent

Lien, Y-C., Klumperink, E. A. M., & Nauta, B. (2018). Wireless communication receiver. (Patent No. *US10033420 B2*).

Nauta, B., Kasri, R., Klumperink, E. A. M., Cathelin, P., & Tournier, E. (2018). System for parallel radio reception with digitally controlled analog mixer amplifiers. (Patent No. *WO2018083387 A1*).

Thijssen, B. J., Klumperink, E. A. M., Nauta, B., & Quinlan, P. (2018). Feedforward phase noise compensation. (Patent No. *WO2018160569 (A1)*).

Huiskamp, M., Annema, A. J., & Nauta, B. (2018). *A delay spread cancelling waveform characterizer for RF power amplifiers*. Paper presented at 2018 IEEE International Symposium on Circuits and Systems, ISCAS 2018, Florence, Italy.

Agarwal, V., Annema, A-J., Hueting, R. J. E., Dutta, S., Nanver, L. K., & Nauta, B. (2018). Data Transmission Capabilities of Silicon Avalanche Mode Light-Emitting Diodes. *IEEE transactions on electron devices*, *65*(11), 4883-4890. [8472882]. https://doi.org/10.1109/TED.2018.2868126

Ghahremani, A., Annema, A-J., & Nauta, B. (2018). Load-mismatch sensitivity of class-E power amplifiers. *IEEE transactions on microwave theory and techniques*, *67*(1), 216-230. https://doi.org/10.1109/TMTT.2018.2873702

Qiao, Z., Boom, B. A., Annema, A. J., Wiegerink, R. J., & Nauta, B. (2018). On frequency-based interface circuits for capacitive MEMS accelerometers. *Micromachines*, *9*(10), [488]. https://doi.org/10.3390/mi9100488

Agarwal, V., Dutta, S., Annema, A. J., Hueting, R. J. E., Schmitz, J., Lee, M-J., ... Nauta, B. (2018). Optocoupling in CMOS. In *2018 IEEE International Electron Devices Meeting (IEDM)* San Francisco, USA: IEEE. https://doi.org/10.1109/IEDM.2018.8614523

Abdulaziz, M., Klumperink, E. A. M., Nauta, B., & Sjöland, H. (2018). Improving receiver close-in blocker tolerance by base-band Gm-C Notch-filtering. *IEEE transactions on circuits and systems I: regular papers*. https://doi.org/10.1109/TCSI.2018.2872469

Ponte, J., Ghahremani, A., Huiskamp, M., Annema, A-J., & Nauta, B. (2018). Augmentation of Class-E PA reliability under load mismatch conditions. In *2018 25th IEEE International Conference on Electronics, Circuits and Systems (ICECS)* IEEE. https://doi.org/10.1109/ICECS.2018.8617864

Other Contributions

UT Research Information System

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Google Scholar Link

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Courses

- 191210850 - Advanced Analog IC Electronics
- 191210870 - Integr. Circuits & Systems Mixed Sign.
- 191211500 - Wireless Transceiver Electronics

- 201800408 - Electronic Systems Design

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Organizations

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