

Author details

[Back to results](#) | 1 of 1[Print](#) | [Email](#)

Ryu, Seungtak

Korea Advanced Institute of Science & Technology,
Yusong, South Korea

Author ID: 7402110742

[About Scopus Author Identifier](#) | [View potential author matches](#)Other name formats: Ryu, Seung Tak
Ryu, S. T.

Documents: 84

Citations: 719 total citations by 639 documents

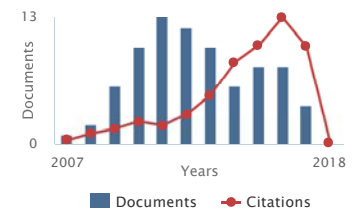
h-index: 15

Co-authors: 123

Subject area: Engineering , Computer Science [View More](#)[Analyze author output](#)[View citation overview](#)[View h-graph](#)

Follow this Author

Receive emails when this author publishes new articles

[Get citation alerts](#)[Add to ORCID](#)[Request author detail corrections](#)[84 Documents](#) | Cited by 639 documents | 123 co-authors84 documents [View in search results format](#)Sort on: [Date](#) [Cited by](#) [...](#)[Export all](#) | [Add all to list](#) | [Set document alert](#) | [Set document feed](#)

A Dual-Imaging Speed-Enhanced CMOS Image Sensor for Real-Time Edge Image Extraction	Kim, H.-J., Hwang, S.-I., Chung, J.-H., Park, J.-H., Ryu, S.-T.	2017	IEEE Journal of Solid-State Circuits	0
View at Publisher	Find it NTU			
Power-efficient flash ADC with complementary voltage-to-time converter	Oh, D.-R., Jo, D.-S., Moon, K.-J., Roh, Y.-J., Ryu, S.-T.	2017	Electronics Letters	0
View at Publisher	Find it NTU			
Normalized-full-scale-referencing digital-domain linearity calibration for SAR ADC	Chang, D.-J., Kim, W., Seo, M.-J., Hong, H.-K., Ryu, S.-T.	2017	IEEE Transactions on Circuits and Systems I: Regular Papers	2
View at Publisher	Find it NTU			
Fully flexible, lightweight, high performance all-solid-state supercapacitor based on 3-Dimensional-graphene/graphite-paper	Ramadoss, A., Yoon, K.-Y., Kwak, M.-J., (...), Ryu, S.-T., Jang, J.-H.	2017	Journal of Power Sources	15
View at Publisher	Find it NTU			
A Delta-Readout Scheme for Low-Power CMOS Image Sensors with Multi-Column-Parallel SAR ADCs	Kim, H.-J., Hwang, S.-I., Kwon, J.-W., (...), Shin, J.-K., Ryu, S.-T.	2016	IEEE Journal of Solid-State Circuits	2
View at Publisher	Find it NTU			
A 0.6 v 12 b 10 MS/s Low-Noise Asynchronous SAR-Assisted Time-Interleaved SAR (SATI-SAR) ADC	Kim, W., Hong, H.-K., Roh, Y.-J., (...), Seo, M.-J., Ryu, S.-T.	2016	IEEE Journal of Solid-State Circuits	2
View at Publisher	Find it NTU			
A 54-μW fast-settling arterial pulse wave sensor for wrist watch type system	Kim, K., Kim, M., Cho, H., (...), Ryu, S.-T., Yoo, H.-J.	2016	Proceedings - IEEE International Symposium on Circuits and Systems	0
View at Publisher	Find it NTU			
A low-power TDC-Configured logarithmic resistance sensor for MLC PCM Readout	Kwon, J.-W., Jin, D.-H., Kim, H.-J., (...), Cheon, J.-H., Ryu, S.-T.	2016	IEEE Sensors Journal	0
View at Publisher	Find it NTU			
A Sign-Equality-Based Background Timing-Mismatch Calibration Algorithm for Time-Interleaved ADCs	Kang, H.-W., Hong, H.-K., Park, S., (...), Ahn, K.-H., Ryu, S.-T.	2016	IEEE Transactions on Circuits and Systems II: Express Briefs	0
View at Publisher	Find it NTU			
A SUC-Based Full-Binary 6-bit 3.1-GS/s 17.7-mW Current-Steering DAC in 0.038 mm ²	Kim, S.-N., Kim, M.-R., Sung, B.-R.-S., (...), Cho, M.-H., Ryu, S.-T.	2016	IEEE Transactions on Very Large Scale Integration (VLSI) Systems	1











Author History

Publication range: 2003 - Present

References: [615](#)

Source history:

[IEEE Transactions on Circuits and Systems II: Express B](#)
[View docu](#)[2010 IEEE Asian Solid-State Circuits Conference, A-SSC 2010](#)
[View docu](#)[Journal of Power Sources](#)[View More](#)[View docu](#)[Show Related Affiliations](#)

View at Publisher Find it  NTU					
Delta readout scheme for image-dependent power savings in a CMOS image sensor with multi-column-parallel SAR ADCs	Kim, H.-J., Hwang, S.-I., Kwon, J.-W., (...), Shin, J.-K., Ryu, S.-T.	2016	2015 IEEE Asian Solid-State Circuits Conference, A-SSCC 2015 - Proceedings	3	
View at Publisher Find it  NTU					
Realization of high performance flexible wire supercapacitors based on 3-dimensional NiCo2O4/Ni fibers	Ramados, A., Kang, K.-N., Ahn, H.-J., (...), Ryu, S.-T., Jang, J.-H.	2016	Journal of Materials Chemistry A	11	
View at Publisher Find it  NTU					
A 6-bit 10-GS/s 63-mW 4x TI time-domain interpolating flash ADC in 65-nm CMOS	Oh, D.-R., Kim, J.-I., Seo, M.-J., Kim, J.-G., Ryu, S.-T.	2015	European Solid-State Circuits Conference	2	
View at Publisher Find it  NTU					
A 65 nm CMOS 7b 2 GS/s 20.7 mW Flash ADC with Cascaded Latch Interpolation	Kim, J.-I., Oh, D.-R., Jo, D.-S., Sung, B.-R.-S., Ryu, S.-T.	2015	IEEE Journal of Solid-State Circuits	5	
View at Publisher Find it  NTU					
A 15 μ m-Pitch, 8.7-ENOB, 13-mcells/sec logarithmic readout circuit for multi-level cell phase change memory	Jin, D.-H., Kwon, J.-W., Kim, H.-J., (...), Cheon, J., Ryu, S.-T.	2015	IEEE Journal of Solid-State Circuits	2	
View at Publisher Find it  NTU					
Ternary-level thermometer C-DAC switching scheme for flash-assisted SAR ADCs	Kang, H.-W., Hong, H.-K., Park, S., (...), Ahn, K.-H., Ryu, S.-T.	2015	IEICE Electronics Express	0	
View at Publisher Find it  NTU					
A decision-error-tolerant 45 nm CMOS 7b 1 GS/s nonbinary 2b/cycle SAR ADC	Hong, H.-K., Kim, W., Kang, H.-W., (...), Park, H.-J., Ryu, S.-T.	2015	IEEE Journal of Solid-State Circuits	14	
View at Publisher Find it  NTU					
Calibration-spur reduction in currentsteering DAC utilising digital-And-Analogue hybrid return-To-zero (HRZ) method	Kim, W.-C., Kim, J.-G., Ahn, K.-H., (...), Park, S.-H., Ryu, S.-T.	2015	Electronics Letters	0	
View at Publisher Find it  NTU					
A 21fJ/conv-step 9 ENOB 1.6GS/S 2 \times time-interleaved FATI SAR ADC with background offset and timing-skew calibration in 45nm CMOS	Sung, B.-R.-S., Jo, D.-S., Jang, I.-H., (...), Park, H.-J., Ryu, S.-T.	2015	Digest of Technical Papers - IEEE International Solid-State Circuits Conference	19	
View at Publisher Find it  NTU					
A 2.6b/cycle-architecture-based 10b 1 JGS/s 15.4mW 4 \times -time-interleaved SAR ADC with a multistep hardware-retirement technique	Hong, H.-K., Kang, H.-W., Jo, D.-S., (...), Park, H.-J., Ryu, S.-T.	2015	Digest of Technical Papers - IEEE International Solid-State Circuits Conference	17	
View at Publisher Find it  NTU					

Display: results per page

Page 1

[Back to results](#) | 1 of 1[Top of page](#)

The data displayed above is compiled exclusively from articles published in the Scopus database. To request corrections to any inaccuracies or provide any further feedback, please [contact us](#) (registration required). The data displayed above is subject to the privacy conditions contained in the [privacy policy](#).

About Scopus

[What is Scopus](#)
[Content coverage](#)
[Scopus blog](#)
[Scopus API](#)
[Privacy matters](#)

Language

[日本語に切り替える](#)
[切换到简体中文](#)
[切换到繁體中文](#)
[Русский язык](#)

Customer Service

[Help](#)
[Contact us](#)

ELSEVIER

[Terms and conditions](#)[Privacy policy](#)

Copyright © 2017 Elsevier B.V. All rights reserved. Scopus® is a registered trademark of Elsevier B.V.
Cookies are set by this site. To decline them or learn more, visit our [Cookies page](#).

