

Scopus

Author details

[About Scopus Author Id](#)[Print](#)

Alioto, Massimo

[Follow this Author](#) h -index: 26[View \$h\$ -](#)

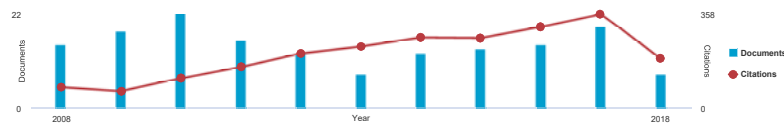
26

[View potential author matches](#)

National University of Singapore, Department of
Electrical & Computer Engineering, Singapore
City, Singapore
Author ID: 7004256085

Other name formats: [Massimo, Alioto](#) [Alioto, M.](#)

Subject area:

[Engineering](#) [Computer Science](#) [Materials Science](#) [Physics and Astronomy](#) [Mathematics](#)
[Energy](#) [Decision Sciences](#) [Biochemistry, Genetics and Molecular Biology](#)
Document and
citation trends:

Documents by author

240

[Analyze author c](#)






Total citations

2496 by 1700 documents

[View citation ov](#)
[Get citation alerts](#) [+ Add to ORCID](#) [Request author detail corrections](#) [Export profile to SciVal](#)
[240 Documents](#)[Cited by 1700 documents](#)[150 co-authors](#)[Author history](#)[View all in search results format >](#)Sort on: [Date \(newest\)](#)
[Export all](#) [Add all to list](#) [Set document alert](#) [Set document feed](#)

Document title	Authors	Year	Source	Ci
Time-Based Sensing for Reference-Less and Robust Read in STT-MRAM Memories	Trinh, Q., Ruocco, S., Alioto, M.	2018	IEEE Transactions on Circuits and Systems I: Regular Papers	
			Article in Press	
View abstract Full Text Finder View at Publisher				
Energy-performance design exploration of a low-power microprogrammed deep-learning accelerator	Santoro, G., Casu, M.R., Peluso, V., Calimera, A., Alioto, M.	2018	Proceedings of the 2018 Design, Automation and Test in Europe Conference and Exhibition, DATE 2018	
			2018-January, pp. 1151-1154	
View abstract Full Text Finder View at Publisher Related documents				
Dynamic Reference Voltage Sensing Scheme for Read Margin Improvement in STT-MRAMs	Trinh, Q.-K., Ruocco, S., Alioto, M.	2018	IEEE Transactions on Circuits and Systems I: Regular Papers	
			65(4), pp. 1269-1278	
View abstract Full Text Finder View at Publisher Related documents				
A 595pW 14pJ/Cycle microcontroller with dual-mode standard cells and self-startup for battery-indifferent distributed sensing	Lin, L., Jain, S., Alioto, M.	2018	Digest of Technical Papers - IEEE International Solid-State Circuits Conference	
			61, pp. 44-46	
View abstract Full Text Finder View at Publisher Related documents				

Document title	Authors	Year	Source	Ci
A Variation-Aware Timing Modeling Approach for Write Operation in Hybrid CMOS/STT-MTJ Circuits	De Rose, R., Lanuzza, M., Crupi, F., (...), Carpentieri, M., Alioto, M.	2018	IEEE Transactions on Circuits and Systems I: Regular Papers 65(3), pp. 1086-1095	
View abstract  Full Text Finder View at Publisher Related documents				
Dynamically Adaptable Pipeline for Energy-Efficient Microarchitectures Under Wide Voltage Scaling	Jain, S., Lin, L., Alioto, M.	2018	IEEE Journal of Solid-State Circuits 53(2),8115158, pp. 632-641	
View abstract  Full Text Finder View at Publisher Related documents				
IRazor: Current-Based Error Detection and Correction Scheme for PVT Variation in 40-nm ARM Cortex-R4 Processor	Zhang, Y., Khayatzaheh, M., Yang, K., (...), Blaauw, D., Sylvester, D.	2018	IEEE Journal of Solid-State Circuits 53(2),8060508, pp. 619-631	
View abstract  Full Text Finder View at Publisher Related documents				
The Internet of Things on Its Edge: Trends Toward Its Tipping Point	Alioto, M., Shahghasemi, M.	2018	IEEE Consumer Electronics Magazine 7(1),8197432, pp. 77-87	
Full Text Finder View at Publisher Related documents				
A fully-synthesizable C-element based PUF featuring temperature variation compensation with native 2.8% BER, 1.02fJ/b at 0.8-1.0V in 40nm	Taneja, S., Alvarez, A., Sadagopan, G., Alioto, M.	2017	2017 IEEE Asian Solid-State Circuits Conference, A-SSCC 2017 - Proceedings 2017-January, pp. 301-304	
View abstract  Full Text Finder View at Publisher Related documents				
EQSCALE: Energy-quality scalable feature extraction engine for Sub-mW real-time video processing with 0.55 mm ² area in 40nm CMOS	Alvarez, A.B., Ponnusamy, G., Alioto, M.	2017	2017 IEEE Asian Solid-State Circuits Conference, A-SSCC 2017 - Proceedings 2017-January, pp. 241-244	
View abstract  Full Text Finder View at Publisher Related documents				
Welcome message from the technical program chairs	Alioto, M., Li, H.	2017	International System on Chip Conference 2017-September	
Full Text Finder View at Publisher				
Design-Oriented Energy Models for Wide Voltage Scaling Down to the Minimum Energy Point	Jain, S., Lin, L., Alioto, M.	2017	IEEE Transactions on Circuits and Systems I: Regular Papers 64(12),8013083, pp. 3115-3125	
View abstract  Full Text Finder View at Publisher Related documents				
Polarization-dependent cut wire in mid-infrared metamaterial absorber	Chen, N., Hasan, D., Pitchappa, P., (...), Lo, G.-Q., Lee, C.	2017	2017 Conference on Lasers and Electro-Optics Pacific Rim, CLEO-PR 2017 2017-January, pp. 1-3	
View abstract  Full Text Finder View at Publisher Related documents				
Power-precision scalable latch memories	Esposito, D., Strollo, A.G.M., Alioto, M.	2017	Proceedings - IEEE International Symposium on Circuits and Systems 8050995	
View abstract  Full Text Finder View at Publisher Related documents				
Transistor sizing strategy for simultaneous energy-delay optimization in CMOS buffers	Lin, L., Quang, K.T., Alioto, M.	2017	Proceedings - IEEE International Symposium on Circuits and Systems 8050997	

Document title	Authors	Year	Source	Ci
View abstract  Full Text Finder View at Publisher Related documents				
Design-oriented models for quick estimation of path delay variability via the fan-out-of-4 metric	Alioto, M., Scotti, G., Trifiletti, A.	2017	Proceedings - IEEE International Symposium on Circuits and Systems 8050910	
View abstract  Full Text Finder View at Publisher Related documents				
A variation-aware simulation framework for hybrid CMOS/spintronic circuits	De Rose, R., Lanuzza, M., Crupi, F., (...), Carpentieri, M., Alioto, M.	2017	Proceedings - IEEE International Symposium on Circuits and Systems 8050920	
View abstract  Full Text Finder View at Publisher Related documents				
Editorial	Chakrabarty, K., Alioto, M., Joshi, R.	2017	IEEE Transactions on Very Large Scale Integration (VLSI) Systems 25(9),8015278, pp. 2393	
Full Text Finder View at Publisher				
Guest Editorial Special Issue on Circuits and Systems for the Internet of Things - From Sensing to Sensemaking	Alioto, M., Sánchez-Sinencio, E., Sangiovanni-Vincentelli, A.	2017	IEEE Transactions on Circuits and Systems I: Regular Papers 64(9),8017696, pp. 2221-2225	
View abstract  Full Text Finder View at Publisher Related documents				
A Novel Framework to Estimate the Path Delay Variability On the Back of an Envelope via the Fan-Out-of-4 Metric	Alioto, M., Scotti, G., Trifiletti, A.	2017	IEEE Transactions on Circuits and Systems I: Regular Papers 64(8),7894172, pp. 2073-2085	
View abstract  Full Text Finder View at Publisher Related documents				

Display:  20 results per page[1](#) [2](#) [3](#) [4](#) [5](#) ... [12](#) [>](#) [>>](#)[^](#) Top of page

The data displayed above is compiled exclusively from documents indexed in the Scopus database. To request corrections to any inaccuracies or provide any further feedback, please use the [Author Feedback Wizard](#).

About Scopus

[What is Scopus](#)
[Content coverage](#)
[Scopus blog](#)
[Scopus API](#)
[Privacy matters](#)

Language

[日本語に切り替える](#)
[切换到简体中文](#)
[切换到繁體中文](#)
[Русский язык](#)

Customer Service

[Help](#)
[Contact us](#)

ELSEVIER[Terms and conditions](#) [Privacy policy](#)

Copyright © 2018 Elsevier B.V. All rights reserved. Scopus® is a registered trademark of Elsevier B.V.

Cookies are set by this site. To decline them or learn more, visit our [Cookies page](#).

