

Scopus

## Author details

About Scopus Author Id

[Return to search results](#) 1 of 2 [Next](#) >[Print](#) [i](#)

Zwoliński, Mark

[Follow this Author](#)*h*-index: ⓘ

14

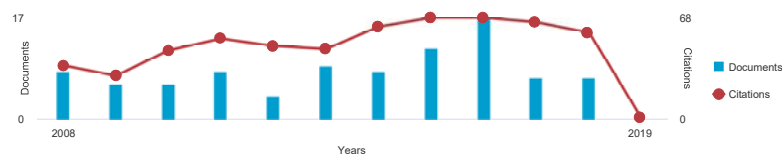
[View \*h\*-](#)University of Southampton, Electronics and  
Computer Science, Southampton, United  
Kingdom

Author ID: 7005900310

Other name formats:

[Zwoliński, Mark](#) [Zwolinski, M.](#) [Zwoliński, Mark](#) [Zwolinski, Mark](#) [Zwoliński, M.](#)[View potential author matches](#)

Subject area:

[Engineering](#) [Computer Science](#) [Mathematics](#) [Materials Science](#) [Physics and Astronomy](#)  
[Decision Sciences](#) [Social Sciences](#) [Energy](#) [Earth and Planetary Sciences](#)  
[Biochemistry, Genetics and Molecular Biology](#)Document and  
citation trends:

Documents by author

166


[Analyze author c](#)

Total citations

767 by 668 documents

[View citation ovr](#)[Get citation alerts](#) [Add to ORCID ⓘ](#) [Request author detail corrections](#) [Export profile to SciVal](#)[166 Documents](#)[Cited by 668 documents](#)[144 co-authors](#)[Author history](#)[View all in search results format](#) >Sort on: [Date \(newest\)](#)[Export all](#) [Add all to list](#) [Set document alert](#) [Set document feed](#)

Document title	Authors	Year	Source	Ci
A Reliable PUF in a Dual Function SRAM	Mispan, M.S., Duan, S., Halak, B., Zwolinski, M.	2018	2018 IEEE 28th International Symposium on Power and Timing Modeling, Optimization and Simulation, PATMOS 2018 8464143, pp. 76-81	
View abstract <a href="#">Full Text Finder</a> <a href="#">View at Publisher</a> <a href="#">Related documents</a>				
Hardware Implementation of a Low-Power K-Best MIMO Detector Based on a Hybrid Merge Network	Bello, I.A., Halak, B., El-Hajjar, M., Zwolinski, M.	2018	2018 IEEE 28th International Symposium on Power and Timing Modeling, Optimization and Simulation, PATMOS 2018 8464169, pp. 191-197	
View abstract <a href="#">Full Text Finder</a> <a href="#">View at Publisher</a> <a href="#">Related documents</a>				
Lifetime Reliability-Aware Digital Synthesis	Duan, S., Zwolinski, M., Halak, B.	2018	IEEE Transactions on Very Large Scale Integration (VLSI) Systems  Article in Press	
View abstract <a href="#">Full Text Finder</a> <a href="#">View at Publisher</a>				
Early detection of system-level anomalous behaviour using hardware performance counters	Woo, L.L., Zwolinski, M., Halak, B.	2018	Proceedings of the 2018 Design, Automation and Test in Europe Conference and Exhibition, DATE 2018 2018-January, pp. 485-490	

Document title	Authors	Year	Source	Ci
View abstract  Full Text Finder View at Publisher Related documents				
Cost-efficient design for modeling attacks resistant PUFs	Mispan, M.S., Su, H., Zwolinski, M., Halak, B.	2018	Proceedings of the 2018 Design, Automation and Test in Europe Conference and Exhibition, DATE 2018 2018-January, pp. 467-472	
View abstract  Full Text Finder View at Publisher Related documents				
Multi-Path Aging Sensor for Cost-Efficient Delay Fault Prediction	Sai, G., Halak, B., Zwolinski, M.	2018	IEEE Transactions on Circuits and Systems II: Express Briefs 65(4), pp. 491-495	
View abstract  Full Text Finder View at Publisher Related documents				
Fault analysis in analog circuits through language manipulation and abstraction	Fraccaroli, E., Stefanni, F., Fummi, F., Zwolinski, M.	2018	Forum on Specification and Design Languages 2017-September, 8303890, pp. 1-7	
View abstract  Full Text Finder View at Publisher Related documents				
BTI mitigation by anti-ageing software patterns	Abbas, H.M., Halak, B., Zwolinski, M.	2017	Microelectronics Reliability 79, pp. 79-90	
View abstract  Full Text Finder View at Publisher Related documents				
A cost-efficient delay-fault monitor	Sai, G., Halak, B., Zwolinski, M.	2017	Proceedings - IEEE International Symposium on Circuits and Systems 8050830	
View abstract  Full Text Finder View at Publisher Related documents				
Hardware performance counters for system reliability monitoring	Leng, E.W.L., Zwolinski, M., Halak, B.	2017	2017 2nd International Verification and Security Workshop, IVSW 2017 8031548, pp. 76-81	
View abstract  Full Text Finder View at Publisher Related documents				
Lightweight obfuscation techniques for modeling attacks resistant PUFs	Mispan, M.S., Halak, B., Zwolinski, M.	2017	2017 2nd International Verification and Security Workshop, IVSW 2017 8031539, pp. 19-24	
View abstract  Full Text Finder View at Publisher Related documents				
An ageing-aware digital synthesis approach	Duan, S., Halak, B., Zwolinski, M.	2017	SMACD 2017 - 14th International Conference on Synthesis, Modeling, Analysis and Simulation Methods and Applications to Circuit Design 7981556	
View abstract  Full Text Finder View at Publisher Related documents				
Overview of PUF-Based hardware security solutions for the internet of things	Halak, B., Zwolinski, M., Mispan, M.S.	2017	Midwest Symposium on Circuits and Systems 7870046	
View abstract  Full Text Finder View at Publisher Related documents				
Using $I_{ddt}$ current degradation to monitor ageing in CMOS circuits	Ramlee, R.H., Zwolinski, M.	2017	Proceedings - 2016 26th International Workshop on Power and Timing Modeling, Optimization and Simulation, PATMOS 2016 7833688, pp. 200-204	
View abstract  Full Text Finder View at Publisher Related documents				

Document title	Authors	Year	Source	Ci
High accuracy implementation of Adaptive Exponential integrated and fire neuron model	Makhlooghpour, A., Soleimani, H., Ahmadi, A., Zwolinski, M., Saif, M.	2016	Proceedings of the International Joint Conference on Neural Networks 2016-October, 7727198, pp. 192-197	
View abstract ▾ Full Text Finder View at Publisher Related documents				
NBTI aging evaluation of PUF-based differential architectures	Mispan, M.S., Halak, B., Zwolinski, M.	2016	2016 IEEE 22nd International Symposium on On-Line Testing and Robust System Design, IOLTS 2016 7604680, pp. 103-108	
View abstract ▾ Full Text Finder View at Publisher Related documents				
A Survey of VLSI Implementations of Tree Search Algorithms for MIMO Detection Open Access	Bello, I.A., Halak, B., El-Hajjar, M., Zwolinski, M.	2016	Circuits, Systems, and Signal Processing 35(10), pp. 3644-3674	
View abstract ▾ Full Text Finder View at Publisher Related documents				
The influence of hysteresis voltage on single event transients in a 65nm CMOS high speed comparator	Nawi, I.M., Halak, B., Zwolinski, M.	2016	Proceedings of the European Test Workshop 2016-July, 7519300	
View abstract ▾ Full Text Finder View at Publisher Related documents				
The European Masters in Embedded Computing Systems (EMECs)	Zwolinski, M., Kunz, W., Svarstad, K., Brown, A.	2016	2016 11th European Workshop on Microelectronics Education, EWME 2016 7496462	
View abstract ▾ Full Text Finder View at Publisher Related documents				
A Low-Cost, Radiation-Hardened Method for Pipeline Protection in Microprocessors	Lin, Y., Zwolinski, M., Halak, B.	2016	IEEE Transactions on Very Large Scale Integration (VLSI) Systems 24(5), 7274360, pp. 1688-1701	
View abstract ▾ Full Text Finder View at Publisher Related documents				

Display: 20 ▾ results per page

1 2 3 4 5 ... 9 > >>

^ Top of page

The data displayed above is compiled exclusively from documents indexed in the Scopus database. To request corrections to any inaccuracies or provide any further feedback, please use the [Author Feedback Wizard](#).

## About Scopus

What is Scopus  
Content coverage  
Scopus blog  
Scopus API  
Privacy matters

## Language

日本語に切り替える  
切换到简体中文  
切换到繁體中文  
Русский язык

## Customer Service

Help  
Contact us

ELSEVIER

[Terms and conditions](#) [Privacy policy](#)

Copyright © 2018 Elsevier B.V. All rights reserved. Scopus® is a registered trademark of Elsevier B.V.  
We use cookies to help provide and enhance our service and tailor content. By continuing, you agree to the use of cookies.

REL