

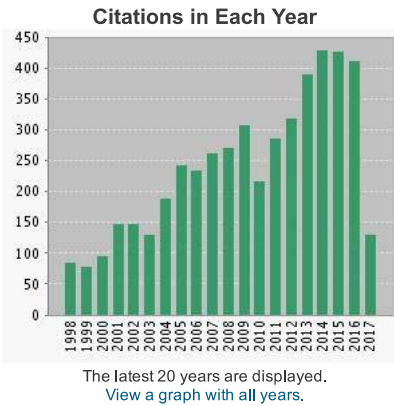
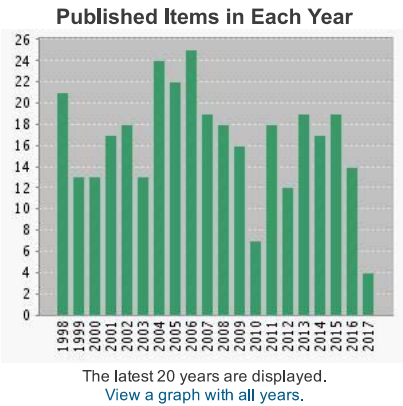


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<input type="checkbox"/>	6. <b>High frequency noise of MOSFETs - I - Modeling</b> By: Chen, CH; Deen, MJ <a href="#">SOLID-STATE ELECTRONICS</a> Volume: 42 Issue: 11 Pages: 2069-2081 Published: NOV 1998	4	1	4	5	1	83	4.15
<input type="checkbox"/>	7. <b>MOSFET Modeling for RF IC design</b> By: Cheng, YH; Deen, MJ; Chen, CH <a href="#">IEEE TRANSACTIONS ON ELECTRON DEVICES</a> Volume: 52 Issue: 7 Pages: 1286-1303 Published: JUL 2005	3	6	3	4	2	82	6.31
<input type="checkbox"/>	8. <b>A general noise and S-parameter deembedding procedure for on-wafer</b>	6	1	1	0	1	82	4.82

high-frequency noise measurements of MOSFETs

By: Chen, CH; Deen, MJ  
IEEE TRANSACTIONS ON MICROWAVE THEORY AND TECHNIQUES Volume: 49 Issue:  
5 Pages: 1004-1005 Published: MAY 2001

9. High-frequency noise of modern MOSFETs: Compact modeling and measurement issues

By: Deen, M. Jamal; Chen, Chih-Hung; Asgaran, Saman; et al.  
IEEE TRANSACTIONS ON ELECTRON DEVICES Volume: 53 Issue: 9 Pages: 2062-2081  
Published: SEP 2006

76541816.75
10. An effective gate resistance model for CMOS RF and noise modeling

By: Jin, XD; Ou, JJ; Chen, CH; et al.  
Book Group Author(s): IEEE; IEEE  
Conference: International Electron Devices Meeting (IEDM) Location: SAN FRANCISCO, CA  
Date: DEC 06-09, 1998  
Sponsor(s): Electron Devices Soc IEEE  
INTERNATIONAL ELECTRON DEVICES MEETING 1998 - TECHNICAL DIGEST Pages:  
961-964 Published: 1998

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