Scopus

Author details

About Scopus Author Id

> ¡   The Scopus Author Identifier assigns a unique number to groups of documents written by the same author via an algorithm that matches authorship based on a certain criteria. If a document cannot be confidently matched with an author identifier, it is grouped separately. In this case, you may see more than one entry for the same author.
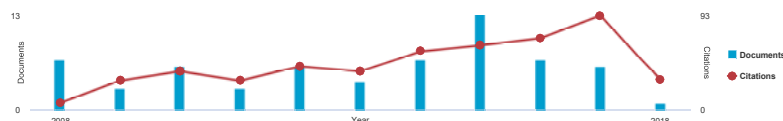
🖶 Print

Di, Jia

University of Arkansas - Fayetteville, Fayetteville, United States
Author ID: 12645196400
Other name formats: (Di, J.)

Follow this Author

View potential author matches

h-index: ⦿

11

View *h*-

Documents by author

84   Analyze author c

Total citations

522 by **436** documents

View citation ov

Subject area: (Engineering) (Computer Science) (Materials Science) (Social Sciences) (Energy) (Earth and Planetary Sciences) (Physics and Astronomy) (Mathematics) (Agricultural and Biological Sciences)

Document and citation trends:

🔔 Get citation alerts   ＋ Add to ORCID ⦿   👥 Request author detail corrections   ⤓ Export profile to SciVal

---

84 Documents   Cited by 436 documents   113 co-authors   Author history

View in search results format ›   Sort on: Date (newest)

Export all   Add all to list   Set document alert   Set document feed

Document title	Authors	Year	Source	Ci
Clockless spintronic logic: A robust and ultra-low power computing paradigm	Bai, Y., DeMara, R.F., Di, J., Lin, M.	2018	IEEE Transactions on Computers 67(5), pp. 631-645	

View abstract ⌄   Full Text Finder   View at Publisher   Related documents

Student Cross-Training Opportunities for Combining Food, Transportation, and Critical Infrastructure Cybersecurity Into an Academic Food Systems Education Program (Book Chapter)	Thompson, D.R., Rainwater, C.E., Di, J., Ricke, S.C.	2017	*Food and Feed Safety Systems and Analysis* pp. 375-391	

View abstract ⌄   Full Text Finder   View at Publisher   Related documents

A SiC CMOS Digitally Controlled PWM Generator for High-Temperature Applications	Roy, S., Murphree, R.C., Abbasi, A., (...), Mantooth, H.A., Di, J.	2017	IEEE Transactions on Industrial Electronics 64(10),7900404, pp. 8364-8372	

View abstract ⌄   Full Text Finder   View at Publisher   Related documents

Golden reference matching for gate-level netlist functionality identification	Le, T., Di, J.	2017	Midwest Symposium on Circuits and Systems 2017-August,8052986, pp. 567-570	

Document title	Authors	Year	Source	Ci

View abstract ∨   Full Text Finder   View at Publisher   Related documents

Recent advances in low power asynchronous circuit design	Di, J., Bell, B.A., Bouillon, W., (...), Sabado, F., Suchanek, A.	2017	Journal of Low Power Electronics 13(3), pp. 280-297	

View abstract ∨   Full Text Finder   View at Publisher   Related documents

Obfuscation-based protection framework against printed circuit boards unauthorized operation and reverse engineering	Guo, Z., Di, J., Tehranipoor, M.M., Forte, D.	2017	ACM Transactions on Design Automation of Electronic Systems 22(3),54	

View abstract ∨   Full Text Finder   View at Publisher   Related documents

High Temperature Data Converters in Silicon Carbide CMOS	Rahman, A., Caley, L., Roy, S., (...), Francis, A.M., Holmes, J.	2017	IEEE Transactions on Electron Devices 64(4),7859462, pp. 1426-1432	

View abstract ∨   Full Text Finder   View at Publisher   Related documents

Implementation of a digitally controlled SiC CMOS PWM generator using a tunable current-starved delay generator for high-temperature switched-mode regulators	Roy, S., Murphee, R.C., Abbasi, A., (...), Francis, A.M., Holmes, J.	2016	WiPDA 2016 - 4th IEEE Workshop on Wide Bandgap Power Devices and Applications 7799939, pp. 210-214	

View abstract ∨   Full Text Finder   View at Publisher   Related documents

High-Temperature SiC CMOS Comparator and op-amp for Protection Circuits in Voltage Regulators and Switch-Mode Converters	Rahman, A., Roy, S., Murphree, R., (...), Holmes, J., Di, J.	2016	IEEE Journal of Emerging and Selected Topics in Power Electronics 4(3),7498635, pp. 935-945	

View abstract ∨   Full Text Finder   View at Publisher   Related documents

Multi-threshold dual-spacer dual-rail delay-insensitive logic: An improved IC design methodology for side channel attack mitigation	Habimana, J.P.T., Sabado, F., Di, J.	2016	Proceedings - IEEE International Symposium on Circuits and Systems 2016-July,7527349, pp. 750-753	

View abstract ∨   Full Text Finder   View at Publisher   Related documents

Golden Reference Library Matching of Structural Checking for securing soft IPs	Weaver, L., Le, T., Di, J.	2016	Conference Proceedings - IEEE SOUTHEASTCON 2016-July,7506737	

View abstract ∨   Full Text Finder   View at Publisher   Related documents

Complex High-Temperature CMOS Silicon Carbide Digital Circuit Designs	Kuhns, N., Caley, L., Rahman, A., (...), Francis, A.M., Holmes, J.	2016	IEEE Transactions on Device and Materials Reliability 16(2),7407369, pp. 105-111	

View abstract ∨   Full Text Finder   View at Publisher   Related documents

Tracking data flow at gate-level through structural checking	Le, T., Di, J., Tehranipoor, M., Forte, D., Wang, L.	2016	Proceedings of the ACM Great Lakes Symposium on VLSI, GLSVLSI 18-20-May-2016, pp. 185-189	

View abstract ∨   Full Text Finder   View at Publisher   Related documents

A novel PUF based on cell error rate distribution of STT-RAM	Zhang, X., Guangyu, S., Zhang, Y., (...), Wen, W., Di, J.	2016	Proceedings of the Asia and South Pacific Design Automation Conference, ASP-DAC 25-28-January-2016,7428035, pp. 342-347	

View abstract ∨   Full Text Finder   View at Publisher   Related documents

Document title	Authors	Year	Source	Ci
Evaluating the capability and performance of access control policy verification tools	Li, A., Li, Q., Hu, V.C., Di, J.	2015	Proceedings - IEEE Military Communications Conference MILCOM 2015-December,7357470, pp. 366-371	

View abstract ⌄   Full Text Finder   View at Publisher   Related documents

Fork path: Improving efficiency of ORAM by removing redundant memory accesses	Zhang, X., Sun, G., Zhang, C., (...), Chen, Y., Di, J.	2015	Proceedings of the Annual International Symposium on Microarchitecture, MICRO 05-09-December-2015, pp. 102-114	

View abstract ⌄   Full Text Finder   View at Publisher   Related documents

Asynchronous and synchronous designs for low-power FDSOI CMOS process optimized for subthreshold operation at 0.3V VDD	Lo, C.-W., Men, L., Brady, J., Di, J.	2015	2015 IEEE SOI-3D-Subthreshold Microelectronics Technology Unified Conference, S3S 2015 7333511	

View abstract ⌄   Full Text Finder   View at Publisher   Related documents

Chip-level anti-reverse engineering using transformable interconnects	Chen, S., Chen, J., Forte, D., (...), Tehranipoor, M., Wang, L.	2015	Proceedings of the 2015 IEEE International Symposium on Defect and Fault Tolerance in VLSI and Nanotechnology Systems, DFTS 2015 7315145, pp. 109-114	

View abstract ⌄   Full Text Finder   View at Publisher   Related documents

Radiation hardened NULL convention logic asynchronous circuit design	Zhou, L., Smith, S.C., Di, J.	2015	Journal of Low Power Electronics and Applications 5(4), pp. 216-233	

View abstract ⌄   Full Text Finder   View at Publisher   Related documents

Delay insensitive ternary CMOS logic for secure hardware	Nair, R.S.P., Smith, S.C., Di, J.	2015	Journal of Low Power Electronics and Applications 5(3), pp. 183-215	

View abstract ⌄   Full Text Finder   View at Publisher   Related documents

Display: [20 ⌄] results per page   **1** 2 3 4 5   ⌃ Top o

The data displayed above is compiled exclusively from documents indexed in the Scopus database. To request corrections to any inaccuracies or provide any further feedback, please use the Author Feedback Wizard .

About Scopus

What is Scopus

Content coverage

Scopus blog

Scopus API

Privacy matters

Language

日本語に切り替える

切换到简体中文

切換到繁體中文

Русский язык

Customer Service

Help

Contact us

ELSEVIER

Terms and conditions   Privacy policy

REL