

Search

Alerts

My list

My Scopus



E-mail

Matsuzawa, AkiraTokyo Institute of Technology, Department
of Physical Electronics, Tokyo, Japan

Author ID: 7102714965

[About Scopus Author Identifier](#) | [View potential author matches](#)

Other name formats: Matsuzawa

Documents: 233

Citations: 1193 total citations by 948 documents

h-index: 16

Co-authors: 150 (maximum 150 co-authors can be displayed)

Subject area: Engineering , Computer Science [View More](#)

Analyze author output

[View citation overview](#)[View h-graph](#)

233 Documents | Cited by 948 documents | 150 co-authors

233 documents [View all in search results format](#)Sort on: **Date** [Cited by](#)[Export all](#) | [Add all to my list](#) | [Set document alert](#) | [Set document feed](#)A fully synthesizable all-digital pll with
interpolative phase coupled oscillator, current-
output DAC, and fine-resolution digital varactor
using gated edge injection techniqueDeng, W., Yang,
D., Ueno, T., (...),
Okada, K.,
Matsuzawa, A.2015 IEEE Journal of
Solid-State Circuits

0

View at Publisher [Find it @ NTU](#)Characterization of crossing transmission line
using two-port measurements for millimeter-
wave CMOS circuit designTokgoz, K.K., Lim,
K., Kawai, S., (...),
Okada, K.,
Matsuzawa, A.2015 IEICE Transactions
on Electronics

0

View at Publisher [Find it @ NTU](#)Ultralow-Voltage High-Speed Flash ADC
Design Strategy Based on FoM-Delay ProductLin, J., Mano, I.,
Miyahara, M.,
Matsuzawa, A.2014 IEEE Transactions
on Very Large Scale
Integration (VLSI)
Systems

0

View at Publisher [Find it @ NTU](#) Article in PressExtension method of tolerance to voltage and
temperature variations in an injection-locked
PLLHan, J., Miyahara,
M., Matsuzawa, A.2014 IEICE Electronics
Express

0

View at Publisher [Find it @ NTU](#)15.1 A 0.0066mm² 780μW fully synthesizable
PLL with a current-output DAC and an
interpolative phase-coupled oscillator using
edge-injection techniqueDeng, W., Yang,
D., Ueno, T., (...),
Okada, K.,
Matsuzawa, A.2014 Digest of Technical
Papers - IEEE
International Solid-
State Circuits
Conference

2

View at Publisher [Find it @ NTU](#)20.3 A 64-QAM 60GHz CMOS transceiver with
4-channel bondingOkada, K., Minami,
R., Tsukui, Y., (...),
Miyahara, M.,
Matsuzawa, A.2014 Digest of Technical
Papers - IEEE
International Solid-
State Circuits
Conference

1

View at Publisher [Find it @ NTU](#)












Follow this Author




Receive emails when this author
publishes new articles[Get citation alerts](#)[Add to ORCID](#)[Request author detail corrections](#)**Author History**

Publication range: 1982 - Present

References: 1568

Source history:[Midwest Symposium on Circuits and Systems](#) [View documents](#)[IEEE Transactions on Microwave Theory and Techniques](#)[View documents](#)[Proceedings - IEEE International Symposium on Circuits and Systems](#)[View documents](#)[View More](#)[Show Related Affiliations](#)

22.6 A 2.2GS/s 7b 27.4mW time-based folding-flash ADC with resistively averaged voltage-to-time amplifiers	Miyahara, M., Mano, I., Nakayama, M., Okada, K., Matsuzawa, A.	2014	Digest of Technical Papers - IEEE International Solid-State Circuits Conference	0
View at Publisher Find it  NTU				
A swing-enhanced current-reuse class-C VCO with dynamic bias control circuits	Siriburanon, T., Deng, W., Okada, K., Matsuzawa, A.	2014	Proceedings of the Asia and South Pacific Design Automation Conference, ASP-DAC	0
View at Publisher Find it  NTU				
A dual-loop injection-locked PLL with all-digital background calibration system for on-chip clock generation	Deng, W., Musa, A., Siriburanon, T., (...), Okada, K., Matsuzawa, A.	2014	Proceedings of the Asia and South Pacific Design Automation Conference, ASP-DAC	0
View at Publisher Find it  NTU				
A variable-supply-voltage 60-GHz PA with consideration of HCI issues for TDD operation	Wu, R., Tsukui, Y., Minami, R., Okada, K., Matsuzawa, A.	2014	IEICE Transactions on Electronics	0
View at Publisher Find it  NTU				
A 7-bit 1-GS/s flash ADC with background calibration	Tsukamoto, S., Miyahara, M., Matsuzawa, A.	2014	IEICE Transactions on Electronics	0
View at Publisher Find it  NTU				
High-Q inductors on locally semi-insulated Si substrate by helium-3 bombardment for RF CMOS integrated circuits	Li, N., Okada, K., Inoue, T., (...), Sakane, H., Matsuzawa, A.	2014	Digest of Technical Papers - Symposium on VLSI Technology	0
View at Publisher Find it  NTU				
A compact, low-power and low-jitter dual-loop injection locked pll using all-digital pvt calibration	Musa, A., Deng, W., Siriburanon, T., (...), Okada, K., Matsuzawa, A.	2014	IEEE Journal of Solid-State Circuits	3
View at Publisher Find it  NTU				
A pulse-driven LC-VCO with a figure-of-merit of -192dBc/Hz	Narayanan, A.T., Kimura, K., Deng, W., Okada, K., Matsuzawa, A.	2014	European Solid-State Circuits Conference	0
View at Publisher Find it  NTU				
Picosecond resolution time-to-digital converter using Gm-C integrator and SAR-ADC	Xu, Z., Miyahara, M., Matsuzawa, A.	2014	IEEE Transactions on Nuclear Science	0
View at Publisher Find it  NTU				
Injection locked charge-pump PLL with a replica of the ring oscillator	Han, J., Miyahara, M., Matsuzawa, A.	2014	IEICE Transactions on Electronics	1
View at Publisher Find it  NTU				
A 60-GHz sub-sampling frequency synthesizer using sub-harmonic injection-locked quadrature oscillators	Siriburanon, T., Ueno, T., Kimura, K., (...), Okada, K., Matsuzawa, A.	2014	Digest of Papers - IEEE Radio Frequency Integrated Circuits Symposium	0
View at Publisher Find it  NTU				

A compact and low-power fractionally injection-locked quadrature frequency synthesizer using a self-synchronized gating injection technique for software-defined radios	Deng, W., Hara, S., Musa, A., Okada, K., Matsuzawa, A.	2014	IEEE Journal of Solid-State Circuits	0
View at Publisher Find it  NTU				
A 17-mW 5-Gb/s 60-GHz CMOS transmitter with efficiency-enhanced on-chip antenna	Wu, R., Deng, W., Sato, S., (...), Okada, K., Matsuzawa, A.	2014	Digest of Papers - IEEE Radio Frequency Integrated Circuits Symposium	0
View at Publisher Find it  NTU				
A 13.2% locking-range divide-by-6, 3.1mW, ILFD using even-harmonic-enhanced direct injection technique for millimeter-wave PLLs	Siriburanon, T., Deng, W., Musa, A., Okada, K., Matsuzawa, A.	2013	European Solid- State Circuits Conference	1
View at Publisher Find it  NTU				
Display <input type="text" value="20"/> results per page Page1				

[Top of page](#)

The data displayed above is compiled exclusively from articles published in the Scopus database. To request corrections to any inaccuracies or provide any further feedback, please [contact us](#) (registration required).
The data displayed above is subject to the privacy conditions contained in the [privacy policy](#) .

About Scopus

[What is Scopus](#)

[Content coverage](#)

Language

日本語に切り替える

切换到简体中文

切换到繁體中文

Customer Service


[Help and Contact](#)

[Live Chat](#)

About Elsevier

[Terms and Conditions](#)

[Privacy Policy](#)



ELSEVIER

Copyright © 2015 Elsevier B.V. All rights reserved.Scopus® is a registered trademark of Elsevier B.V.
Cookies are set by this site. To decline them or learn more, visit our [Cookies](#) page.

▲