Dr. Sheldon Tan

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Introduction

I received my B.S. and M.S. from Fudan University, Shanghai, P. R. China, in 1992 and 1995 respectively and the Ph.D. from University of Iowa, Iowa City, in 1999. Currently I am Professor at Department of Electrical and Computer Engineering. Here are my latest curriculum vitae and short biography and my publication list.

I am the director of VLSI System and Computation Lab (VSC Lab) at UC Riverside. I am also the associate director for the Computer Engineering Program at Bourns College of Engineering. I am also a cooperating faculty member (co-faculty) of the Computer Science and Engineering department at Bourns College of Engineering.

In addition, I am the Editor in Chief (EIC) of Integration, The VLSI Journal, which is one of the primary journals in the VLSI design and CAD/design automation areas.

My previous website can be found here

VSCLAB Recent Research Highlights

- The new physics-based EM model for more accurate EM assessment and EM signoff
- GLU (with sources codes and examples) - *GPU-enabled parallel LU factorization solver package*
  - More discussion on GLU solver can be found at GLU project.

Recent VSCLAB News

- 2017DAC WIP
- SRC Mahboob Khan Outstanding Industry Liaison/Associate Awards
- Ph.D. student Taeyoung Kim passed his Ph.D. defenses today

- One PhD students at VSCLab, which are directed by Prof. Sheldon Tan, has received the Richard Newton Young Student Fellow Award from 54rd Design Automation Conference (DAC)
- Dr. David Pan from UT Austin visited VSCLAB

Job Openings in VSCLAB

- VSCLAB lab has **one Post Doctoral Fellow positions starting January 2017**.
- Also we have **a few new Ph.D. student positions available for Fall 2017**.
- Please see details for the openings at VSCLAB at Research Job Openings

Current Research

- VLSI long-term reliability, resilient systems, fault tolerant computing, reliability-aware design and management at circuit and system levels
- Parallel and intelligent computing (deep learning) and analysis on heterogeneous and accelerator-rich (GPUs) platforms
- Hardware security and trusted computing
- Smart devices and embedded and cyber-physical systems
- Thermal modeling, optimization and dynamic thermal management at circuit, chip and board levels
- Statistical modeling and optimization for VLSI systems

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