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## Jae-Joon Kim

Associate Professor

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### Research Interests

Neuromorphic Circuit and System, Low Power VLSI design,  
Flexible Device/Circuit Design, Smart Memory

### Work Experiences

Associate Professor, POSTECH, 2013.2 - Present

Research Staff Member, IBM T. J. Watson Research Center,  
2004. 5 - 2013.1

**Education**

Ph.D. Electrical and Computer Eng., Purdue University, 2004.

M.S. School of Electrical Eng., Seoul National University, 1998.

B.S. Dept. of Electronics Eng., Seoul National University, 1994.

**Activities**

Associate Editor, ACM Transactions on Design Automation of Electronic Systems (TODAES), 2014 - Present

Vice Technical Program Chair, VLSI-SOC 2015

Finance Chair, International Conference on Computer Design (ICCD) 2014

Program Committee Member:

DAC(2015-2016), DATE(2016-2017), ASP-DAC(2014-15,17), ISLPED(2010-2012,2014-2016), ISQED(2007 - 2010)

Judge Committee Member for DAC/ISSCC Student Design Contest, 2008 - 2011

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