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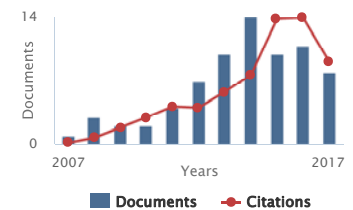
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A Scaling Compatible, Synthesis Friendly VCO-based Delta-sigma ADC Design and Synthesis Methodology	Xu, B., Li, S., Sun, N., Pan, D.Z.	2017	Proceedings - Design Automation Conference	0
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A 0.7-V 0.6- $\mu$ w 100-kS/s Low-Power SAR ADC with Statistical Estimation-Based Noise Reduction	Chen, L., Tang, X., Sanyal, A., (...), Cong, J., Sun, N.	2017	IEEE Journal of Solid-State Circuits	0
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A 0.95-mW 6-b 700-MS/s Single-Channel Loop-Unrolled SAR ADC in 40-nm CMOS	Chen, L., Ragab, K., Tang, X., (...), Sanyal, A., Sun, N.	2017	IEEE Transactions on Circuits and Systems II: Express Briefs	0
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A 10-b 800MS/s time-interleaved SAR ADC with fast timing-skew calibration	Song, J., Ragab, K., Tang, X., Sun, N.	2017	2016 IEEE Asian Solid-State Circuits Conference, A-SSCC 2016 - Proceedings	0
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A 12-b ENOB 2.5-MHz BW VCO-Based 0-1 MASH ADC With Direct Digital Background Calibration	Ragab, K., Sun, N.	2017	IEEE Journal of Solid-State Circuits	0
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A 10-b 750 $\mu$ W 200MS/s fully dynamic single-channel SAR ADC in 40nm CMOS	Tang, X., Chen, L., Song, J., Sun, N.	2016	European Solid-State Circuits Conference	0
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A 18.5-fJ/step VCO-based 0-1 MASH $\Delta\Sigma$ ADC with digital background calibration	Sanyal, A., Sun, N.	2016	IEEE Symposium on VLSI Circuits, Digest of Technical Papers	1
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Comparator common-mode variation effects analysis and its application in SAR ADCs	Chen, L., Sanyal, A., Ma, J., Tang, X., Sun, N.	2016	Proceedings - IEEE International Symposium on Circuits and Systems	1
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Second-order VCO-based $\delta\sigma$ ADC using a modified DPLL	Sanyal, A., Sun, N.	2016	Electronics Letters	0
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Dynamic Element Matching Techniques for Static and Dynamic Errors in Continuous-Time Multi-Bit $\Delta\Sigma$ Modulators	Sanyal, A., Sun, N.	2015	IEEE Journal on Emerging and Selected Topics in Circuits and Systems	3
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