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- B.Sc. -Electronics Engineering Instituto Militar de Engenharia (IME), Rio de Janeiro, RJ, Brazil, December 1984
- M.Sc. -Electrical Engineering -COPPE/Universidade Federal do Rio de Janeiro, RJ, Brazil, September 1990. Thesis: The MTB-Mesh-Block Transform
- Ph.D. -Electronics Systems Engineering Essex University - Colshester, UK, June 1995. Thesis: Wavelet Transforms for Image Coding



Eduardo Antônio Barros da Silva was born in Rio de Janeiro, Brazil, in 1963. He got his B.Sc in Engineering from Instituto Militar de Engenharia (IME) in 1984, his M.Sc in Electrical Engineer from COPPE/UFRJ in 1990 and his Ph.D in Electronics Systems Engineering from Essex University, England in 1995.

In 1985 he worked has an engineer for EMBRATEL (a Brazilian Telecommunications Holding). In 1986 he worked as an engineer for IBM-Brazil. In 1987 and 1988 he worked at the Departament of Electrical Engineering of Instituto Militar de Engenharia (IME), Rio de Janeiro, Brazil. Since 1989 he works at the Electronics and Computer Science Departament from Escola Politécnica (former Escola de Engenharia), from Universidade Federal do Rio de Janeiro (UFRJ). He was coordinator of the specialization course in Telecomunication Systems from Escola Politécnica (TELECOM) from 2003 to 2008. He also works since 1996 at the Programa de Engenharia Elétrica from COPPE/UFRJ, where he was Head of Department in 2002.

In 2002, 2003 and 2008 and 2009 he has been Associate Editor of IEEE Transactions on Circuits and systems, Part I, in the area of Digital Filters, Wavelets and Filter Banks. In 2006 and 2007 he has been Associate Editor of IEEE Transactions on Circuits and systems, Part II, in the area of Digital Signal Processing, and has been Associate Editor of the Journal Multidimensional Systems and Signal Processing, from Springer Verlag, since 2006.

He gives consulting and trainning in Image and Video Compression for TV operators in Brazil. In 1994 he won the "British Telecom Postgraduate Publication Prize" for his IEEE article on Aliasing in Subband Coding. He has been appointed a <u>Distinguished Lecturer of the IEEE CAS Society</u> for 2003 and 2004. He also is co-author of the book "<u>Digital Signal Processing - System Analysis and Design"</u>, published by Cambridge University Press. His research

interesses are in the areas of Digital Signal and Image Processing, Signal Coding, Digital Television, Wavelet Transforms, Mathematical Morphology and Telecommunications Applications. He is a member of IEEE, of the Brazilian Telecommunication Society (SBrT) and the Brazilian Television Engineering Society (SET).

Selected Publications

• Diniz, P. S. R., da Silva, E. A. B., and Lima Netto, S.,

Digital Signal Processing: System Analysis and Design, 2nd Edition, Cambridge University Press, Cambridge, UK, 2010, ISBN: 978-0-0521-88775-5 (970 pages).

For acquisition, contact <u>Cambridge University Press</u> in the US, or <u>Amazon</u>.

Other editions:

- Chinese English language edition: ISBN 9787111382539, 7111382536, ASIN: B008FLUAPQ, http://www.hzbook.com, www.china-pub.com.

For Acquisition contact **China**.

- Portuguese language edition: <u>Bookman</u>, Brazil, 2014. ISBN 9788582601235
- Diniz, P. S. R., da Silva, E. A. B., and Lima Netto, S.,

Digital Signal Processing: System Analysis and Design,

Cambridge University Press, Cambridge, UK, 2002, ISBN 0-521-78175-2.

For Acquisition contact <u>Cambridge University Press</u> in the UK, <u>Cambridge University Press</u> in the US, or <u>Amazon</u>.

Other editions:

- Portuguese language edition: <u>Bookman</u>, Brazil, 2004. ISBN 85-363-0418-9.
- Chinese english language edition: <u>Publishing House of Electronics</u> <u>Industry</u>, China. ISBN 7-5053-8171-7.
- Chinese language edition: <u>Publishing House of Electronics</u> <u>Industry</u>, China.

ISBN 7-121-00063-6

- South Asian Edition: ISBN 0-521-54083-6

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