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### Professor at the Institute PES



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CV

Johann W. Kolar received his M.Sc. degree in Industrial Electronics and Control Engineering (with highest honors) and the Ph.D. degree in Electrical Engineering (summa cum laude / promotio sub auspiciis praesidentis rei publicae) from the Vienna University of Technology, Austria, in 1997 and 1999, respectively. Since 1984 he has been working as an independent researcher and international consultant in close collaboration with the Vienna University of Technology, in the fields of power electronics, industrial electronics and high performance drives. He was appointed Assoc. Professor and Head of the Power Electronic Systems Laboratory at the Swiss Federal Institute of Technology (ETH) Zurich on Feb. 1, 2001, and was promoted to the rank of Full Prof. in 2004. Dr. Kolar has proposed numerous novel converter topologies and modulation and control concepts, e.g., the VIENNA Rectifier, the SWISS Rectifier, the Delta-Switch Rectifier, the Isolated Y-Matrix AC-DC Converter and the three-phase AC-AC Sparse Matrix Converter. He has published over 500 scientific papers at main international conferences, over 200 papers in international journals, and 3 book chapters. Furthermore, he has filed more than 140 patents. He has presented over 20 educational seminars at leading international conferences and has served as IEEE PELS Distinguished Lecturer from 2012 through 2016.

The focus of his current research is on AC-AC and AC-DC converter topologies with low effects on the mains, e.g. for data centers, More-Electric-Aircraft and distributed renewable energy systems, Solid-State Transformers for smart microgrid systems, and Inductive Power Transfer for EV/HEV charging and medical applications). Further main research areas are the realization of ultra-compact and ultra-efficient converter modules employing latest power semiconductor technology (SiC and GaN), Micro Power Electronics and/or Power Supplies on Chip, multi-domain/scale modeling/simulation and multi-objective (Pareto) optimization, as well as ultra-high speed and ultra-light weight drives, bearingless motors, and energy harvesting.

Dr. Kolar has received 11 IEEE Transactions Prize Paper Awards, 14 IEEE Conference Prize Paper Awards, the PCIM Europe Conference Prize Paper Award 2013, the SEMIKRON Innovation Award 2014, the 2014 IEEE Power Electronics Society R. David Middlebrook Achievement Award, and the 2016 IEEE William E. Newell Power Electronics Award, Furthermore, he received the ETH Zurich Golden Owl Award 2011 for Excellence in Teaching and an Erskine Fellowship from the University of Canterbury, New Zealand, in 2003. In 2006, the European Power Supplies Manufacturers Association (EPSMA) awarded the Power Electronics Systems Laboratory of ETH Zurich as the leading academic research institution in Power Electronics in Europe.

He has supervised over 60 Ph.D. students and PostDocs and has initiated and/or is the founder/co-founder of 4 spin-off companies targeting ultra-high speed drives, multidomain/level simulation, ultra-compact/efficient converter systems and pulsed power/electronic energy processing.



Publications prior to 2001 Publications after 2001

Dr. Kolar is a Fellow of the IEEE, a Member of the IEEJ, and a member of International Steering Committees and Technical Program Committees of numerous international conferences in the field (e.g. Director of the Power Quality Branch of the International Conference on Power Conversion and Intelligent Motion). He is the founder and chair of the IEEE PELS Austria Chapter (1997-2001) and of the IEEE PELS Switzerland Chapter (2001-2011), and Chairman of the Education Chapter of the European Power Electronics (EPE) Association (since 2001). From 1997 through 2000 he has been serving as Associate Editor of the IEEE Transactions on Industrial Electronics and from 2001 through 2013 as Associate Editor of the IEEE Transactions on Power Electronics. Since 2002 he also is an Associate Editor of the Journal of Power Electronics of the Korean Institute of Power Electronics and a member of the Editorial Advisory Board of the IEEJ Transactions on Electrical and Electronic Engineering.

#### Courses

- Networks and Circuits I
- Networks and Circuits I
- Networks and Circuits II
- Power Electronic Systems I
- Power Electronic Systems I
- Power Electronic Systems II
- Power Electronics
- Power Electronics

#### **Publications**

### Conference Papers

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- L. Schrittwieser, M. Leibl, M. Haider, F. Thöny, J. W. Kolar, T. B. Soeiro, 99.3% Efficient Three-Phase Buck-Type All-SiC SWISS Rectifier for DC Distribution Systems, Proceedings of the 32nd Applied Power Electronics Conference and Exposition (APEC 2017), Tampa, FL, USA, March 26-30, 2017.
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- M. Antivachis, M. Kasper, D. Bortis, J. W. Kolar, Analysis of Capacitive Power Transfer GaN ISOP Multi-Cell DC/DC Converter Systems for Single-Phase Telecom Power Supply Modules, Proceedings of the 42nd Annual Conference of the IEEE Industrial Electronics Society (IECON 2016), Florence, Italy, October 23-27, 2016. Video
- J. W. Kolar, R. Bosshard, Inductive EV Battery Charging Systems Requirements, Basics, Limitations, Future Research, Keynote Presentation at the 42nd Annual Conference of the IEEE Industrial Electronics Society (IECON 2016), Florence, Italy, October 23-27, 2016.
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- O. Knecht, J. W. Kolar, Impact of Transcutaneous Energy Transfer on the Electric Field and Specific Absorption Rate in the Human Tissue, Proceedings of the 41th Annual Conference of the IEEE Industrial Electronics Society (IECON 2015), Yokohama, Japan, November 9-12, 2015.
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- I. Kovacevic, R. Burkart, C. Dittli, A. Müsing, J. W. Kolar, Fast Method for the Calculation of Power Losses in Foil Windings, Proceedings of the 17th European Conference on Power Electronics and Applications (ECCE Europe 2015), Geneva, Switzerland, September 8-10, 2015.
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- I. Kovacevic, T. Friedli, J. W. Kolar, PEEC-Based Modeling of EMI Filters Including Parasitics and Geometrical Arrangement of Filter-Elements, ECPE Workshop "Parasitic Effects in Power Electronics", Berlin, Germany, February 23-24, 2011.
- M. Schweizer, T. Friedli, J. W. Kolar, Comparative Evaluation of Advanced 3-level Inverter/Converter Topologies against 2-level Systems, ECPE Workshop "Advanced Multilevel Converter Systems", Västeras, Sweden, September 28-29, 2010.
- J. W. Kolar, Performance Trends and Limitations of Electronic EEnergy Processing Systems, Invited Distinguished Lecture, University of Padova, Department of Information Engineering, September 10, 2010.
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# Further Publications

- J. W. Kolar, Power Electronic Topologies Do We Need More or Any Benefit to New Ones?, Presentation in Rap Session "Power Electronics Topologies Do We Need More or Any Benefit to New Ones?", 32nd Applied Power Electronics Conference and Exposition (APEC 2017), Tampa, FL, USA, March 26-30, 2017.
- J. W. Kolar, et al., Power Magnetics @ High Frequency State-of-the-Art and Future Prospects, Keynote Presentation in Industry Session "High Frequency Magnetics Transforming the Black Magic to Engineering", 32nd Applied Power Electronics Conference and Exposition (APEC 2017), Tampa, FL, USA, March 26-30, 2017.
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