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Design of linear phase FIR filters in subexpression space using mixed integer linear programming Yu, Y.J., Lim, Y.C. 2007 IEEE Transactions on Circuits and Systems I: Regular Papers 56

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Design of linear phase FIR filters with high probability of achieving minimum number of adders Shi, D., Yu, Y.J. 2011 IEEE Transactions on Circuits and Systems I: Regular Papers 55

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Optimum masking levels and coefficient sparseness for Hilbert transformers and half-band filters designed using the frequency-response masking technique Lim, Y.C., Yu, Y.J., Saramäki, T. 2005 IEEE Transactions on Circuits and Systems I: Regular Papers 33

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A novel genetic algorithm for the design of a signed power-of-two coefficient quadrature mirror filter lattice filter bank Yu, Y.J., Lim, Y.C. 2002 Circuits, Systems, and Signal Processing 31

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Synthesis of very sharp Hilbert transformer using the frequency-response masking technique Lim, Y.C., Yu, Y.J. 2005 IEEE Transactions on Signal Processing 30

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Optimization of linear phase FIR filters in dynamically expanding subexpression space Yu, Y.J., Lim, Y.C. 2010 Circuits, Systems, and Signal Processing 23

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Genetic algorithm approach for the optimization of multiplierless sub-filters generated by the frequency-response masking technique Yu, Y.J., Lim, Y.C. 2002 Proceedings of the IEEE International Conference on Electronics, Circuits, and Systems 20

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Generalization of Orthogonal Frequency Division Multiplexing With Index Modulation Fan, R., Yu, Y.J., Guan, Y.L. 2015 IEEE Transactions on Wireless Communications 19

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FRM-based FIR filters with optimum finite word-length performance	Lim, Y.C., Yu, Y.J., Teo, K.L., Saramäki, T.	2007	IEEE Transactions on Signal Processing	19
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A width-recursive depth-first tree search approach for the design of discrete coefficient perfect reconstruction lattice filter bank	Lim, Y.C., Yu, Y.J.	2003	IEEE Transactions on Circuits and Systems II: Analog and Digital Signal Processing	19
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FPGA implementation of digital filters synthesized using the FRM technique	Lim, Y.C., Yu, Y.J., Zheng, H.Q., Foo, S.W.	2003	Circuits, Systems, and Signal Processing	18
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Successive reoptimization approach for the design of discrete coefficient perfect reconstruction lattice filter bank	Lim, Y.C., Yu, Ya Jun	2000	Proceedings - IEEE International Symposium on Circuits and Systems	17
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Single-stage and cascade design of high order multiplierless linear phase fir filters using genetic algorithm	Ye, W.B., Yu, Y.J.	2013	IEEE Transactions on Circuits and Systems I: Regular Papers	16
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Extrapolated impulse response filter and its application in the synthesis of digital filters using the frequency-response masking technique	Yu, Y.J., Teo, K.L., Lim, Y.C., Zhao, G.H.	2005	Signal Processing	16
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Design of discrete-coefficient FIR filters on loosely connected parallel machines	Lim, Y.C., Sun, Y., Yu, Y.J.	2002	IEEE Transactions on Signal Processing	15
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FRM based FIR filter design - The WLS approach	Yu, Y.J., Lim, Y.C.	2002	Proceedings - IEEE International Symposium on Circuits and Systems	15
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Fixed-point analysis and parameter selections of MSR-CORDIC with applications to FFT designs	Park, S.Y., Yu, Y.J.	2012	IEEE Transactions on Signal Processing	14
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Design of discrete-valued linear phase FIR filters in cascade form	Shi, D., Yu, Y.J.	2011	IEEE Transactions on Circuits and Systems I: Regular Papers	14
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Design of extrapolated impulse response FIR filters with residual compensation in subexpression space	Yu, Y.J., Shi, D., Lim, Y.C.	2009	IEEE Transactions on Circuits and Systems I: Regular Papers	12
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An efficient implementation of linear-phase FIR filters for a rational sampling rate conversion	Bregović, R., Saramäki, T., Yu, Y.J., Lim, Y.C.	2006	Proceedings - IEEE International Symposium on Circuits and Systems	12
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Bit-level multiplierless FIR filter optimization incorporating sparse filter technique	Ye, W.B., Yu, Y.J.	2014	IEEE Transactions on Circuits and Systems I: Regular Papers	9
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Implementation of linear-phase FIR filters for a rational sampling-rate conversion utilizing the coefficient symmetry	Bregovic, R., Yu, Y.J., Saramäki, T., Lim, Y.C.	2011	IEEE Transactions on Circuits and Systems I: Regular Papers	9
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New natural selection process and chromosome encoding for the design of multiplierless lattice QMF using genetic algorithm	Yu, Y.J., Lim, Y.C.	2001	Proceedings of the IEEE International Conference on Electronics, Circuits, and Systems	9
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Orthogonal frequency division multiplexing with generalized index modulation	Fan, R., Yu, Y.J., Guan, Y.L.	2014	2014 IEEE Global Communications Conference, GLOBECOM 2014	8
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Low-complexity design of variable bandedge linear phase FIR filters with sharp transition band	Yu, Y.J., Lim, Y.C., Shi, D.	2009	IEEE Transactions on Signal Processing	8
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Roundoff noise analysis of signals represented using signed power-of-two terms	Yu, Y.J., Lim, Y.C.	2007	IEEE Transactions on Signal Processing	8
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Mixed-radix fast filter bank approach for the design of variable digital filters with simultaneously tunable bandedge and fractional delay	Yu, Y.J., Xu, W.J.	2012	IEEE Transactions on Signal Processing	7
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Restoring coefficient symmetry in polyphase implementation of linear-phase FIR filters	Yu, Y.J., Lim, Y.C., Saramäki, T.	2006	Circuits, Systems, and Signal Processing	7
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High-speed multiplier block design based on bit-level critical path optimization	Lou, X., Yu, Y.J., Meher, P.K.	2014	Proceedings - IEEE International Symposium on Circuits and Systems	6
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Design of variable bandedge FIR filters with extremely large bandedge variation range	Yu, Y.J.	2011	Proceedings - IEEE International Symposium on Circuits and Systems	6
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Implementation of linear-phase FIR nearly perfect reconstruction cosine-modulated filterbanks utilizing the coefficient symmetry	Bregović, R., Yu, Y.J., Viholainen, A., Lim, Y.C.	2010	IEEE Transactions on Circuits and Systems I: Regular Papers	6
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Optimization of FIR filters in subexpression space with constrained adder depth	Yu, Y.J., Lim, Y.C.	2009	ISPA 2009 - Proceedings of the 6th International Symposium on Image and Signal Processing and Analysis	6
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An efficient algorithm for the optimization of FIR filters synthesized using the multistage frequency-response masking approach	Yli-Kaakinen, J., Saramäki, T., Yu, Y.J.	2004	Proceedings - IEEE International Symposium on Circuits and Systems	6
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An iterative method for optimizing FIR filters synthesized using the two-stage frequency-response masking technique	Yu, Y.J., Saramäki, T., Lim, Y.C.	2003	Proceedings - IEEE International Symposium on Circuits and Systems	6
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An adaptive body-bias low voltage low power LC VCO	Sun, P., Wang, G., Woods, W., Wang, H., Yu, Y.J.	2010	ISCAS 2010 - 2010 IEEE International Symposium on Circuits and Systems: Nano-Bio Circuit Fabrics and Systems	5
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Optimization of extrapolated impulse response filters using semi-infinite programming	Yu, Y.J., Zhao, G., Tea, K.L., Lim, Y.C.	2004	International Symposium on Control, Communications and Signal Processing, ISCCSP	5
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Two-step optimization approach for the design of multiplierless linear-phase FIR filters	Ye, W.B., Yu, Y.J.	2015	IEEE Transactions on Circuits and Systems I: Regular Papers	4
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Fine-grained critical path analysis and optimization for area-time efficient realization of multiple constant Multiplications	Lou, X., Yu, Y.J., Meher, P.K.	2015	IEEE Transactions on Circuits and Systems I: Regular Papers	4
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Guest editorial: Low-Power digital filter design techniques and their applications	Lian, Y., Yu, Y.J.	2010	Circuits, Systems, and Signal Processing	4
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Sparse FIR filter design based on Genetic Algorithm	Zhao, H., Ye, W.B., Yu, Y.J.	2013	Proceedings - IEEE International Symposium on Circuits and Systems	3
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Investigation on the optimization criteria for the design of variable fractional delay filters	Yu, Y.J., Xu, W.J.	2013	IEEE Transactions on Circuits and Systems II: Express Briefs	3
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Design of high order and wide coefficient wordlength multiplierless FIR filters with low hardware cost using genetic algorithm	Ye, W.B., Yu, Y.J.	2012	ISCAS 2012 - 2012 IEEE International Symposium on Circuits and Systems	2
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FRM-based FIR filters with minimum coefficient sensitivities	Lim, Y.C., Yu, Y.J., Teo, K.L., Saramäki, T.	2007	Proceedings - IEEE International Symposium on Circuits and Systems	2

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Signed power-of-two allocation scheme for the design of lattice orthogonal filter banks	Yu, Y.J., Lim, Y.C.	2005	Proceedings - IEEE International Symposium on Circuits and Systems	2
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Optimization and implementation of extrapolated impulse response filters	Yu, Y.J., Zhao, G., Teo, K.L., Lim, Y.C.	2003	Proceedings of 2003 International Conference on Neural Networks and Signal Processing, ICNNSP'03	2
--	--	------	--	---

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Greedy Algorithm for the Design of Linear-Phase FIR Filters with Sparse Coefficients	Ye, W., Yu, Y.J.	2016	Circuits, Systems, and Signal Processing	1
--	------------------	------	--	---

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Two-Step Optimization Approach for the Design of Multiplierless Linear-Phase FIR Filters	Ye, W. B., Yu, Y. J.	2015	IEEE Transactions on Circuits and Systems I: Regular Papers	1
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