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Atila Alvandpour received the M.S. and Ph.D. degrees from Linköping University, Sweden, in 1995 and 1999, respectively. From 1999 to 2003, he was a senior research scientist with Circuit Research Lab, Intel Corporation. In 2003, he joined the department of Electrical Engineering, Linköping University, as a Professor of VLSI design. From 2004 to 2014 he was head of Electronic Devices Division, and since 2014 he is head of Integrated Circuits and Systems Division. Also, he is currently the vice head of the department. His research interests include various issues in design of integrated circuits and systems in advanced nano-scale technologies, with special focus on data converters (ADCs and DACs), analog front-ends, sensor readout and data acquisition systems, energy-harvesting and power management systems, analog/digital baseband and RF frontends for multi-Gigabit/s radio transceivers, low-power wireless sensors, clock generators/synthesizers, as well as multi-GHz digital circuits and building blocks.

He has published more than 100 papers in international journals and conferences, and holds 24 U.S. patents. Prof. Alvandpour is a senior member of IEEE, and has served as a member of technical program committees for many IEEE and other international conferences, including the IEEE Solid-State Circuits Conference, ISSCC, and the European Solid-State Circuits Conference, ESSCIRC. He has also served as guest editor for IEEE Journal of Solid-State Circuits.

Short CV (pdf)
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List of Publications

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Electronic Devices Division

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Journals:

- [1] A. Bhide, O. E. Najari, B. Mesgarzadeh, and A. Alvandpour, "An 8-GS/s 200-MHz Bandwidth 68-mW $\Delta\Sigma$ DAC in 65-nm CMOS", in IEEE Transactions on Circuits and Systems II, vol. 60, no. 7, pp. 387-391, July 2013.
- [2] Y. Jung; J. Fritzin, M. Enqvist, A. Alvandpour, "Least-Squares Phase Predistortion of a +30 dBm Class-D Outphasing RF PA in 65 nm CMOS", in IEEE Transactions on Circuits and Systems I, vol. 60, no. 7, pp. 1915-1928, July 2013.
- [3] Fazli Yeknami, F. Qazi and A. Alvandpour, "Low-Power DT $\Delta\Sigma$ Modulators Using SC Passive Filters in 65nm CMOS," in IEEE Transaction on Circuits and Systems-I, vol. pp, no. 99, pp. 1-13, 2013.
- [4] D. Svärd, C. Jansson and A. Alvandpour, "A Readout IC for an Uncooled Microbolometer infrared FPA with On-chip Self-heating Compensation in 0.35 μm CMOS", in Journal of Analog Integrated Circuits and Signal Processing, vol. 77, no. 1, pp. 29 - 44, Oct. 2013.
- [5] Fazli Yeknami and A. Alvandpour, "A 2.1 μW 80 dB SNR DT $\Delta\Sigma$ Modulator for Medical Implant Devices," in Journal of Analog Integrated Circuits and Signal Processing, vol. 77 , pp. 69-78, no. 1, Oct. 2013.
- [6] H. Raza Khan, J. Fritzin, A. Alvandpour , Q. Wahab," A parallel circuit differential class-E power amplifier using series", in Journal of Analog Integrated Circuits and Signal Processing, vol. 75, no. 1, pp. 31-40, April 2013.
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- [9] J. Fritzin, C. Svensson, and A. Alvandpour, "Design and Analysis of a Class-D Stage with Harmonic Suppression," in IEEE Transactions on Circuits and Systems-I, vol. 59, no. 6, pp. 1178-1186, 2012.
- [10] D. Zhang, A. Bhide, and A. Alvandpour, "A 53-nW 9.1-ENOB 1-kS/s SAR ADC in 0.13- μm CMOS for Medical Implant Devices," in IEEE Journal of Solid-State Circuits, vol. 47, no. 7, pp.1585-1593, July 2012.
- [11] J. Fritzin, Y. Jung, P.N. Landin, P. Handel, M. Enqvist, and A. Alvandpour, "Phase Predistortion of a Class-D Outphasing RF Amplifier in 90nm CMOS," IEEE Transactions on Circuits and Systems-II, vol. 58, no. 10, pp. 642-646, Oct. 2011.

- [12] T. Sundström, C. Svensson, and A. Alvandpour, "A 2.4 GS/s, Single-Channel, 31.3 dB SNDR at Nyquist, 8-bit Pipeline ADC in 65nm CMOS," in *Journal of Solid State Circuits*, vol. 46, no. 7, pp. 1575-1584 July 2011.
- [13] J. Fritzlin and A. Alvandpour, "A 3.3V 72.2Mbit/s 802.11n WLAN Transformer-Based Power Amplifier in 65nm CMOS" in *Journal of Analog Integrated Circuits and Signal Processing*, vol. 64, no. 3, pp. 241-247, Sept. 2010.
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- [25] D. Zhang and A. Alvandpour, "A 3-nW 9.1-ENOB SAR ADC at 0.7 V and 1 kS/s," in *IEEE European Solid-State Circuits Conference (ESSCIRC)*, pp. 369-372, September 2012.
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