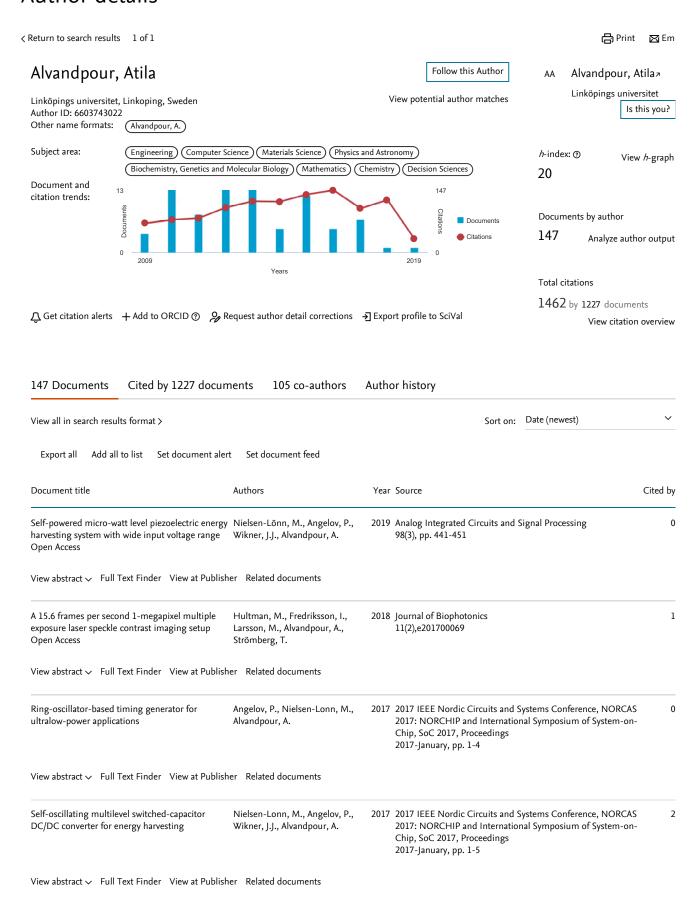
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Document title	Authors	Year	Source	Cited by
Design and analysis of high-speed split- segmented switched-capacitor DACs	Duong, QT., Bhide, A., Alvandpour, A.	2017	Analog Integrated Circuits and Signal Processing 92(2), pp. 199-217	0
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Capacitive charge pump gain-stage with source follower buffers for pipelined SAR ADCs	Chen, K., Alvandpour, A.	2017	2016 International Symposium on Integrated Circuits, ISIC 2016 7829747	0
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VCO-based ADCs for IoT applications	Unnikrishnan, V., Vesterbacka, M., Alvandpour, A.	2017	2016 International Symposium on Integrated Circuits, ISIC 2016 7829746	1
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Interface power-transfer conditions for piezoelectric energy harvesting of damped waves	Nielsen-Lönn, M., Alvandpour, A.	2017	International Conference and Exhibition on Integration Issues of Miniaturized Systems 2017, SSI 2017 pp. 428-431	0
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A pipelined SAR ADC with gain-stage based on capacitive charge pump Open Access	Chen, K., Alvandpour, A.	2017	Analog Integrated Circuits and Signal Processing 90(1), pp. 43-53	0
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Design of a gain-stage for pipelined SAR ADC using capacitive charge pump	Chen, K., Alvandpour, A.	2016	Proceedings of the 23rd International Conference Mixed Design of Integrated Circuits and Systems, MIXDES 2016 7529729, pp. 187-190	2
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A 0.4-V Subnanowatt 8-Bit 1-kS/s SAR ADC in 65- nm CMOS for Wireless Sensor Applications	Harikumar, P., Wikner, J.J., Alvandpour, A.	2016	IEEE Transactions on Circuits and Systems II: Express Briefs 63(8),7410048, pp. 743-747	6
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A 12.5-ENOB 10-kS/s Redundant SAR ADC in 65-nm CMOS	Zhang, D., Alvandpour, A.	2016	IEEE Transactions on Circuits and Systems II: Express Briefs 63(3),7279120, pp. 244-248	11
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Design of a 12.8 ENOB, 1 kS/s pipelined SAR ADC in 0.35-µm CMOS	Chen, K., Harikumar, P., Alvandpour, A.	2016	Analog Integrated Circuits and Signal Processing 86(1), pp. 87-98	6
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An ultra-low-voltage OTA in 28 nm UTBB FDSOI CMOS using forward body bias	Harikumar, P., Wikner, J.J., Alvandpour, A.	2015	2015 Nordic Circuits and Systems Conference, NORCAS 2015: NORCHIP and International Symposium on System- on-Chip, SoC 2015 7364416	0

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A fully-differential OTA in 28 nm UTBB FDSOI CMOS for PGA applications	Harikumar, P., Wikner, J.J., Alvandpour, A.	2015	2015 European Conference on Circuit Theory and Design, ECCTD 2015 7300114	1
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Design of efficient CMOS rectifiers for integrated piezo-MEMS energy-harvesting power management systems	Nielsen-Lonn, M., Harikumar, P., Wikner, J.J., Alvandpour, A.	2015	2015 European Conference on Circuit Theory and Design, ECCTD 2015 7300010	2
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An 11 GS/s 1.1 GHz Bandwidth Interleaved $\Delta\Sigma$ DAC for 60 GHz Radio in 65 nm CMOS	Bhide, A., Alvandpour, A.	2015	IEEE Journal of Solid-State Circuits 50(10),7206611, pp. 2306-2318	10
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Effect of Clock Duty-Cycle Error on Two-Channel Interleaved LaTeX\Delta\Sigma DACs	Bhide, A., Ojani, A., Alvandpour, A.	2015	IEEE Transactions on Circuits and Systems II: Express Brie 62(7),7065319, pp. 646-650	fs 3
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