# Prof. Bang-Sup Song

Professor, Charles Lee Powell Chair in Wireless Communication


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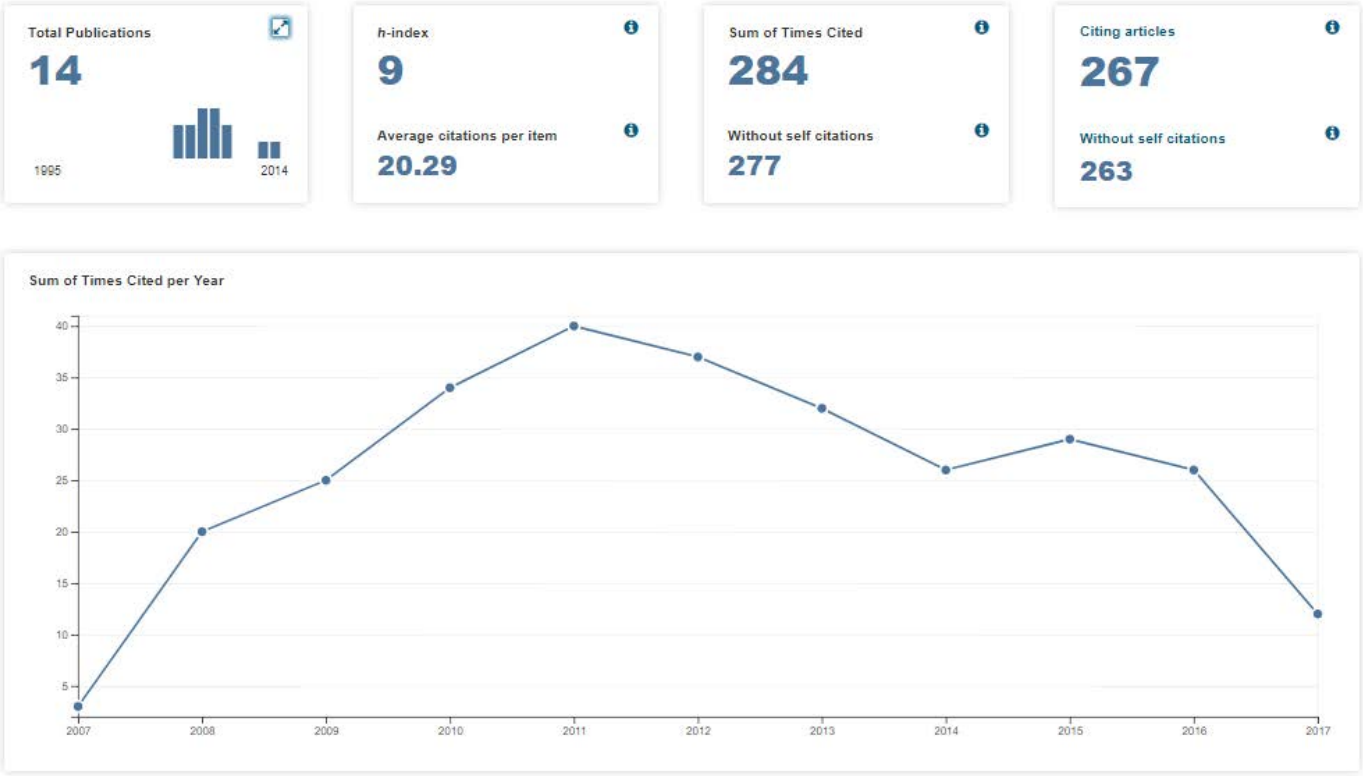
**Biography**

Dr. Song received the B.S. from the Seoul National University in 1973, the M.S. from the Korea Advanced Institute of Science in 1975, and the Ph.D. from the University of California, Berkeley in 1983. From 1975 to 1978, Dr. Song was a research staff at Agency for Defense Development, Korea working on fire-control radars and spread-spectrum communications. From 1983 to 1986, he was a member of technical staff at AT&T Bell Laboratories, Murray Hill, and was also a visiting faculty member in the Department of Electrical Engineering, Rutgers University. From 1986 to 1999, he was a professor in the Department of Electrical and Computer Engineering and the Coordinated Science Laboratory, University of Illinois at Urbana. In 1999, he joined the faculty of the Department of Electrical and Computer Engineering, University of California, San Diego, where he is endowed with Charles Lee Powell Chair in Wireless Communication.

Dr. Song received a Distinguished Technical Staff Award from AT&T Bell Laboratories in 1986, a Career Development Professor Award from Analog Devices in 1987, and a Xerox Senior Faculty Research Award from the University of Illinois in 1995. His IEEE activities have been in the capacities of an Associate Editor, a Guest Editor, and a Program Committee Member for IEEE Transactions on Circuits and Systems, IEEE Journal of Solid-State Circuits, IEEE International Solid-State Circuits Conference, and IEEE Symposium on Circuits and Systems. Dr. Song is an IEEE Fellow.



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| 2. **A 48-860 MHz CMOS low-IF direct-conversion DTV tuner**<br>By: Lerstaveesin, Supisa; Gupta, Manoj; Kang, David; et al.<br>Conference: Bipolar/BiCMOS Circuits and Technology Meeting Location: Boston, MA Date: SEP 30-OCT 02, 2007<br>Sponsor(s): IEEE Electron Devices Soc; IEEE Solid State Circuits Soc<br>IEEE JOURNAL OF SOLID-STATE CIRCUITS Volume: 43 Issue: 9 Pages: 2013-2024 Published: SEP 2008 | 5 | 3 | 3 | 3 | 0 | 45 | 4.50 |
| 3. **A 15-bit linear 20-MS/s pinelined ADC digitally calibrated with signal-dependent dithering**<br>By: Shu, Yun-Shiang; Song, Bang-Sup<br>IEEE JOURNAL OF SOLID-STATE CIRCUITS Volume: 43 Issue: 2 Pages: 342-350 Published: FEB 2008 | 5 | 3 | 4 | 0 | 0 | 45 | 4.50 |
| 4. **A 1.8-GHz spur-cancelled fractional-N frequency synthesizer with LMS-based DAC gain calibration**<br>By: Gupta, Manoj; Song, Bang-Sup<br>IEEE JOURNAL OF SOLID-STATE CIRCUITS Volume: 41 Issue: 12 Pages: 2842-2851 Published: DEC 2006 | 3 | 3 | 3 | 1 | 0 | 43 | 3.58 |
| 5. **A complex image rejection circuit with sign detection only**<br>By: Lerstaveesin, Supisa; Song, Bang-Sup<br>IEEE JOURNAL OF SOLID-STATE CIRCUITS Volume: 41 Issue: 12 Pages: 2693-2702 Published: DEC 2006 | 2 | 6 | 1 | 0 | 0 | 25 | 2.08 |
| 6. **A 13-b linear, 40-MS/s pipelined ADC with self-configured capacitor matching**<br>By: Ray, Sourja; Song, Bang-Sup<br>IEEE JOURNAL OF SOLID-STATE CIRCUITS Volume: 42 Issue: 3 Pages: 463-474 Published: MAR 2007 | 2 | 2 | 1 | 0 | 0 | 24 | 2.18 |
| 7. **LMS-Based Noise Leakage Calibration of Cascaded Continuous-Time Delta Sigma Modulators**<br>By: Shu, Yun-Shiang; Kamiishi, Junpei; Tomioka, Koji; et al.<br>IEEE JOURNAL OF SOLID-STATE CIRCUITS Volume: 45 Issue: 2 Pages: 368-379 Published: FEB 2010 | 2 | 3 | 1 | 2 | 0 | 17 | 2.12 |
| 8. **A Fifth-Order G(m)-C Continuous-Time Delta Sigma Modulator With Process-Insensitive Input Linear Range**<br>By: Aiba, Yusuke; Tomioka, Koji; Nakashima, Yuta; et al.<br>IEEE JOURNAL OF SOLID-STATE CIRCUITS Volume: 44 Issue: 9 Pages: 2381-2391 Published: SEP 2009 | 2 | 3 | 2 | 0 | 0 | 14 | 1.56 |
| 9. **A 14b 60 MS/s Pipelined ADC Adaptively Cancelling Opamp Gain and Nonlinearity**<br>By: Miyahara, Yuichi; Sano, Mitsuhiro; Koyama, Kazuo; et al.<br>IEEE JOURNAL OF SOLID-STATE CIRCUITS Volume: 49 Issue: 2 Pages: 416-425 Published: FEB 2014 | 0 | 3 | 7 | 1 | 0 | 11 | 2.75 |
| 10. **A 10 similar to 15-bit 60-MS/s Floating-Point ADC With Digital Gain and Offset Calibration**<br>By: Shu, Yun-Shiang; Kyung, MoonJung; Lee, Wei-Ming; et al.<br>IEEE JOURNAL OF SOLID-STATE CIRCUITS Volume: 44 Issue: 9 Pages: 2356-2365 Published: SEP 2009 | 2 | 0 | 0 | 0 | 0 | 7 | 0.78 |

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| 12. | **A 10 similar to 15b 60MS/s Floating-Point ADC with Digital Gain and Offset Calibration**<br>By: Shu, Yun-Shiang; Kyung, Moon-Jung; Lee, Wei-Ming; et al.<br>Book Group Author(s): IEEE<br>Conference: IEEE Custom Integrated Circuits Conference Location: San Jose, CA Date: SEP 21-24, 2008<br>Sponsor(s): IEEE; IEEE Custom Integrated Circuits Conf; IEEE Solid State Circuits Soc; IEEE Elect Devices Soc<br>PROCEEDINGS OF THE IEEE 2008 CUSTOM INTEGRATED CIRCUITS CONFERENCE Book Series: IEEE Custom Integrated Circuits Conference Pages: 157-+ Published: 2008 | 1 | 0 | 0 | 0 | 0 | 2 | 0.20 |
| 13. | **Adaptive Cancellation of Gain and Nonlinearity Errors in Pipelined ADCs**<br>By: Miyahara, Yuichi; Sano, Mitsuhiro; Koyama, Kazuo; et al.<br>Book Group Author(s): IEEE<br>Conference: IEEE International Solid-State Circuits Flagship Conference of the IEEE Solid-State-Circuits-Society (ISSCC) Location: San Francisco, CA Date: FEB 17-21, 2013<br>Sponsor(s): IEEE; IEEE Solid State Circuits Soc<br>2013 IEEE INTERNATIONAL SOLID-STATE CIRCUITS CONFERENCE DIGEST OF TECHNICAL PAPERS (ISSCC) Book Series: IEEE International Solid State Circuits Conference Volume: 56 Pages: 282-+ Published: 2013 | 0 | 0 | 0 | 0 | 0 | 0 | 0.00 |
| 14. | **LMS-Based Digital Assisting for Data Converters**<br>By: Song, Bang-Sup<br>Edited by: VanRoermund, AHM; Casier, H; Steyaert, M<br>Conference: 18th Workshop on Advances in Analog Circuit Design Location: Lund, SWEDEN Date: MAR 31-APR 02, 2009<br>ANALOG CIRCUIT DESIGN Pages: 3-21 Published: 2010 | 0 | 0 | 0 | 0 | 0 | 0 | 0.00 |

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