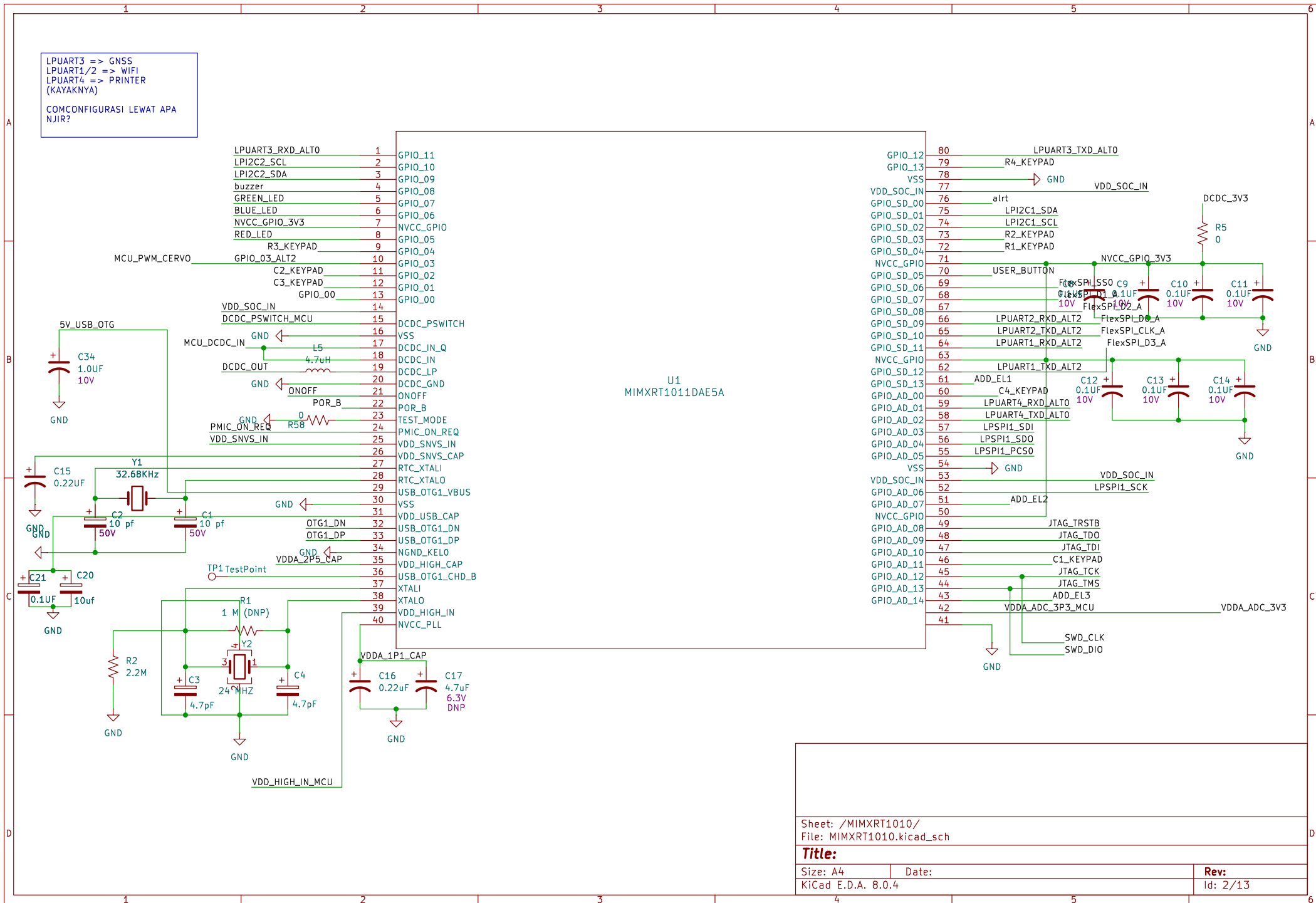
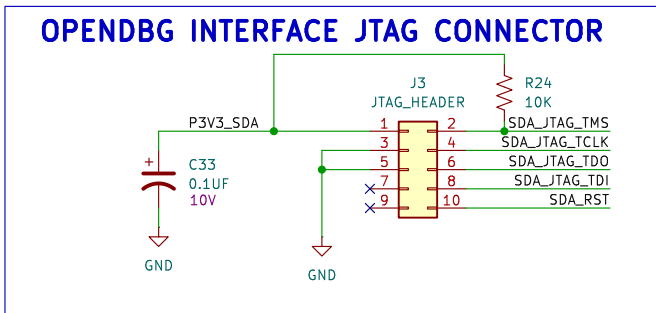
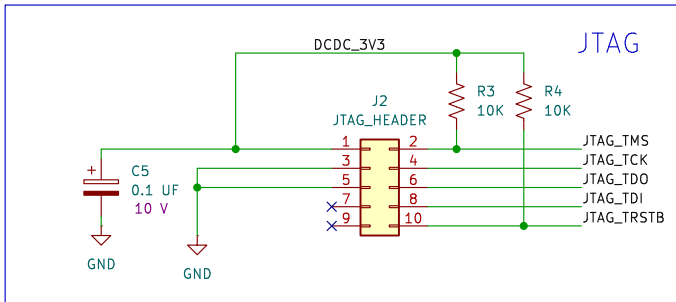


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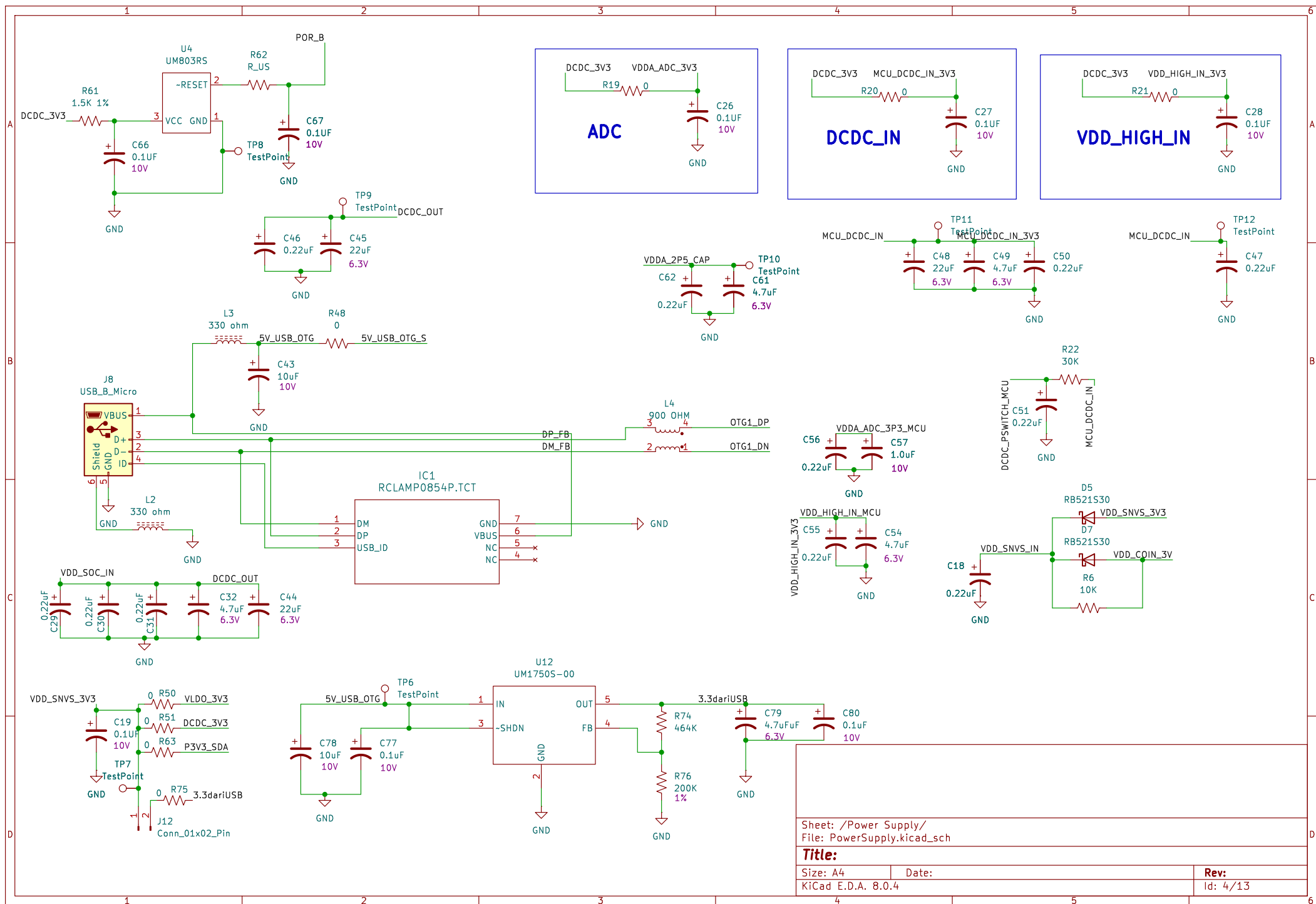
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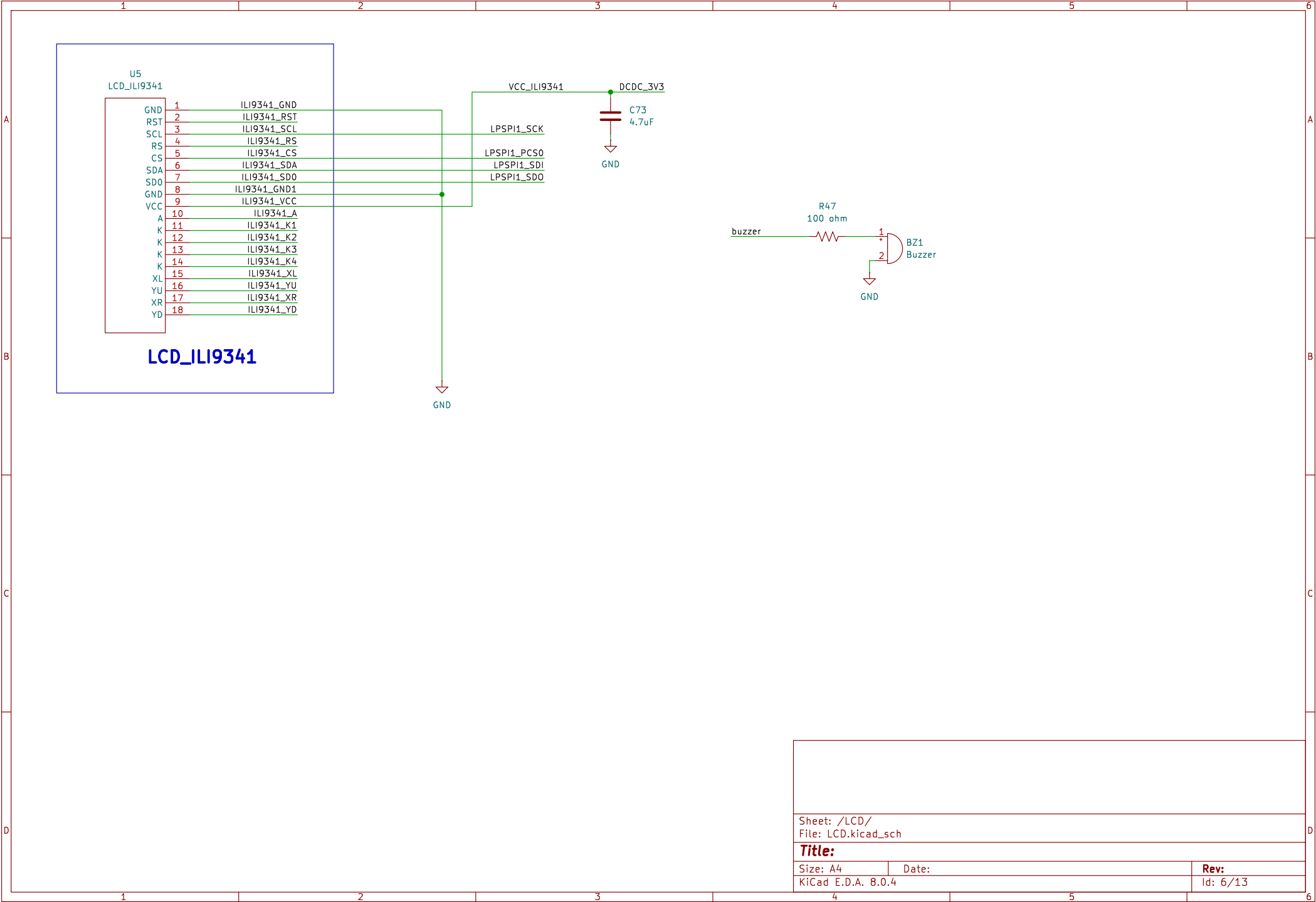


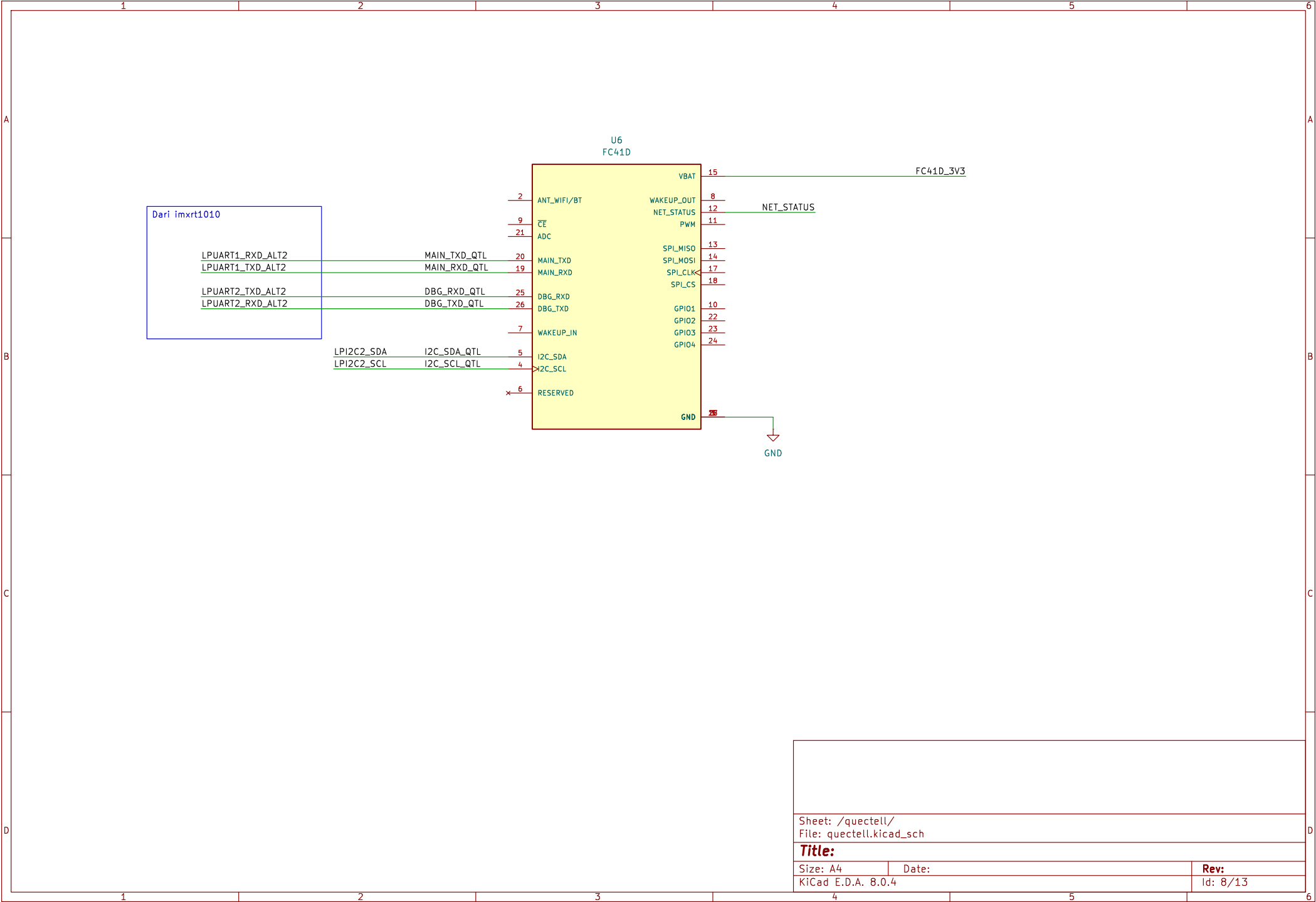
LPSPi Flash

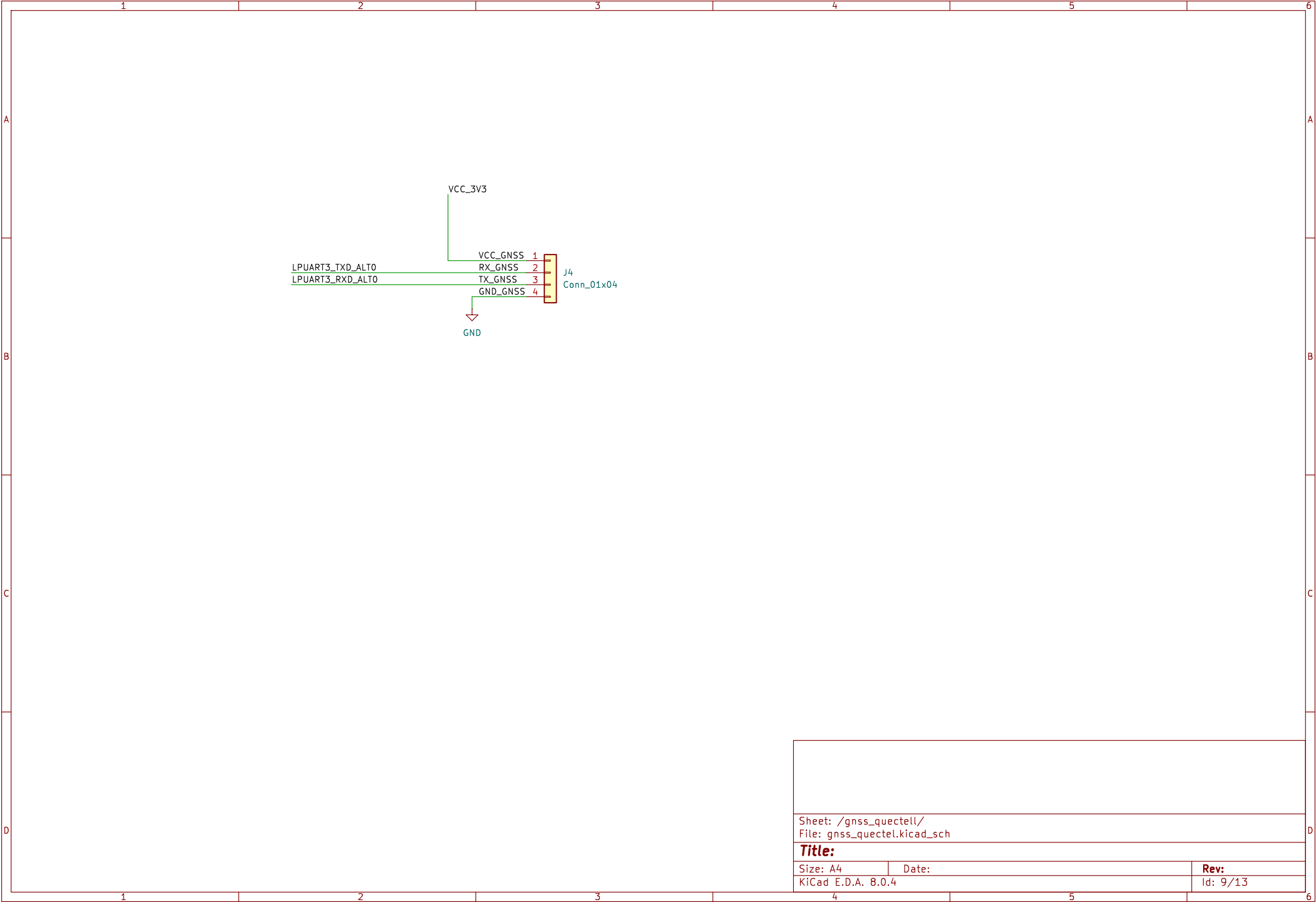
The schematic diagram illustrates the LPSPi Flash circuit. The flash memory (U2, MX25L4006EM2R-12G) is connected to the LPSPi controller. The flash is connected to DCDC_3V3 via a 0.1uF capacitor (C22) and to GND via a 0.1uF capacitor (C23). The flash is also connected to the LPSPi controller via a 10k resistor (R9) and to the LPSPi controller via a 0.1uF capacitor (C22). The flash is connected to the LPSPi controller via a 0.1uF capacitor (C22). The flash is connected to the LPSPi controller via a 0.1uF capacitor (C22).

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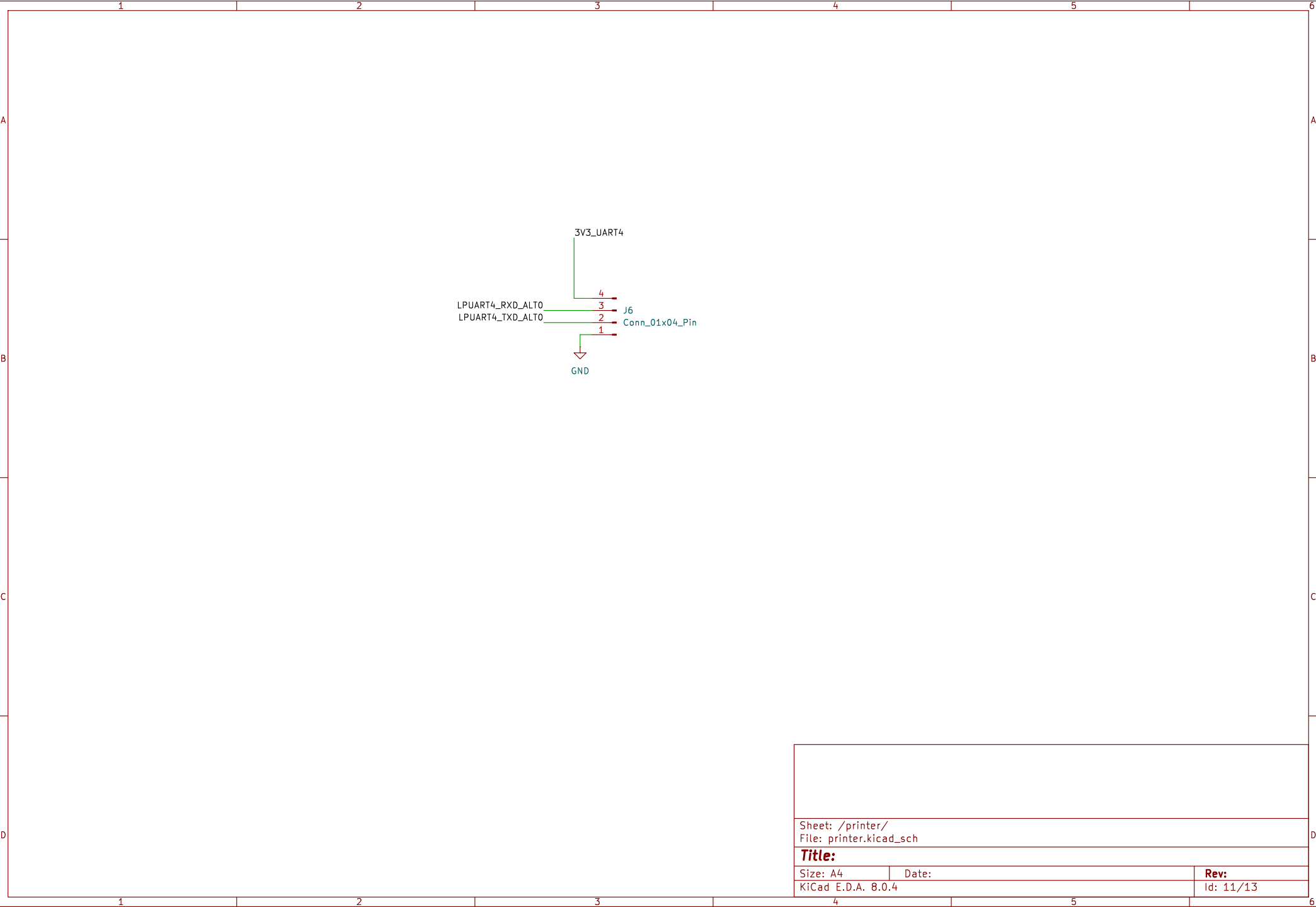
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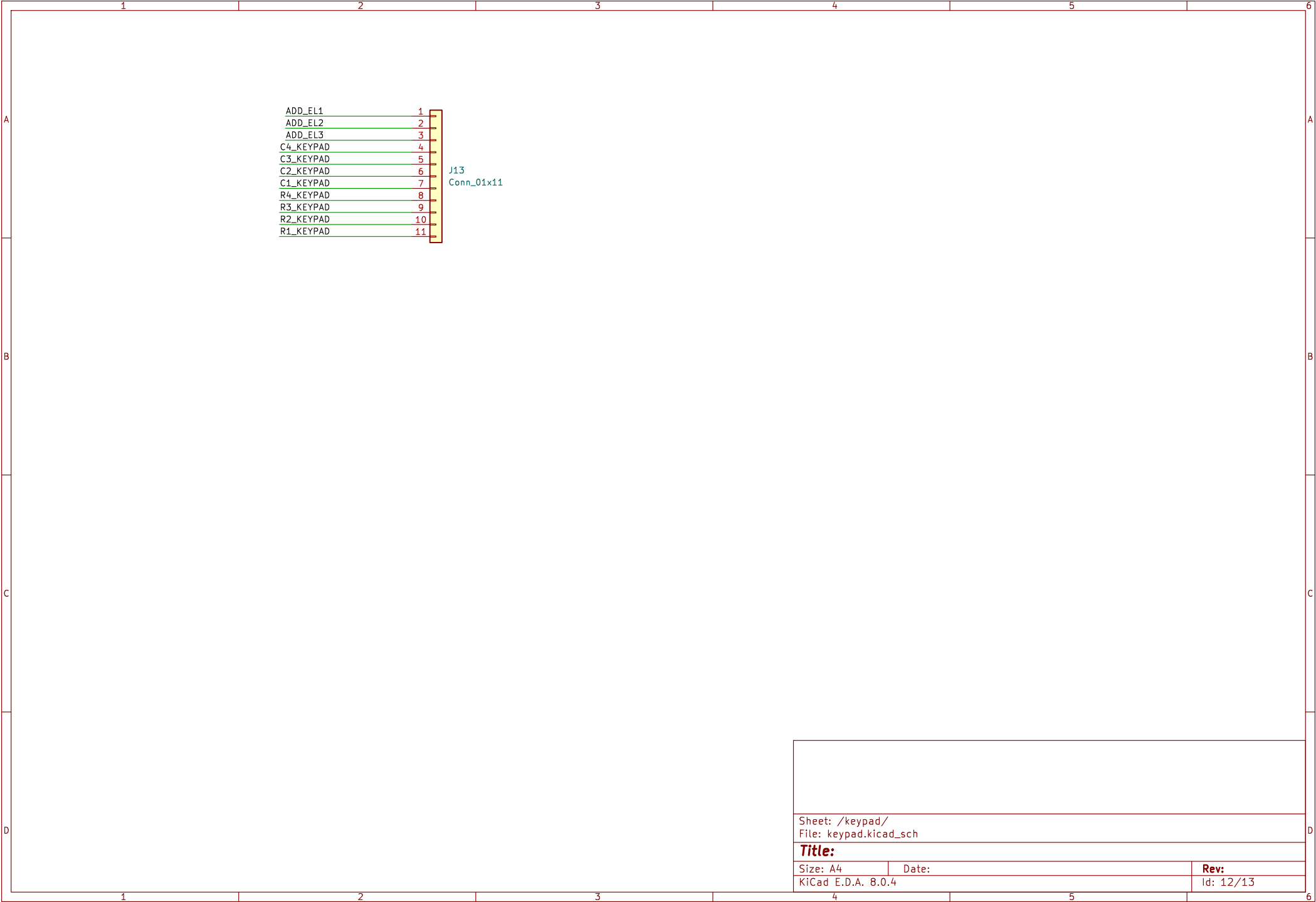
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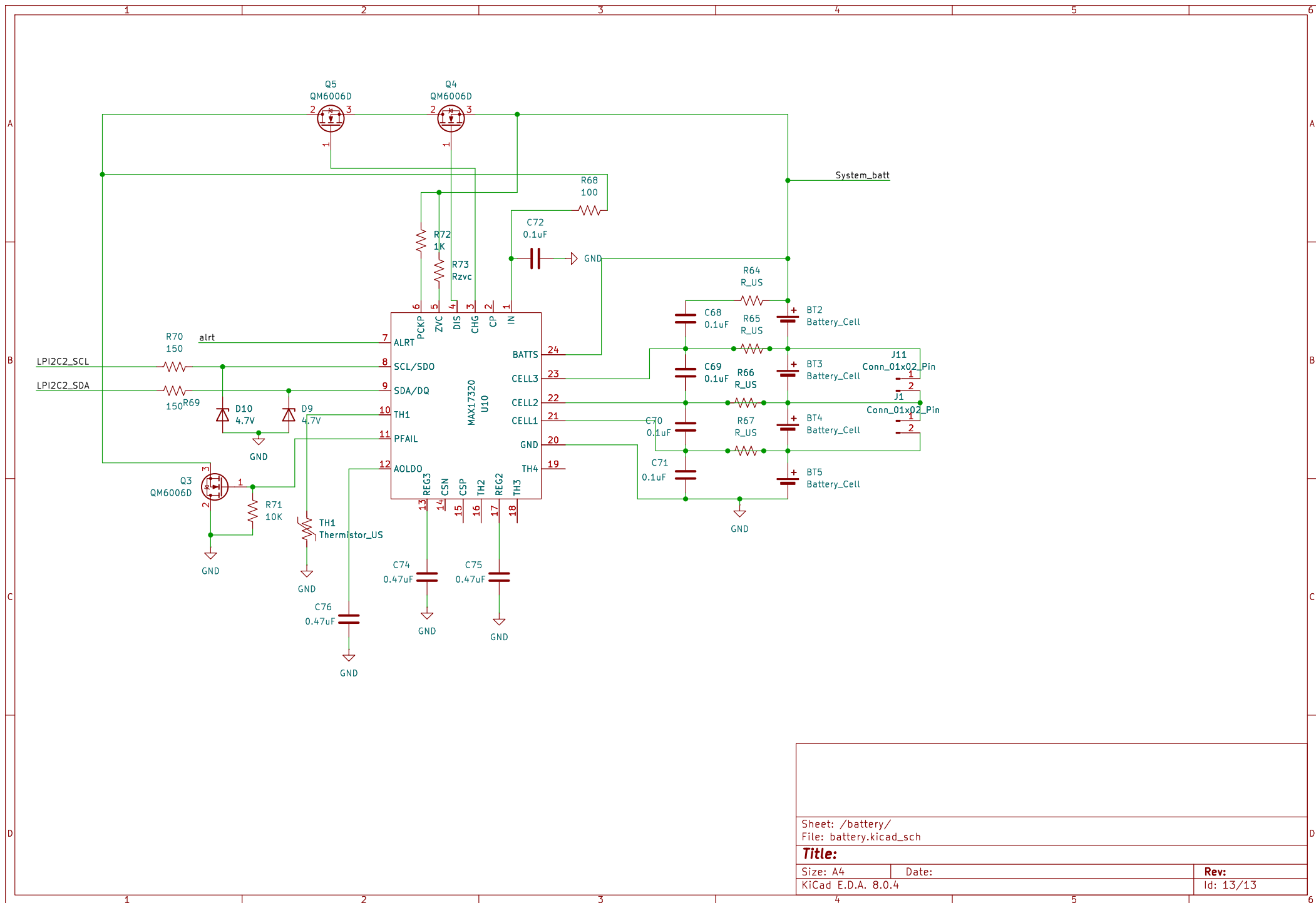
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