1. Seluruh file device tree QCS404 yang dapat dimodifikasi ada di path berikut:

```
$<work dir>/LE.UM.4.1.2/apps proc/kernel/msm-4.14/arch/arm64/boot/dts/qcom/
```

\$<work dir> adalah path relative untuk directory source code Qualcomm.

2. Untuk memodifikasi device tree terkait fungsi LED, edit file:

```
qcs405-circular-pca9956.dtsi
```

Berikut ini adalah file hasil modifikasi untuk board DVT1:

```
* Copyright (c) 2018, The Linux Foundation. All rights reserved.
* This program is free software; you can redistribute it and/or modify
* it under the terms of the GNU General Public License version 2 and
* only version 2 as published by the Free Software Foundation.
* This program is distributed in the hope that it will be useful,
* but WITHOUT ANY WARRANTY; without even the implied warranty of
* MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the
* GNU General Public License for more details.
/* Customized for DVT-1 Bahasa Kita */
&i2c 2 {
      status = "ok";
      qcom, clk-freq-out = <100000>;
      /* PCA9956B LED Drivers */
      nxp-ledseg-i2c@69 {
            #address-cells = <1>;
            #size-cells = <0>;
            compatible = "nxp,pca9956b";
            reg = <0x69>;
            pca9956b, support initialize = <1>;
            pca9956b, mode1 = <0x09>;
            pca9956b, mode2 = <0x05>;
            pca9956b, ledout0 = <0xAA>;
            pca9956b, ledout1 = <0xAA>;
            pca9956b, ledout2 = \langle 0xAA \rangle;
            pca9956b, ledout3 = \langle 0xFF \rangle;
            pca9956b, ledout4 = \langle 0xFF \rangle;
            pca9956b, ledout5 = \langle 0xFF \rangle;
            pca9956b, defaultiref = <0x2f>;
            out0@0 {
                  label = "ledsec3 b";
                 reg = <0x0>;
            };
            out101 {
                  label = "ledsec3 g";
                  reg = <0x1>;
            };
            out2@2 {
                  label = "ledsec3 r";
                  reg = <0x2>;
```

```
out3@3 {
     label = "ledsec5 b";
     reg = <0x3>;
};
out4@4 {
     label = "ledsec5 g";
     reg = <0x4>;
};
out5@5 {
     label = "ledsec5 r";
     reg = <0x5>;
};
out6@6 {
     label = "ledsec7 b";
     reg = \langle 0x6 \rangle;
};
out7@7 {
     label = "ledsec7 g";
     reg = <0x7>;
};
out8@8 {
     label = "ledsec7 r";
     reg = <0x8>;
};
out9@9 {
     label = "ledsec9_b";
     reg = <0x9>;
};
out10@10 {
     label = "ledsec9 g";
     reg = \langle 0xA \rangle;
};
out11011 {
     label = "ledsec9 r";
     reg = \langle 0xB \rangle;
};
out12@12 {
     label = "ledsec11 b";
     reg = \langle 0xC \rangle;
};
out13@13 {
     label = "ledsec11 g";
     reg = \langle 0xD \rangle;
};
out14014 {
     label = "ledsec11_r";
     reg = \langle 0xE \rangle;
};
out15@15 {
     label = "ledsec13 b";
     reg = <0xF>;
};
out16016 {
     label = "ledsec13 g";
     reg = <0x10>;
};
out17@17 {
     label = "ledsec13 r";
     reg = <0x11>;
out18@18 {
      label = "ledsec16 b";
     reg = <0x12>;
```

```
out19@19 {
                  label = "ledsec16 g";
                  reg = <0x13>;
            };
            out20@20 {
                  label = "ledsec16 r";
                  reg = <0x14>;
            };
            out21@21 {
                  label = "ledsec1 b";
                  reg = <0x15>;
            };
            out22@22 {
                  label = "ledsec1 g";
                  reg = <0x16>;
            };
            out23@23 {
                  label = "ledsec1 r";
                  reg = <0x17>;
            };
     };
};
```

Ada beberapa point yang perlu diperhatikan, yaitu:

- Pastikan nxp-ledseg-i2c sesuai dengan address I2C untuk LED Driver PCA9956B dari NXP. Perhatikan baris berikut:

```
nxp-ledseg-i2c@69
```

Nilai 69 pada baris tersebut adalah konfigurasi address I2C yang digunakan pada board DVT1. Apabila dilakukan perubahan konfigurasi address I2C, maka ubah nilai tersebut sesuai dengan nilai yang tertera pada datasheet LED Driver PCA9956B dari NXP.

- Pastikan label masing-masing LED sudah sesuai untuk mengatur pattern ketika LED menyala. Perhatikan baris berikut:

```
out000 {
    label = "ledsec3_b";
    reg = <0x0>;
};
out101 {
    label = "ledsec3_g";
    reg = <0x1>;
};
out202 {
    label = "ledsec3_r";
    reg = <0x2>;
};
```

Label ini memiliki format:

```
"ledsec<number>_<color>"
```

Perhatikan nilai number dan color. Pastikan bahwa satu number harus memiliki color b (blue), g (green), dan r (red).

3. Untuk memodifikasi device tree terkait fungsi audio seperti microphone dan speaker, edit file:

```
qcs405-wsa-audio-overlay.dtsi
```

Berikut ini adalah file hasil modifikasi untuk board DVT1:

```
* Copyright (c) 2018-2019, The Linux Foundation. All rights reserved.
 * This program is free software; you can redistribute it and/or modify
 * it under the terms of the GNU General Public License version 2 and
 * only version 2 as published by the Free Software Foundation.
 * This program is distributed in the hope that it will be useful,
* but WITHOUT ANY WARRANTY; without even the implied warranty of
* MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the
 * GNU General Public License for more details.
/* Customized for DVT-1 Bahasa Kita */
&q6core {
     wsa swr gpios: wsa swr clk data pinctrl {
           compatible = "qcom,msm-cdc-pinctrl";
            pinctrl-names = "aud active", "aud sleep";
            pinctrl-0 = <&wsa_swr_clk_active &wsa_swr_data_active>;
            pinctrl-1 = <&wsa swr clk sleep &wsa swr data sleep>;
            gcom, lpi-gpios;
      };
      wsa_spkr_en_1_2: wsa_spkr_en_1_2_pinctrl {
            compatible = "qcom,msm-cdc-pinctrl";
            pinctrl-names = "aud active", "aud sleep";
            pinctrl-0 = <&wsa_en_1_2_active>;
            pinctrl-1 = <&wsa en 1 2 sleep>;
      };
      cdc dmic01 gpios: cdc dmic01 pinctrl {
            compatible = "qcom,msm-cdc-pinctrl";
            pinctrl-names = "aud_active", "aud_sleep";
            pinctrl-0 = <&cdc_dmic01_clk_active &cdc dmic01 data active>;
            pinctrl-1 = <&cdc dmic01 clk sleep &cdc dmic01 data sleep>;
            qcom, lpi-gpios;
      };
      cdc dmic23 gpios: cdc dmic23 pinctrl {
            compatible = "qcom,msm-cdc-pinctrl";
            pinctrl-names = "aud active", "aud sleep";
           pinctrl-0 = <&cdc dmic23 clk active &cdc dmic23 data active>;
           pinctrl-1 = <&cdc dmic23 clk sleep &cdc dmic23 data sleep>;
            gcom, lpi-gpios;
      };
};
&a6core {
      bolero: bolero-cdc {
            compatible = "qcom,bolero-codec";
            clock-names = "lpass core hw vote";
           clocks = <&lpass core hw vote 0>;
           qcom, num-macros = <2>;
      };
};
&qcs405 snd {
     qcom, model = "qcs405-wsa-snd-card";
      qcom, va-bolero-codec = <1>;
      qcom, wsa-bolero-codec = <1>;
      asoc-codec = <&stub codec>, <&bolero>;
```

```
asoc-codec-names = "msm-stub-codec.1", "bolero codec";
      qcom, wsa-max-devs = <1>;
      qcom, wsa-devs = <&wsa881x 0211>, <&wsa881x 0212>,
                  <&wsa881x_0213>, <&wsa881x_0214>;
      qcom, wsa-aux-dev-prefix = "SpkrLeft", "SpkrRight",
                           "SpkrLeft", "SpkrRight";
      qcom,cdc-dmic01-gpios = <&cdc dmic01 gpios>;
      qcom,cdc-dmic23-gpios = <&cdc dmic23 gpios>;
      qcom, audio-routing =
            "VA DMICO", "VA MIC BIAS1",
            "VA MIC BIAS1", "Digital Mic0",
            "VA DMIC1", "VA MIC BIAS1",
            "VA MIC BIAS1", "Digital Mic1",
            "VA DMIC2", "VA MIC BIAS1",
            "VA MIC BIAS1", "Digital Mic2",
            "VA DMIC3", "VA MIC BIAS1",
            "VA MIC BIAS1", "Digital Mic3", "SpkrLeft IN", "WSA_SPK1 OUT",
            "SpkrRight IN", "WSA_SPK2 OUT",
            "WSA_SPK1 OUT", "VA_MCLK",
            "WSA_SPK2 OUT", "VA MCLK";
};
#include "qcs405-va-bolero.dtsi"
#include "gcs405-wsa-bolero.dtsi"
#include "qcs405-wsa881x.dtsi"
```

Ada beberapa point yang perlu diperhatikan, yaitu:

Pastikan cdc_dmic_gpios sesuai dengan microphone yang terpasang pada board.
 Perhatikan baris berikut:

```
cdc_dmic01_gpios: cdc_dmic01_pinctrl {
    compatible = "qcom,msm-cdc-pinctrl";
    pinctrl-names = "aud_active", "aud_sleep";
    pinctrl-0 = <&cdc_dmic01_clk_active &cdc_dmic01_data_active>;
    pinctrl-1 = <&cdc_dmic01_clk_sleep &cdc_dmic01_data_sleep>;
    qcom,lpi-gpios;
};

cdc_dmic23_gpios: cdc_dmic23_pinctrl {
    compatible = "qcom,msm-cdc-pinctrl";
    pinctrl-names = "aud_active", "aud_sleep";
    pinctrl-0 = <&cdc_dmic23_clk_active &cdc_dmic23_data_active>;
    pinctrl-1 = <&cdc_dmic23_clk_sleep &cdc_dmic23_data_sleep>;
    qcom,lpi-gpios;
};
```

Pada board DVT1, microphone terpasang pada cdc_dmic_gpio 0, 1, 2, dan 3.

- Pastikan wsa-max-devs sesuai dengan jumlah amplifier (wsa) yang terpasang.

Perhatikan baris berikut:

```
qcom, wsa-max-devs = <1>;
```

Pada board DVT1, jumlah amplifier (wsa) terpasang adalah 1.

- Pastikan audio-routing sesuai dengan audio yang terpasang pada board. Perhatikan baris berikut:

```
qcom,audio-routing =
    "VA DMICO", "VA MIC BIAS1",
    "VA MIC BIAS1", "Digital MicO",
```

Sesuaikan VA DMIC dan VA MIC BIAS sesuai dengan konfigurasi microphone.