Modifikasi Device Tree QCS404

1. Seluruh file device tree QCS404 yang dapat dimodifikasi ada di path berikut:

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| $<work\_dir>/LE.UM.4.1.2/apps\_proc/kernel/msm-4.14/arch/arm64/boot/dts/qcom/ |

$<work\_dir> adalah path relative untuk directory source code Qualcomm.

1. Untuk memodifikasi device tree terkait fungsi LED, edit file:

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| --- |
| qcs405-circular-pca9956.dtsi |

Berikut ini adalah file hasil modifikasi untuk board DVT1:

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| --- |
| /\*  \* Copyright (c) 2018, The Linux Foundation. All rights reserved.  \*  \* This program is free software; you can redistribute it and/or modify  \* it under the terms of the GNU General Public License version 2 and  \* only version 2 as published by the Free Software Foundation.  \*  \* This program is distributed in the hope that it will be useful,  \* but WITHOUT ANY WARRANTY; without even the implied warranty of  \* MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the  \* GNU General Public License for more details.  \*/  /\* Customized for DVT-1 Bahasa Kita \*/  &i2c\_2 {  status = "ok";  qcom,clk-freq-out = <100000>;  /\* PCA9956B LED Drivers \*/  nxp-ledseg-i2c@69 {  #address-cells = <1>;  #size-cells = <0>;  compatible = "nxp,pca9956b";  reg = <0x69>;  pca9956b,support\_initialize = <1>;  pca9956b,mode1 = <0x09>;  pca9956b,mode2 = <0x05>;  pca9956b,ledout0 = <0xAA>;  pca9956b,ledout1 = <0xAA>;  pca9956b,ledout2 = <0xAA>;  pca9956b,ledout3 = <0xFF>;  pca9956b,ledout4 = <0xFF>;  pca9956b,ledout5 = <0xFF>;  pca9956b,defaultiref = <0x2f>;  out0@0 {  label = "ledsec3\_b";  reg = <0x0>;  };  out1@1 {  label = "ledsec3\_g";  reg = <0x1>;  };  out2@2 {  label = "ledsec3\_r";  reg = <0x2>;  };  out3@3 {  label = "ledsec5\_b";  reg = <0x3>;  };  out4@4 {  label = "ledsec5\_g";  reg = <0x4>;  };  out5@5 {  label = "ledsec5\_r";  reg = <0x5>;  };  out6@6 {  label = "ledsec7\_b";  reg = <0x6>;  };  out7@7 {  label = "ledsec7\_g";  reg = <0x7>;  };  out8@8 {  label = "ledsec7\_r";  reg = <0x8>;  };  out9@9 {  label = "ledsec9\_b";  reg = <0x9>;  };  out10@10 {  label = "ledsec9\_g";  reg = <0xA>;  };  out11@11 {  label = "ledsec9\_r";  reg = <0xB>;  };  out12@12 {  label = "ledsec11\_b";  reg = <0xC>;  };  out13@13 {  label = "ledsec11\_g";  reg = <0xD>;  };  out14@14 {  label = "ledsec11\_r";  reg = <0xE>;  };  out15@15 {  label = "ledsec13\_b";  reg = <0xF>;  };  out16@16 {  label = "ledsec13\_g";  reg = <0x10>;  };  out17@17 {  label = "ledsec13\_r";  reg = <0x11>;  };  out18@18 {  label = "ledsec16\_b";  reg = <0x12>;  };  out19@19 {  label = "ledsec16\_g";  reg = <0x13>;  };  out20@20 {  label = "ledsec16\_r";  reg = <0x14>;  };  out21@21 {  label = "ledsec1\_b";  reg = <0x15>;  };  out22@22 {  label = "ledsec1\_g";  reg = <0x16>;  };  out23@23 {  label = "ledsec1\_r";  reg = <0x17>;  };  };  }; |

Ada beberapa point yang perlu diperhatikan, yaitu:

* Pastikan nxp-ledseg-i2c sesuai dengan address I2C untuk LED Driver PCA9956B dari NXP. Perhatikan baris berikut:

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| nxp-ledseg-i2c@69 |

Nilai 69 pada baris tersebut adalah konfigurasi address I2C yang digunakan pada board DVT1. Apabila dilakukan perubahan konfigurasi address I2C, maka ubah nilai tersebut sesuai dengan nilai yang tertera pada datasheet LED Driver PCA9956B dari NXP.

* Pastikan label masing-masing LED sudah sesuai untuk mengatur pattern ketika LED menyala. Perhatikan baris berikut:

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| --- |
| out0@0 {  label = "ledsec3\_b";  reg = <0x0>;  };  out1@1 {  label = "ledsec3\_g";  reg = <0x1>;  };  out2@2 {  label = "ledsec3\_r";  reg = <0x2>;  }; |

Label ini memiliki format:

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| "ledsec<number>\_<color>" |

Perhatikan nilai number dan color. Pastikan bahwa satu number harus memiliki color b (blue), g (green), dan r (red).

1. Untuk memodifikasi device tree terkait fungsi audio seperti microphone dan speaker, edit file:

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| qcs405-wsa-audio-overlay.dtsi |

Berikut ini adalah file hasil modifikasi untuk board DVT1:

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| --- |
| /\*  \* Copyright (c) 2018-2019, The Linux Foundation. All rights reserved.  \*  \* This program is free software; you can redistribute it and/or modify  \* it under the terms of the GNU General Public License version 2 and  \* only version 2 as published by the Free Software Foundation.  \*  \* This program is distributed in the hope that it will be useful,  \* but WITHOUT ANY WARRANTY; without even the implied warranty of  \* MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the  \* GNU General Public License for more details.  \*/  /\* Customized for DVT-1 Bahasa Kita \*/  &q6core {  wsa\_swr\_gpios: wsa\_swr\_clk\_data\_pinctrl {  compatible = "qcom,msm-cdc-pinctrl";  pinctrl-names = "aud\_active", "aud\_sleep";  pinctrl-0 = <&wsa\_swr\_clk\_active &wsa\_swr\_data\_active>;  pinctrl-1 = <&wsa\_swr\_clk\_sleep &wsa\_swr\_data\_sleep>;  qcom,lpi-gpios;  };  wsa\_spkr\_en\_1\_2: wsa\_spkr\_en\_1\_2\_pinctrl {  compatible = "qcom,msm-cdc-pinctrl";  pinctrl-names = "aud\_active", "aud\_sleep";  pinctrl-0 = <&wsa\_en\_1\_2\_active>;  pinctrl-1 = <&wsa\_en\_1\_2\_sleep>;  };  cdc\_dmic01\_gpios: cdc\_dmic01\_pinctrl {  compatible = "qcom,msm-cdc-pinctrl";  pinctrl-names = "aud\_active", "aud\_sleep";  pinctrl-0 = <&cdc\_dmic01\_clk\_active &cdc\_dmic01\_data\_active>;  pinctrl-1 = <&cdc\_dmic01\_clk\_sleep &cdc\_dmic01\_data\_sleep>;  qcom,lpi-gpios;  };  cdc\_dmic23\_gpios: cdc\_dmic23\_pinctrl {  compatible = "qcom,msm-cdc-pinctrl";  pinctrl-names = "aud\_active", "aud\_sleep";  pinctrl-0 = <&cdc\_dmic23\_clk\_active &cdc\_dmic23\_data\_active>;  pinctrl-1 = <&cdc\_dmic23\_clk\_sleep &cdc\_dmic23\_data\_sleep>;  qcom,lpi-gpios;  };  };  &q6core {  bolero: bolero-cdc {  compatible = "qcom,bolero-codec";  clock-names = "lpass\_core\_hw\_vote";  clocks = <&lpass\_core\_hw\_vote 0>;  qcom,num-macros = <2>;  };  };  &qcs405\_snd {  qcom,model = "qcs405-wsa-snd-card";  qcom,va-bolero-codec = <1>;  qcom,wsa-bolero-codec = <1>;  asoc-codec = <&stub\_codec>, <&bolero>;  asoc-codec-names = "msm-stub-codec.1", "bolero\_codec";  qcom,wsa-max-devs = <1>;  qcom,wsa-devs = <&wsa881x\_0211>, <&wsa881x\_0212>,  <&wsa881x\_0213>, <&wsa881x\_0214>;  qcom,wsa-aux-dev-prefix = "SpkrLeft", "SpkrRight",  "SpkrLeft", "SpkrRight";  qcom,cdc-dmic01-gpios = <&cdc\_dmic01\_gpios>;  qcom,cdc-dmic23-gpios = <&cdc\_dmic23\_gpios>;  qcom,audio-routing =  "VA DMIC0", "VA MIC BIAS1",  "VA MIC BIAS1", "Digital Mic0",  "VA DMIC1", "VA MIC BIAS1",  "VA MIC BIAS1", "Digital Mic1",  "VA DMIC2", "VA MIC BIAS1",  "VA MIC BIAS1", "Digital Mic2",  "VA DMIC3", "VA MIC BIAS1",  "VA MIC BIAS1", "Digital Mic3",  "SpkrLeft IN", "WSA\_SPK1 OUT",  "SpkrRight IN", "WSA\_SPK2 OUT",  "WSA\_SPK1 OUT", "VA\_MCLK",  "WSA\_SPK2 OUT", "VA\_MCLK";  };  #include "qcs405-va-bolero.dtsi"  #include "qcs405-wsa-bolero.dtsi"  #include "qcs405-wsa881x.dtsi" |

Ada beberapa point yang perlu diperhatikan, yaitu:

* Pastikan cdc\_dmic\_gpios sesuai dengan microphone yang terpasang pada board. Perhatikan baris berikut:

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| cdc\_dmic01\_gpios: cdc\_dmic01\_pinctrl {  compatible = "qcom,msm-cdc-pinctrl";  pinctrl-names = "aud\_active", "aud\_sleep";  pinctrl-0 = <&cdc\_dmic01\_clk\_active &cdc\_dmic01\_data\_active>;  pinctrl-1 = <&cdc\_dmic01\_clk\_sleep &cdc\_dmic01\_data\_sleep>;  qcom,lpi-gpios;  };  cdc\_dmic23\_gpios: cdc\_dmic23\_pinctrl {  compatible = "qcom,msm-cdc-pinctrl";  pinctrl-names = "aud\_active", "aud\_sleep";  pinctrl-0 = <&cdc\_dmic23\_clk\_active &cdc\_dmic23\_data\_active>;  pinctrl-1 = <&cdc\_dmic23\_clk\_sleep &cdc\_dmic23\_data\_sleep>;  qcom,lpi-gpios;  }; |

Pada board DVT1, microphone terpasang pada cdc\_dmic\_gpio 0, 1, 2, dan 3.

* Pastikan wsa-max-devs sesuai dengan jumlah amplifier (wsa) yang terpasang. Perhatikan baris berikut:

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| qcom,wsa-max-devs = <1>; |

Pada board DVT1, jumlah amplifier (wsa) terpasang adalah 1.

* Pastikan audio-routing sesuai dengan audio yang terpasang pada board. Perhatikan baris berikut:

|  |
| --- |
| qcom,audio-routing =  "VA DMIC0", "VA MIC BIAS1",  "VA MIC BIAS1", "Digital Mic0", |

Sesuaikan VA DMIC dan VA MIC BIAS sesuai dengan konfigurasi microphone.