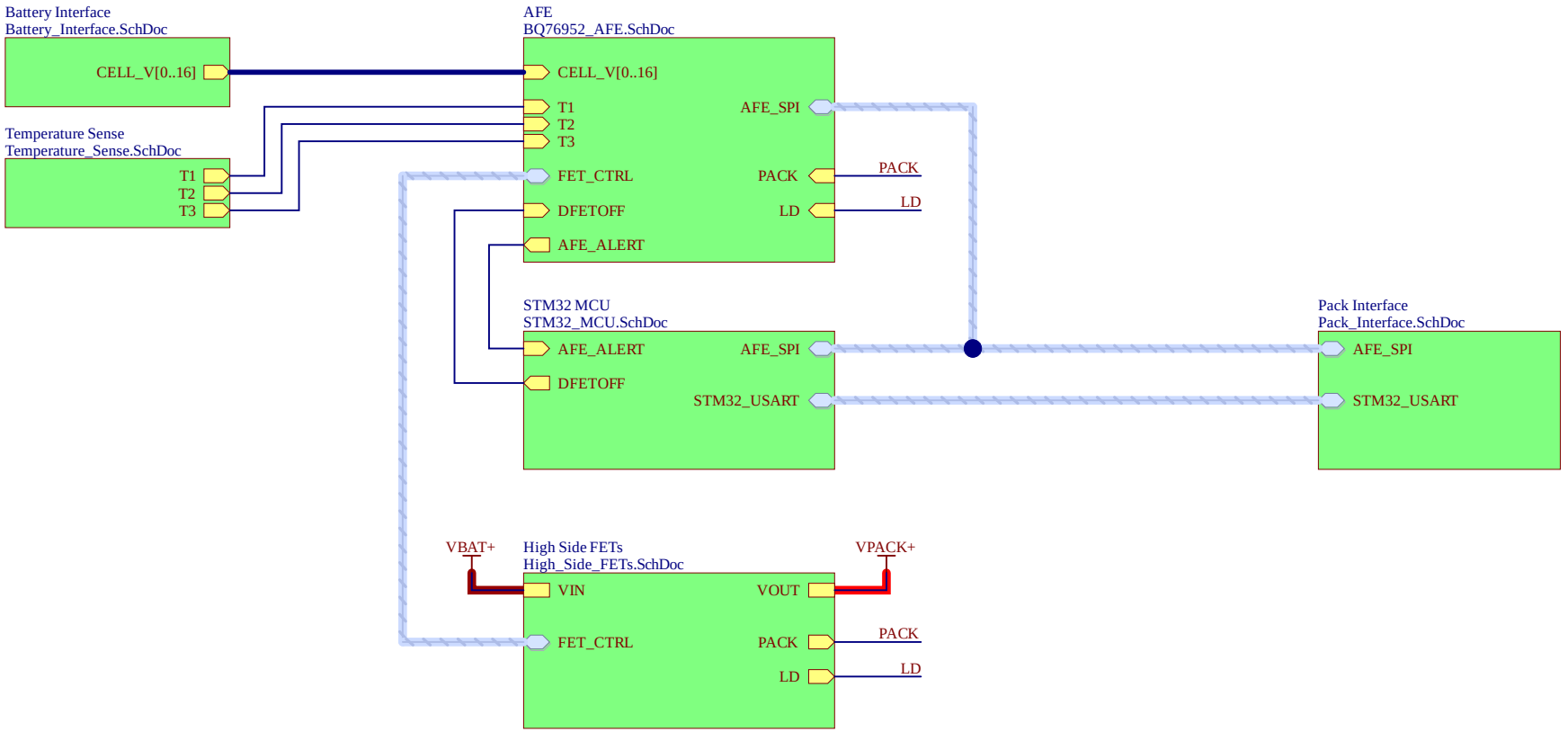

Mounting Holes

MH1 MH2

MH3 MH4

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DOCUMENT Top.SchDoc		MODIFIED 2023-08-03
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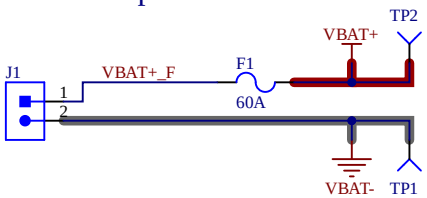
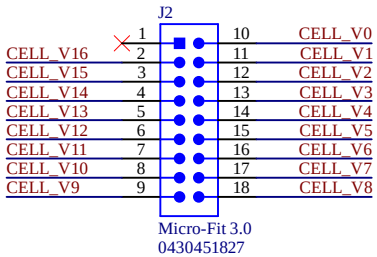
BATTERY INTERFACE

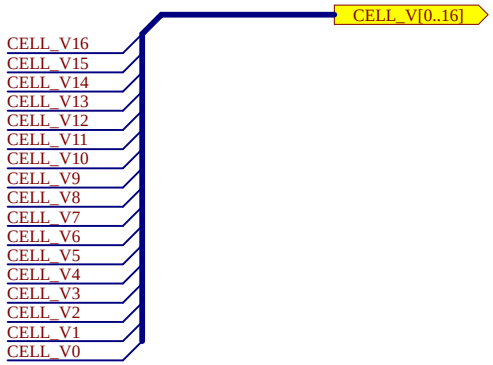
Battery specs:
- 16s1p 20Ah LTO battery
- 1.5V - 2.7V cell voltage range, 2.3V nominal

Power Input



Cell Sensing Inputs





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PROJECT LTO 16S BMS.PrjPcb, [No Variations]		
DOCUMENT Battery_Interface.SchDoc		MODIFIED 2023-07-06
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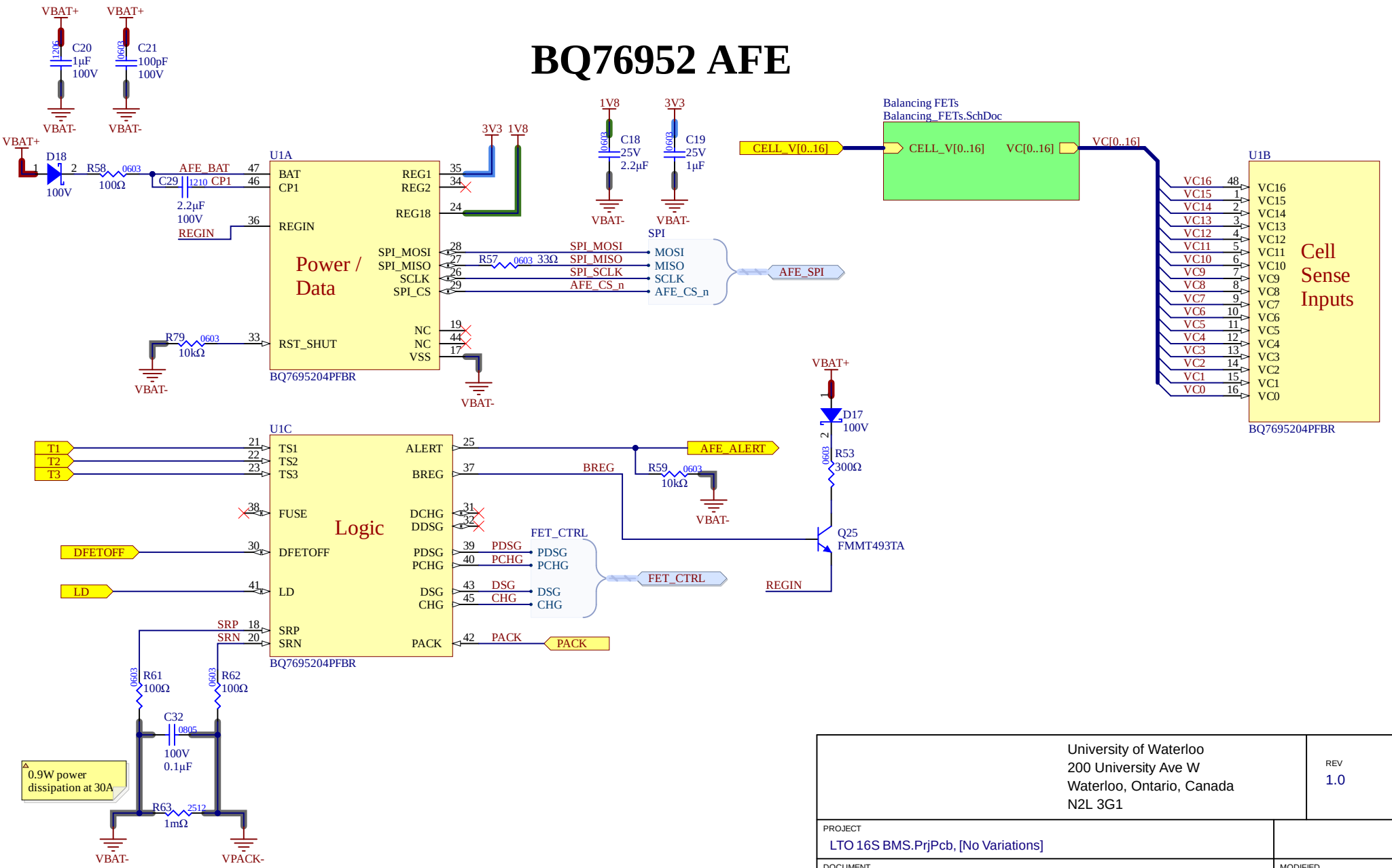
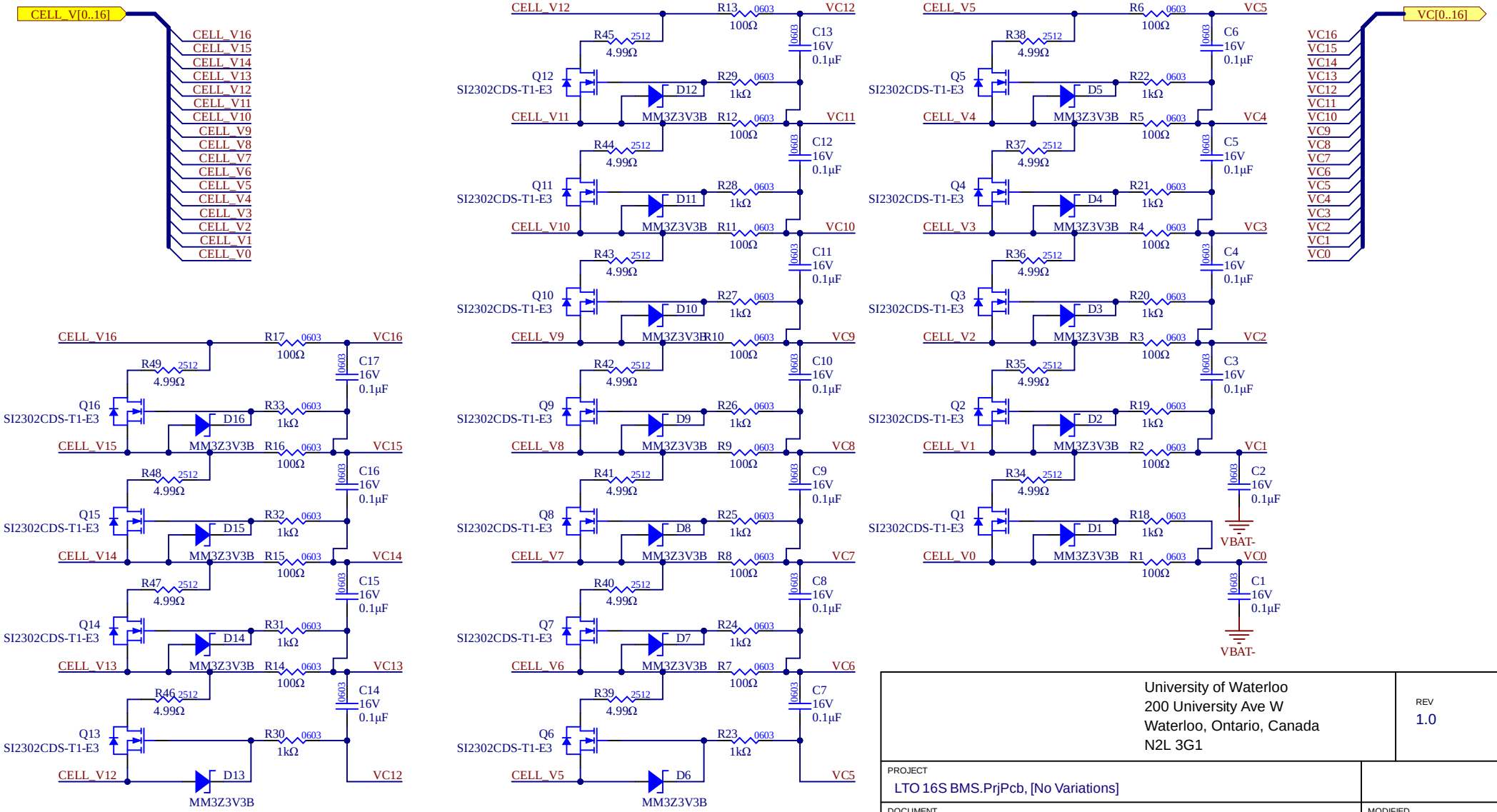
BQ76952 AFE



University of Waterloo 200 University Ave W Waterloo, Ontario, Canada N2L 3G1		REV 1.0
PROJECT LTO 16S BMS.PrjPcb, [No Variations]		
DOCUMENT BQ76952_AFE.SchDoc		MODIFIED 2023-08-03
ENGINEER Farris Matar	REVIEWER *	SHEET 3 OF 8

BALANCING FETS



University of Waterloo 200 University Ave W Waterloo, Ontario, Canada N2L 3G1		REV 1.0
PROJECT LTO 16S BMS.PrjPcb, [No Variations]		
DOCUMENT Balancing_FETs.SchDoc		MODIFIED 2023-08-03
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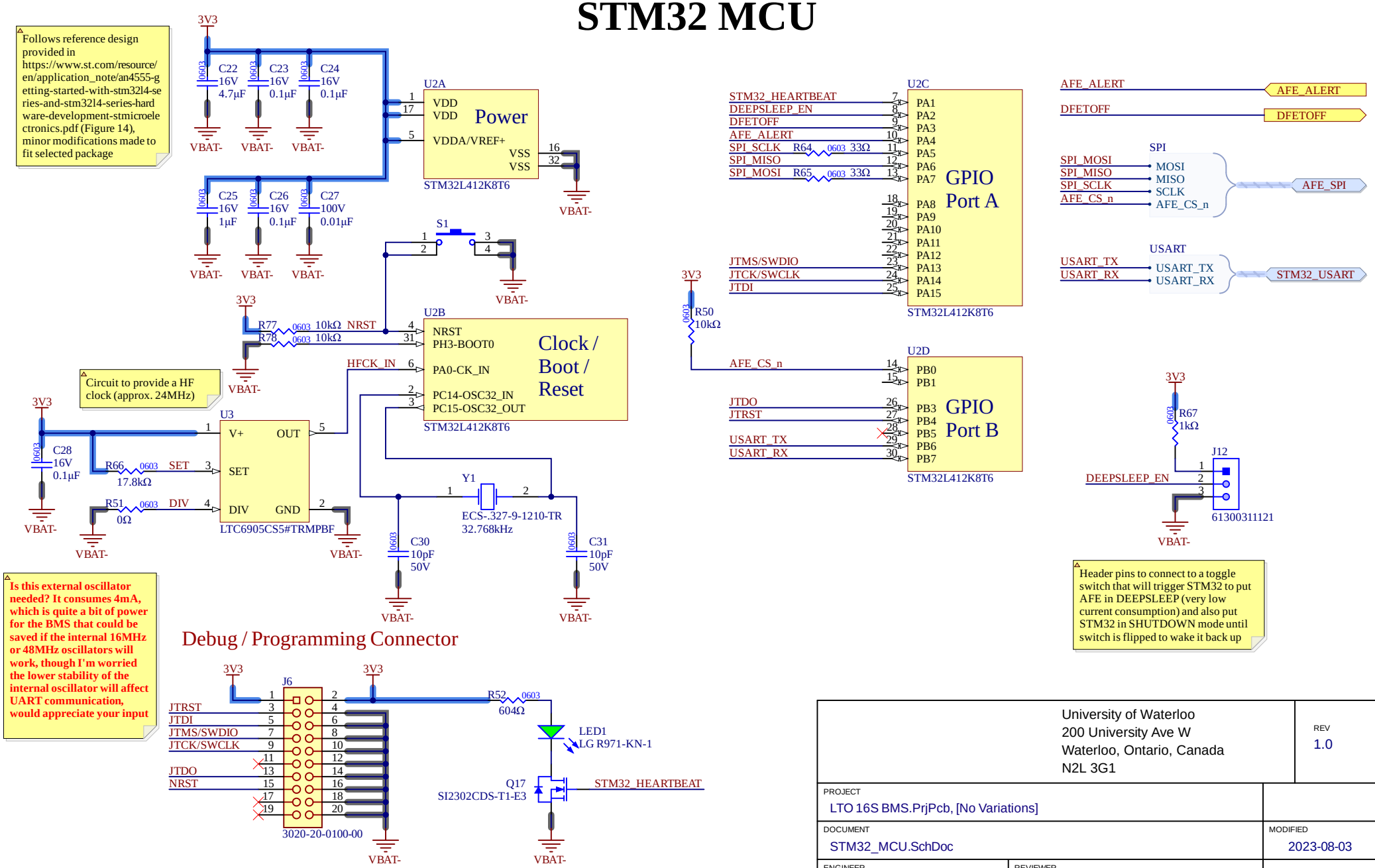
STM32 MCU



University of Waterloo 200 University Ave W Waterloo, Ontario, Canada N2L 3G1	REV 1.0
PROJECT: LTO 16S BMS.PrjPcb, [No Variations]	
DOCUMENT: STM32_MCU.SchDoc	MODIFIED: 2023-08-03
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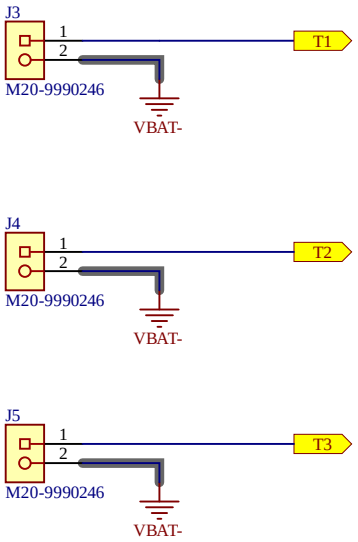
TEMPERATURE SENSING

103-AT thermistors to be connected here, will use 18k internal pull-up on AFE inputs



University of Waterloo 200 University Ave W Waterloo, Ontario, Canada N2L 3G1		REV 1.0
PROJECT LTO 16S BMS.PrjPcb, [No Variations]		
DOCUMENT Temperature_Sense.SchDoc		MODIFIED 2023-07-29
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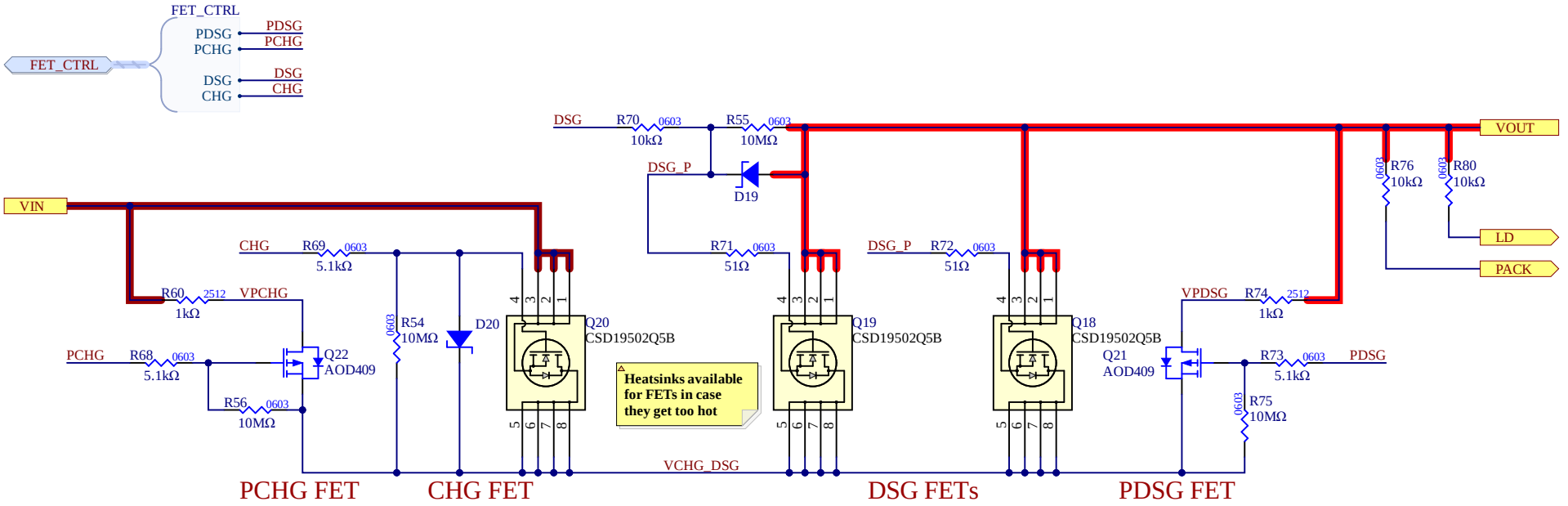
HIGH SIDE POWER FETS



University of Waterloo 200 University Ave W Waterloo, Ontario, Canada N2L 3G1	REV 1.0
PROJECT: LTO 16S BMS.PrjPcb, [No Variations]	
DOCUMENT: High_Side_FETs.SchDoc	MODIFIED: 2023-08-03
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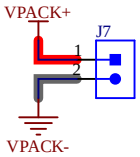
Power Output

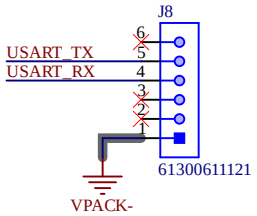


Data Output

UART will be used to interface with the main control board, sending battery info & state and receiving any commands from the main control board/user

SPI Debug

FTDI USB to UART Debug



Breakout headers for debugging/testing

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