

Operating Systems

Synchronization Tools-Part1

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Background

- Processes can execute concurrently
 - May be interrupted at any time, partially completing execution.

Concurrent access to shared data may result in data inconsistency.

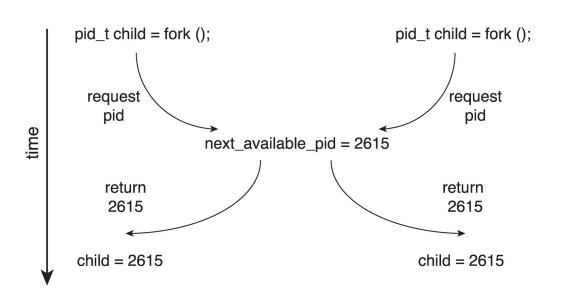
 Maintaining data consistency requires mechanisms to ensure the orderly execution of cooperating processes.

Background (cont.)

In chapter 4, when we considered the Bounded Buffer problem with use of a counter that is updated concurrently by the producer and consumer, which **lead to race condition**.

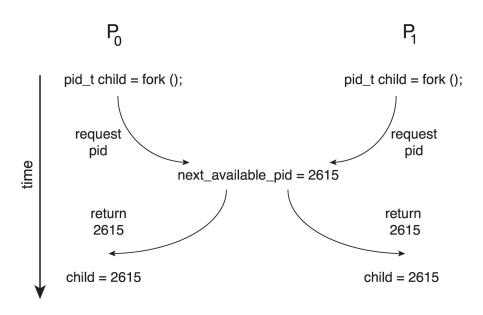
Race Condition

- Processes P_0 and P_1 are creating child processes using the **fork()** system call.
- Race condition on kernel variable next_available_pid which represents the next available process identifier (pid)
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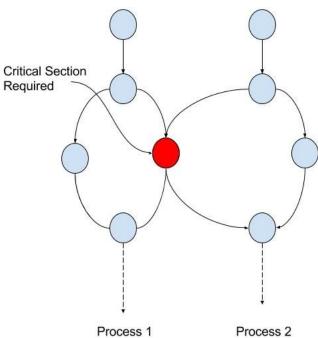
Race Condition (Cont.)



• Unless there is a mechanism to prevent P₀ and P₁ from accessing the variable next_available_pid the same pid could be assigned to two different processes!

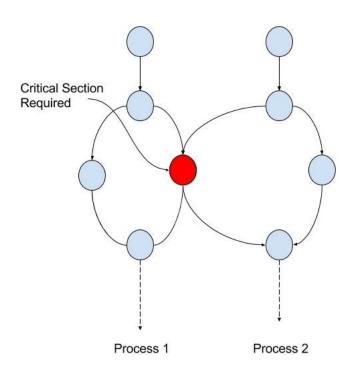
Critical Section Problem

- Consider system of n processes $\{p_0, p_1, ..., p_{n-1}\}$
- Each process has critical section segment of code
 - Process may be changing common variables, updating table, writing file, etc.



Critical Section Problem

- When one process in critical section, no other may be in its critical section.
- Critical section problem is to design protocol to solve this.





Critical Section

- Each process
 - must ask permission to enter critical section in entry section,
 - may follow critical section with exit section,
 - then remainder section.

• General structure of process P_i

```
do {
     entry section
     critical section

     exit section

remainder section
```

} while (true);

Requirements for solution to critical-section problem

1. Mutual Exclusion

2. Progress

3. Bounded Waiting



1- Mutual Exclusion

If process P_i is executing in its critical section, then no other processes can be executing in their critical sections.

No two processes simultaneously in critical region.



2- Progress

• If no process is executing in its critical section and there exist some processes that wish to enter their critical section, then the selection of the process that will enter the critical section next cannot be postponed indefinitely.

 No process running outside its critical region may block another process.

3- Bounded Waiting

- A bound must exist on the number of times that other processes are allowed to enter their critical sections after a process has made a request to enter its critical section and before that request is granted.
 - Assume that each process executes at a nonzero speed
 - No assumption concerning relative speed of the n processes

No process must wait forever to enter its critical region.

Interrupt-based Solution

- Entry section: disable interrupts
- Exit section: enable interrupts

- Will this solve the problem?
 - What if the critical section is code that runs for an hour?
 - Can some processes starve -- never enter their critical section.
 - What if there are two CPUs?

Software Solution 1

- Two process solution.
- Assume that the load and store machine-language instructions are atomic; that is, cannot be interrupted.

- The two processes share one variable:
 - int *turn*;
 - turn indicates whose turn it is to enter the critical section.

Algorithm for Process P_i

```
while (true) {
    while (turn = = j);
     /* critical section */
     turn =
     /* remainder section */
```



Algorithm for P₀ and P₁

```
Initially turn = 0
```



Correctness of the Software Solution

- Mutual exclusion is preserved
 - P_i enters critical section only if:

turn = i

- turn cannot be both 0 and 1 at the same time
- It wastes CPU time
 - So we should avoid busy waiting as much as we can.
- Can be used only when the waiting period is expected to be short.

Correctness of the Software Solution (cont.)

- However there is a problem in the above approach!
 - What about the Progress requirement?
 - What about the Bounded-waiting requirement?



Correctness of the Software Solution (cont.)

- P₀ leaves its critical region and sets turn to 1, enters its non-critical region.
- P₁ enters its critical region, sets turn to 0 and leaves its critical region.
- P₁ enters its non-critical region, quickly finishes its job and goes back to the while loop.
- Since turn is 0, process 1 has to wait for process
 0 to finish its non-critical region so that it can enter its critical region.
- This violates the second condition (progress) of providing mutual exclusion.

```
Initially turn = 0
  while (TRUE) {
      while (turn != 0)
      critical_region();
      turn = 1;
      noncritical_region();
while (TRUE) {
    while (turn != 1)
    critical_region();
    turn = 0:
    noncritical_region();
```

How About this solution?

```
//Algorithm for P;
while (true) {
     turn = i;
     while (turn = = j);
     /* critical section */
     turn = j;
     /* remainder section */
```



Peterson's Solution

 The previous solution solves the problem of one process blocking another process while its outside its critical section.

 Peterson's Solution is a neat solution with busy waiting, that defines the procedures for entering and leaving the critical region.



Peterson's Solution (cont.)

- Two process solution
- Assume that the **load** and **store** machine-language instructions are **atomic**; that is, cannot be interrupted.
- The two processes share two variables:
 - int turn;
 - boolean flag[2]
- The variable turn indicates whose turn it is to enter the critical section.
- The flag array is used to indicate if a process is ready to enter the critical section.
 - flag[i] = true implies that process P_i is ready!



Algorithm for Process P_i

```
while (true) {
     flag[i] = true;
     turn = j;
     while (flag[j] \&\& turn = = j);
        /* critical section */
     flag[i] = false;
     /* remainder section */
```



Correctness of Peterson's Solution

- Provable that the three CS requirement are met:
 - 1. Mutual exclusion is preserved

P_i enters CS only if:

either flag[j] = false or turn = I

2. Progress requirement is satisfied

3. Bounded-waiting requirement is met