CSCE-312 | Summer 2019
Project 1
Building Boolean Logic Gates

Due Date: Submit on eCampus by **Wednesday July10th, 11:59PM**

Grading

(A) Project Submission [100%]: Logistics to be provided soon

You will be graded for correctness of the chips (hdl) you have designed and coded. You will be submitting all your HDL codes on **eCampus**. The HDL codes will be tested on the NandtoTetris software by the TA. The same simulator you have to use to check your chips in the project. So, make sure to test and verify your codes before finally submitting on eCampus.

Rubric: Each *HDL* is worth **between 5-10 points each**. Each circuit needs to pass all its test cases to get the points, else you will receive **0** on that circuit.

Deliverables & Submission

You need to turn in only the HDL files for all the chips implemented. Put your full name in the introductory comment present in each HDL code. Use relevant code comments and indentation in your code. Also, include this cover sheet with your signature below. Zip all the required HDL files and the signed cover sheet into a compressed file *FirstName-LastName-UIN.zip*. Submit this zip file on eCampus.

Late Submission Policy: Refer to the Syllabus

First Name: Jorge	Last Name: Farinacci	UIN: 125008668

Any assignment turned in without a fully completed cover page will NOT BE GRADED.

Please list all below all sources (people, books, web pages, etc) consulted regarding this assignment:

CSCE 312 Students	Other People	Printed Material	Web Material (URL)	Other
1.	1.	1.	1.	1.
2.	2.	2.	2.	2.
3.	3.	3.	3.	3.
4.	4.	4.	4.	4.
5.	5.	5.	5.	5.

Please consult the Aggie Honor System Office for additional information regarding academic misconduct – it is your responsibility to understand what constitutes academic misconduct and to ensure that you do not commit it.

I certify that I have listed above all the sources that I consulted regarding this assignment, and that I have not received nor given any assistance that is contrary to the letter or the spirit of the collaboration guidelines for this assignment.

Submission Date: _____07/10/2019_____

Printed Name (in lieu of a signature): _____Jorge Farinacci_____