

CSCE-312 | Summer 2019

GRADING & COVER SHEET

Project 2: Combinational Chip

Grading

- (A) Project Submission [100%]: **You have to submit your Project 2 files by (Jul 14, 11:59 PM) to be considered for the maximum possible points.** You will be graded on the correctness of your chips (hdl) you have designed and coded by running **test scripts** on all your HDL codes using Nand2tetris software (Hardware Simulator) by the TA. The same simulator you would have used to check your chips in the project. So, make sure to test and verify your codes before finally submitting on eCampus.

First Name:

Last Name:

UIN:

Any assignment turned in without a fully completed cover page will NOT BE GRADED.

Please list all below all sources (people, books, web pages, etc) consulted regarding this assignment:

CSCE 312 Students	Other People	Printed Material	Web Material (URL)	Other
1.	1.	1.	1.	1.
2.	2.	2.	2.	2.
3.	3.	3.	3.	3.
4.	4.	4.	4.	4.
5.	5.	5.	5.	5.

Please consult the Aggie Honor System Office for additional information regarding academic misconduct – it is your responsibility to understand what constitutes academic misconduct and to ensure that you do not commit it.

I certify that I have listed above all the sources that I consulted regarding this assignment, and that I have not received nor given any assistance that is contrary to the letter or the spirit of the collaboration guidelines for this assignment.

Submission Date: 7/14/2019

Printed Name (in lieu of a signature): Jorge Farinacci

NAME:

SECTION:

- The project is divided into three stages. Every stage is equally important for the completion of the final goal i.e. the **HACK ALU**.
- Each of these chips needs to pass all the tests in order to get the full points, else you will receive 0 for that chip.

Category	Chip	Working ?
A	HalfAdder	____ / 10
	FullAdder	____ / 10
B	Add16	____ / 15
	Inc16	____ / 15
C	ALU (no stat)	____ / 25
	ALU (complete)	____ / 25
	Total	____ / 100

