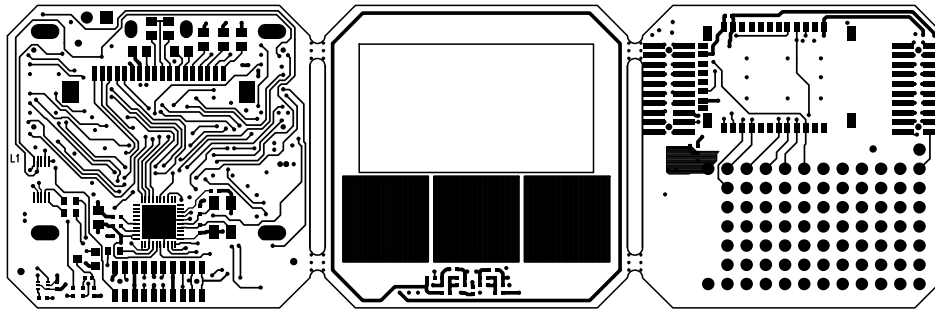
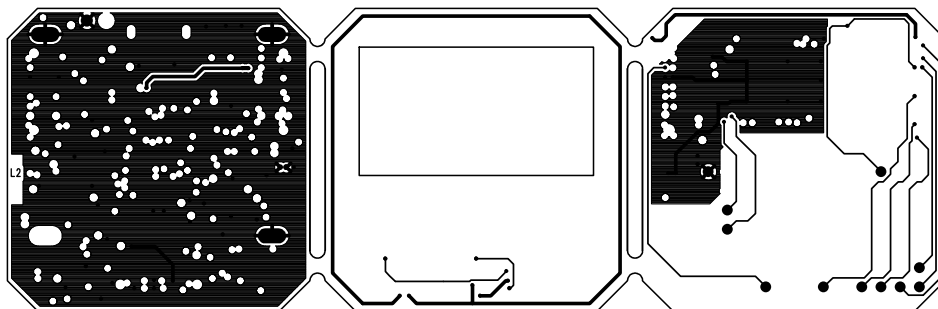


TOP



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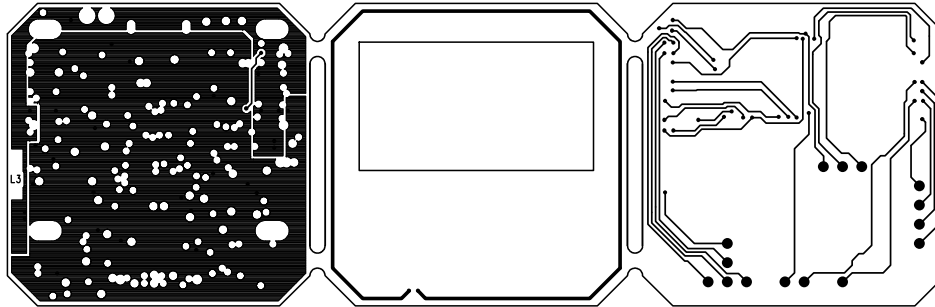
INNER LAYER 2



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CAMtasticDXP (TM): layer2.spl

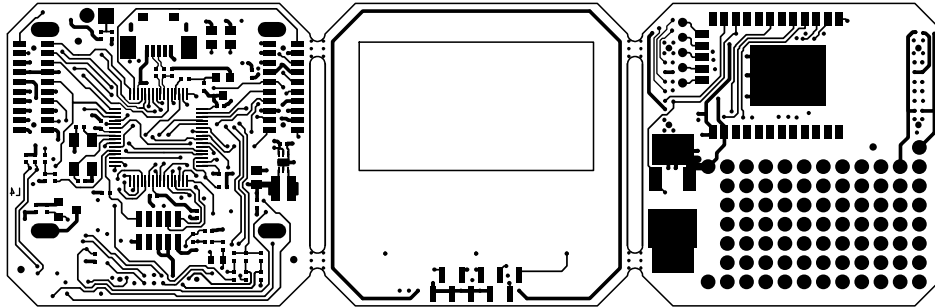
INNER LAYER 3



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CAMtasticDXP (TM): layer3.spl

BOTTOM

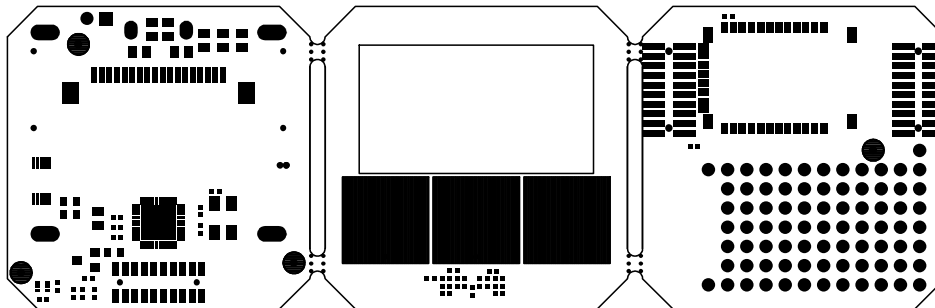


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CAMtasticDXP (TM): bottom.spl

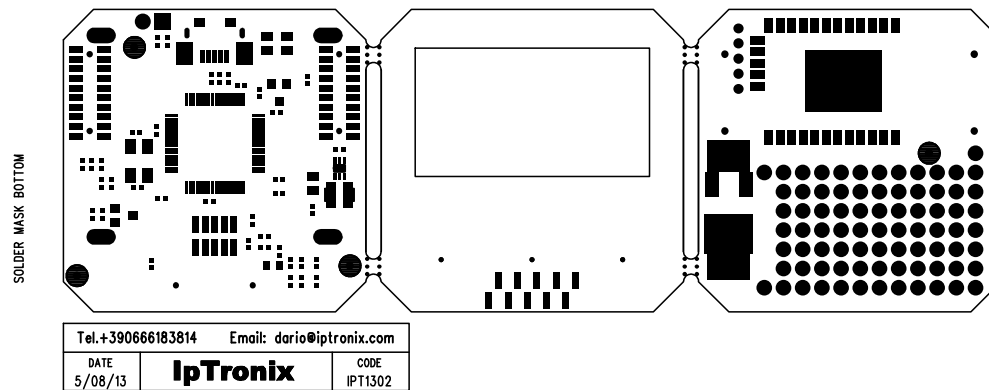
CAMtasticDXP (TM): silkscreen_top.spl

SOLDER MASK TOP

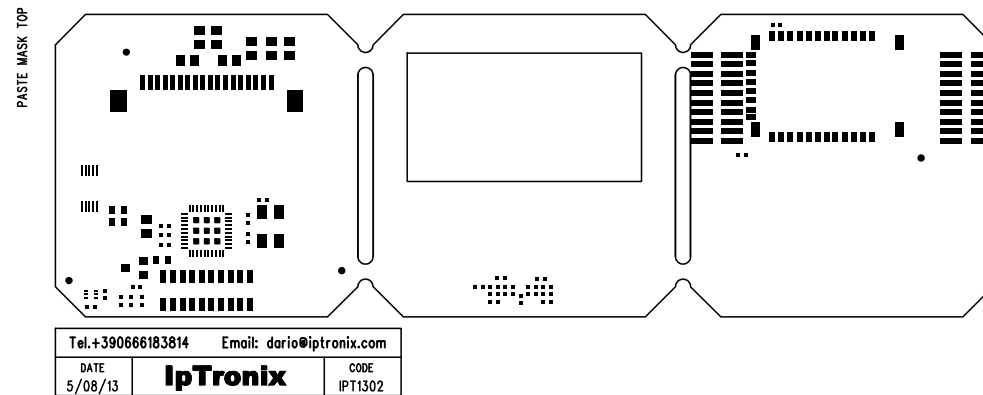


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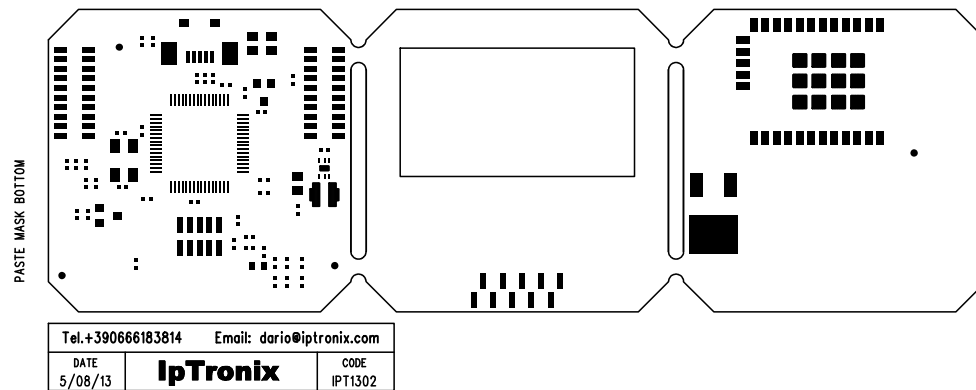
CAMtasticDXP (TM): solder_mask_top.spl



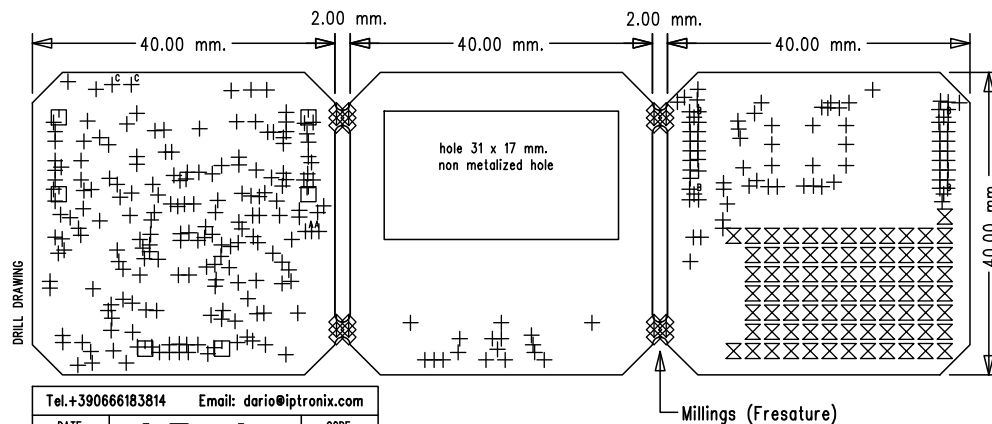
CAMtasticDXP (TM): solder_mask_bottom.spl



CAMtasticDXP (TM): past_mask_top.spl



CAMtasticDXP (TM): past_mask_bottom.spl



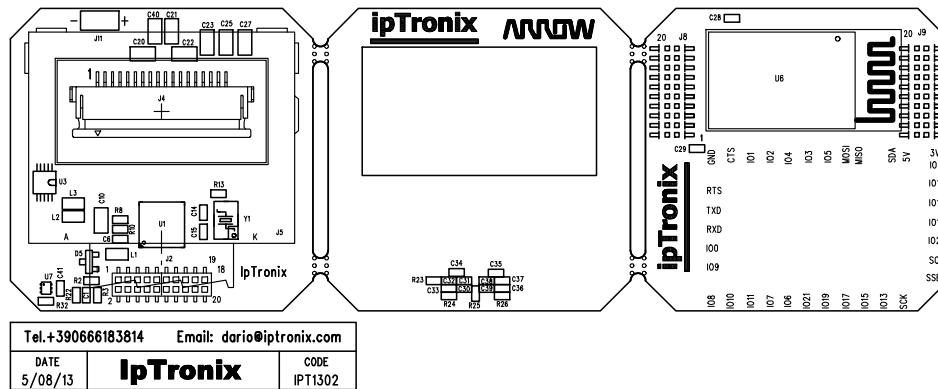
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4 layer PCB-Thickness1,6 mm-CU 35 um- ENIG Finishing

SIZE	QTY	SYM	PLATED	TOL
0.3	285	+	YES	+/-0.0
0.76	6	□	NO	+/-0.0
0.508	24	◇	NO	+/-0.0
0.9	80	⊗	YES	+/-0.0
1 x 2.8	4	⊗	YES	+/-0.0
0.4064	2	⊕ ^A	YES	+/-0.0
0.9906	4	⊕ ^B	NO	+/-0.0
0.9906	2	⊕ ^C	YES	+/-0.0
0.7 x 1.4	2	⊕ ^D	NO	+/-0.0

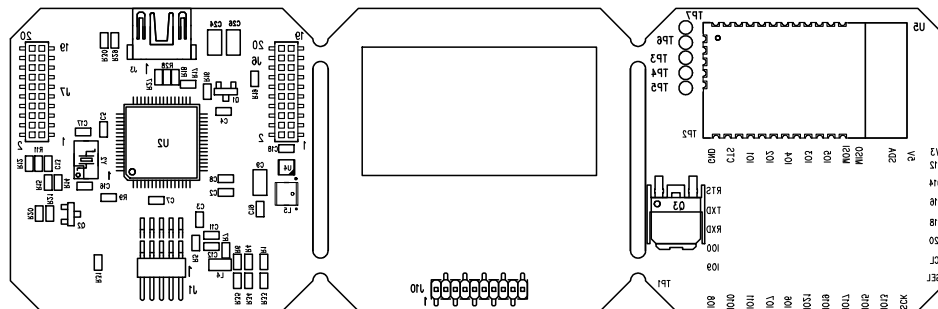
CAMtasticDXP (TM): drill_drawing.spl

ASSEMBLY DRAWING TOP



CAMtasticDXP (TM): assembly_top_layer.spl

ASSEMBLY DRAWING BOTTOM



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CAMtasticDXP (TM): assembly_bottom_layer.spl