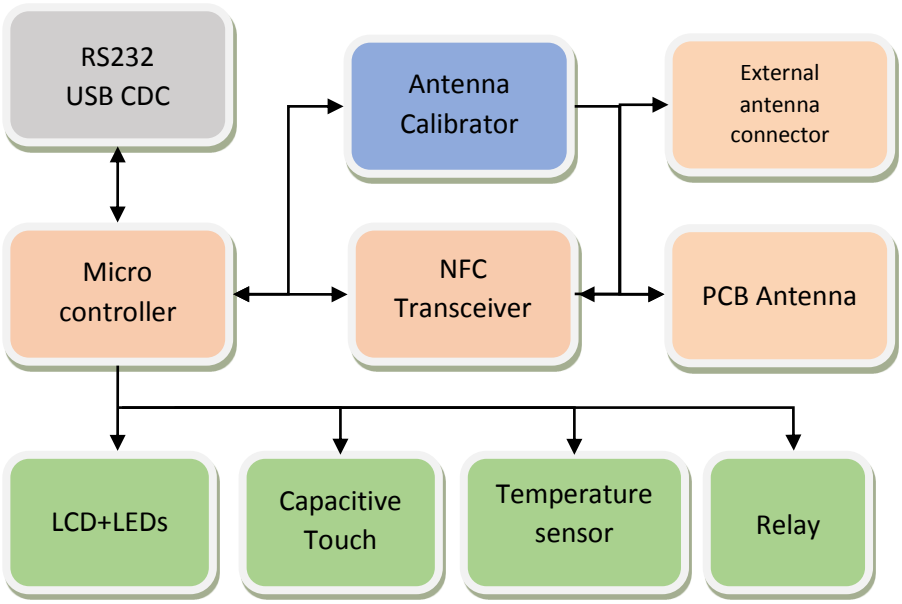
NFC Reference Design

Introduction

NFC Reference Design is a kit that includes hardware and software required to develop access control/identification, micropayment and contactless data exchange applications.

Block Diagram



Functional Description

Reference design includes with several ready to run applications that demonstrate NFC functionalities in a variety of use cases:

- Access Control
  board interacts with an Android NFC enabled smartphone or with a Mifare tag and enables relay when an enabled device is detected
- Micropayment
  Vending machine simulation which allows recharging and subtracting credit from a secure area of a Mifare tag

Antenna Calibration

In order to ease PCB antenna design on custom NFC products, in addition to kit documentation, the reference design also acts as a calibrator that allows verifying antenna frequency response and helps in selecting calibration components for customer boards.

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Availability

NFC Reference design will be available from Q3'13 exclusively from Arrow Electronics.
All components used in the reference design are available for purchase from Arrow Electronics.

Main Components

NXP CPU: LPC11U37
50 MHz Cortex-M0
128KB Flash
12KB RAM
4KB EEPROM
USB, USART, SSP, ADC

NXP NFC transceiver: PN532
ISO/IEC 14443A/MIFARE
FeliCa, ISO/IEC 18092
ECMA 340 Peer-to-Peer

CCT LCD Display: G64128x16
128x64 Graphic display

SiLabs VersaClock: Si5351

Contatti

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