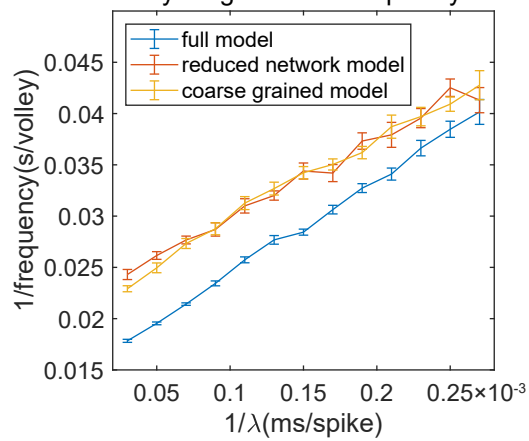
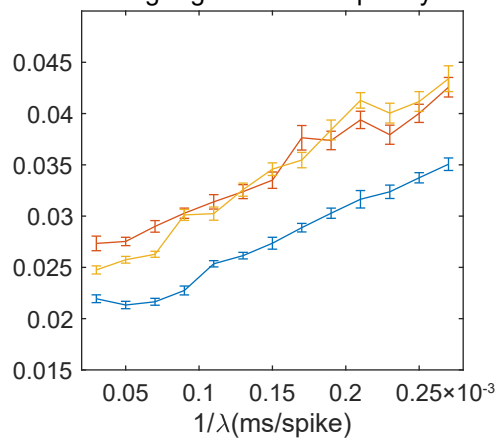


**A**

Syn regime MFE frequency



Reg regime MFE frequency

**B**

SSI

