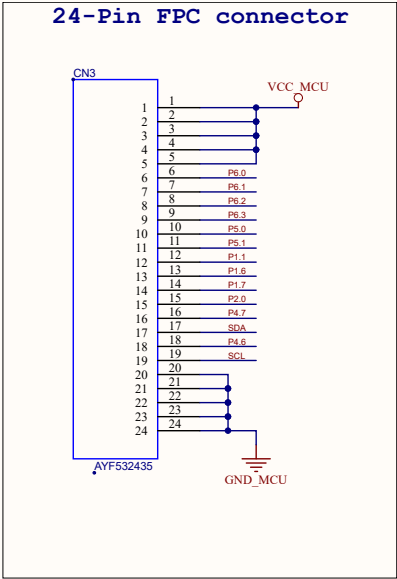


24-Pin FPC connector

Pinout diagram for the 24-Pin FPC connector (CN3) connected to the AYF532435 component.

Pin	Signal
1	VCC_MCU
2	PB.0
3	PB.1
4	PB.2
5	PB.3
6	PS.0
7	PS.1
8	P1.1
9	P1.6
10	P1.7
11	P2.0
12	P4.7
13	SDA
14	P4.6
15	SCL
16	GND_MCU
17	GND_MCU
18	GND_MCU
19	GND_MCU
20	GND_MCU
21	GND_MCU
22	GND_MCU
23	GND_MCU
24	GND_MCU



2\*8 pin female header pitch 2.54mm

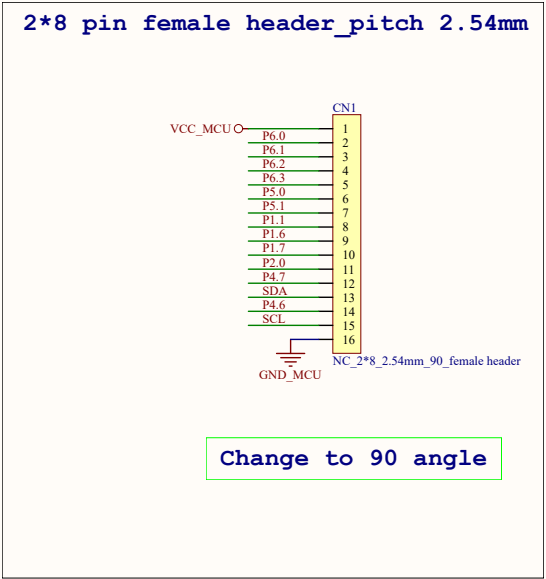
Pinout diagram for a 2\*8 pin female header (pitch 2.54mm) connected to a microcontroller (MCU).

The header pins are labeled 1 through 16. The connections are as follows:

- Pin 1: VCC\_MCU
- Pin 2: P6.0
- Pin 3: P6.1
- Pin 4: P6.2
- Pin 5: P6.3
- Pin 6: P5.0
- Pin 7: P5.1
- Pin 8: P1.1
- Pin 9: P1.6
- Pin 10: P1.7
- Pin 11: P2.0
- Pin 12: P4.7
- Pin 13: SDA
- Pin 14: SCL
- Pin 15: P4.6
- Pin 16: GND\_MCU

The header is labeled NC\_2\*8\_2.54mm\_90\_female header.

Change to 90 angle



2\*8 pin female header pitch 2.54mm

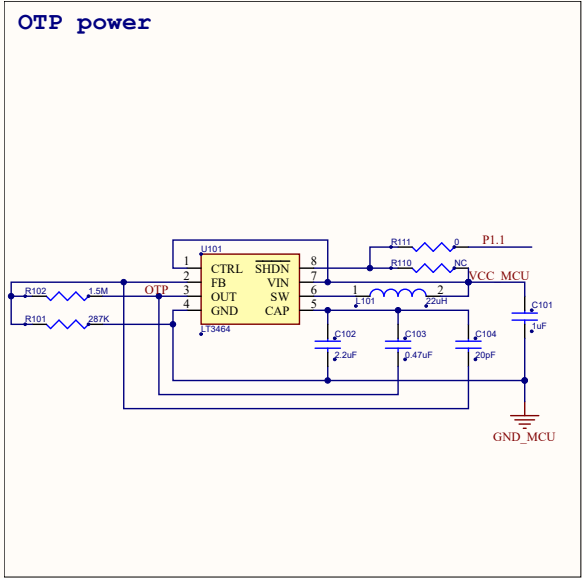
Pinout diagram for a 2\*8 pin female header (pitch 2.54mm) connected to a microcontroller (MCU).

The header pins are labeled 1 through 16. The connections are as follows:

- Pin 1: VCC\_MCU
- Pin 2: P6.0
- Pin 3: P6.1
- Pin 4: P6.2
- Pin 5: P6.3
- Pin 6: P5.0
- Pin 7: P5.1
- Pin 8: P1.1
- Pin 9: P1.6
- Pin 10: P1.7
- Pin 11: P2.0
- Pin 12: P4.7
- Pin 13: SDA
- Pin 14: SCL
- Pin 15: P4.6
- Pin 16: GND\_MCU

The header is labeled NC\_2\*8\_2.54mm\_90\_female header.

Change to 90 angle

[illegible]

panel terminal

### EPD Circuit

CN2

Pin	Signal	Component
1	NC	
2	GDR	P5.1
3	RESE	
4	VGL	
5	VGH	C7, C8, C9, C10, C11, C12
6	TSC	
7	TSCL	
8	TSDA	SDA
9	BS1	P6.0
10	BUSY	P6.2
11	RES#	P6.1
12	D/C#	P5.0
13	CS#	P6.3
14	SCL	P4.7
15	SDA	P4.6
16	NC	
17	VCL	
18	VSS	
19	VDD	VCC_MCU
20	NC	
21	VSH	
22	PREVGH	
23	VSL	
24	PREVGL	
25	VCOM	

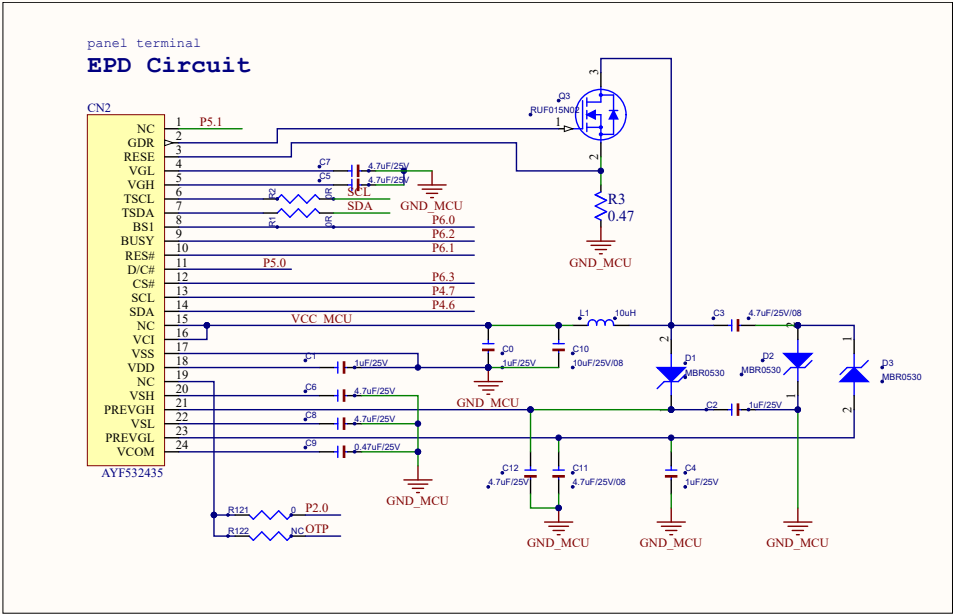
AYF532435

Components:


- Resistors: R121, R122, R3 (0.47)
- Capacitors: C0, C1, C2, C3, C4, C5, C6, C7, C8, C9, C10, C11, C12
- Inductor: L1 (10uH)
- Diodes: P1, P2, P3 (MBR0530)
- Transistor: Q3 (RUF015N)

Power and Ground Connections:

- 5V Supply: P5.1
- Ground: GND\_MCU
- VCC\_MCU

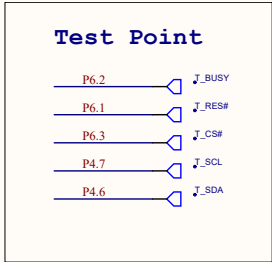


## Test Point



The diagram shows five horizontal lines representing test points, each with a label to its left and a signal name to its right. From top to bottom: P6.2 is connected to J\_BUSY; P6.1 is connected to J\_RES#; P6.3 is connected to J\_CS#; P4.7 is connected to J\_SCL; and P4.6 is connected to J\_SDA. Each connection is indicated by a horizontal line ending in a trapezoidal shape that fits into a corresponding trapezoidal shape on the signal label.

Test Point	Signal
P6.2	J_BUSY
P6.1	J_RES#
P6.3	J_CS#
P4.7	J_SCL
P4.6	J_SDA



# Screw hole

