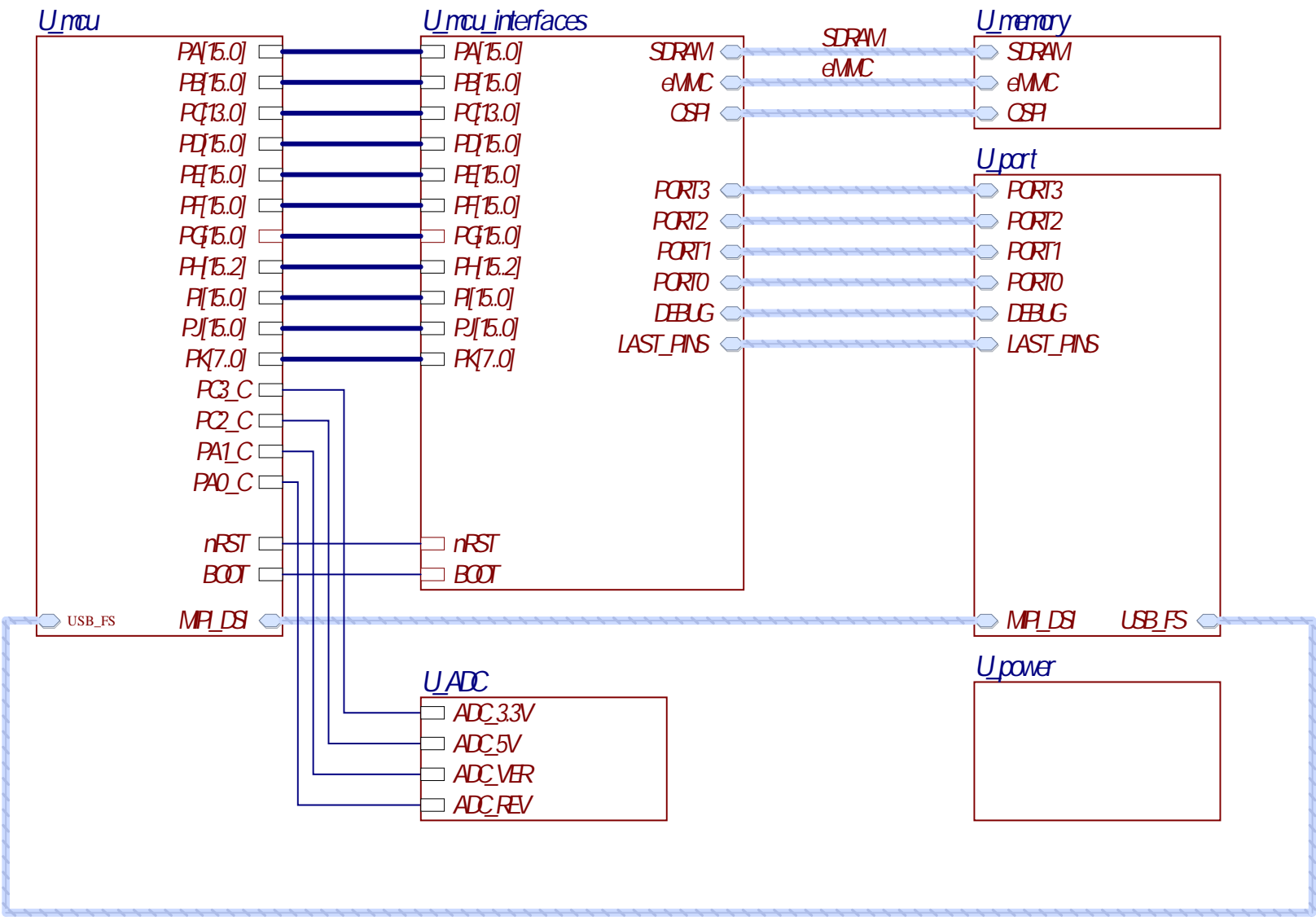


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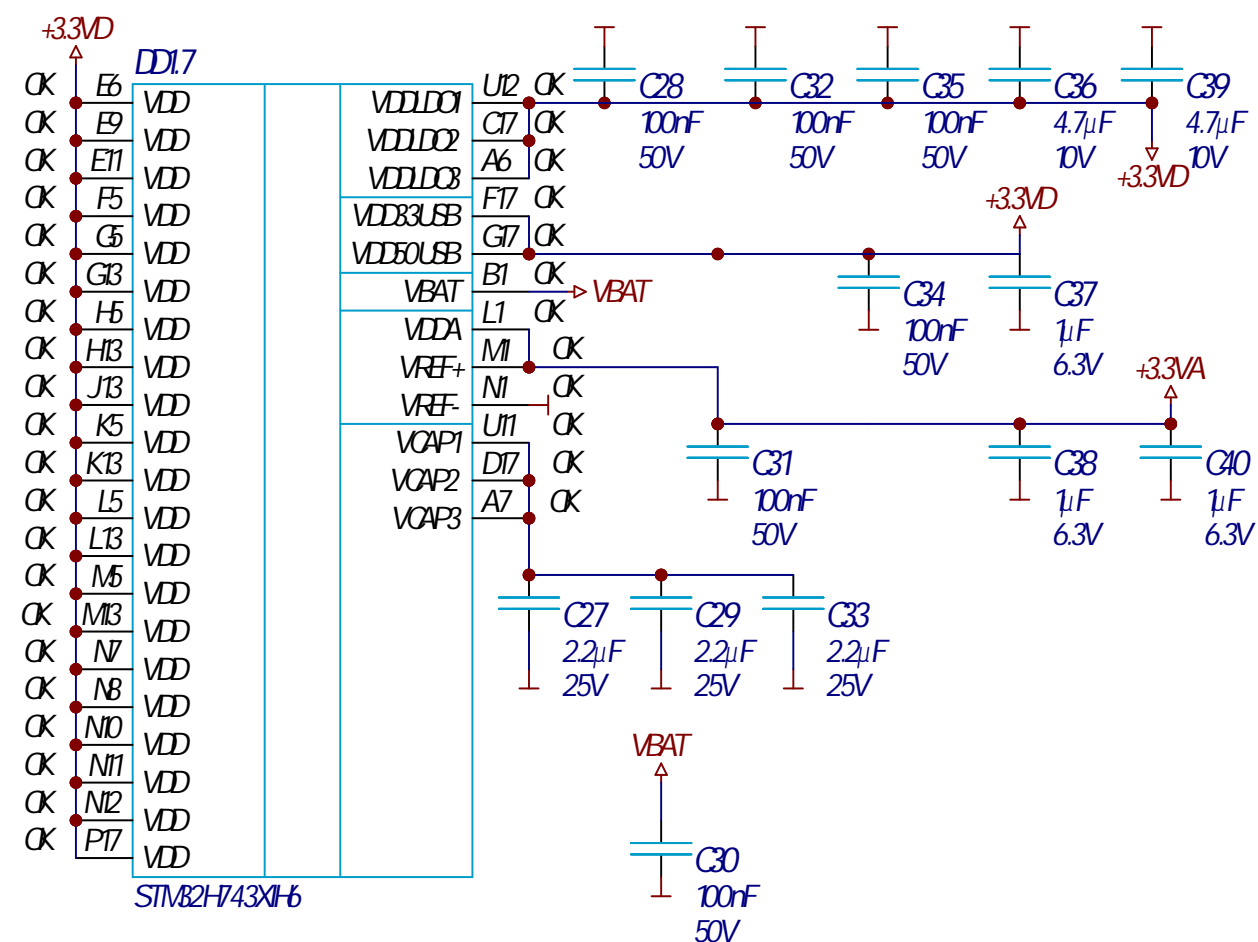
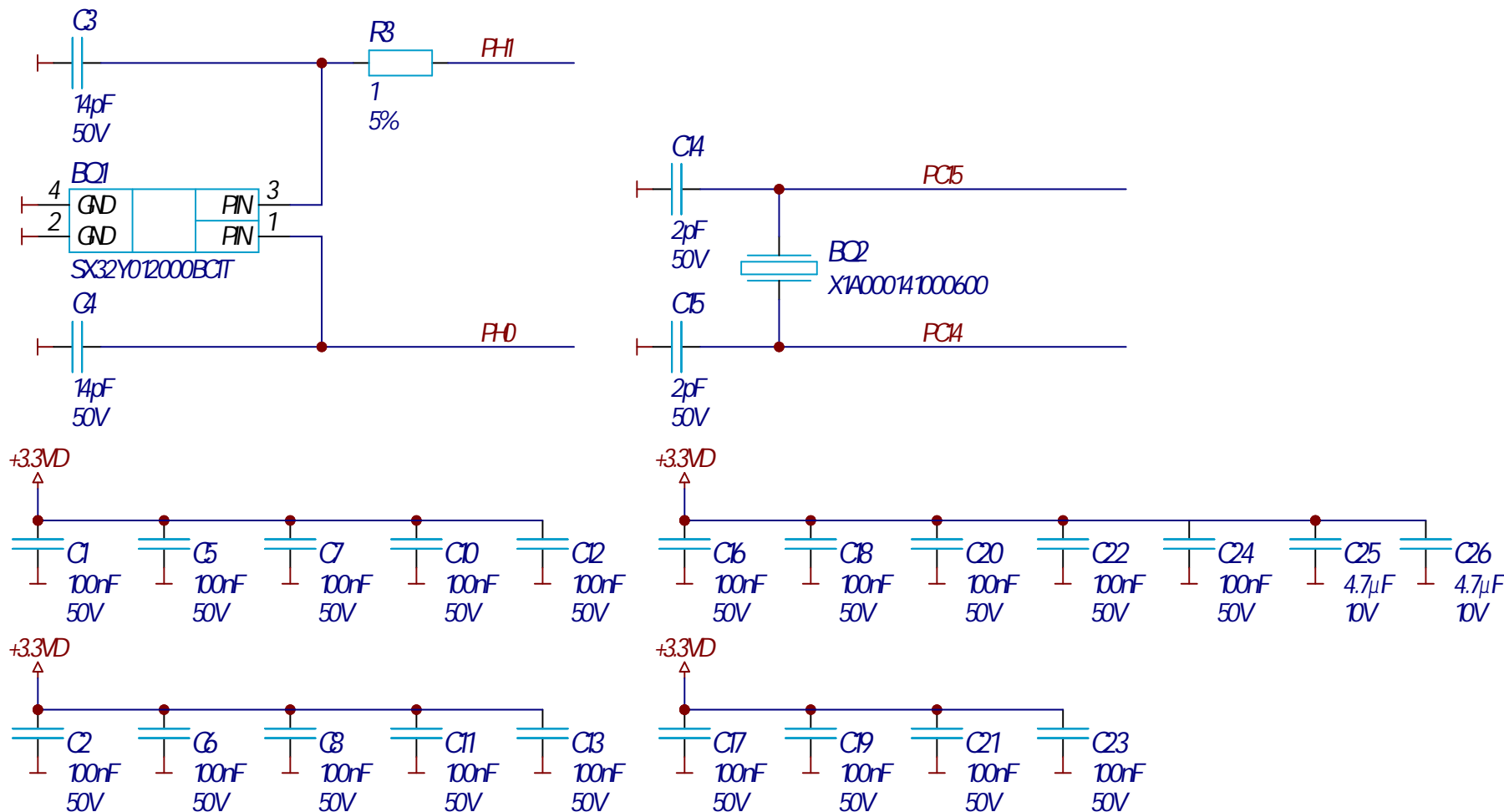
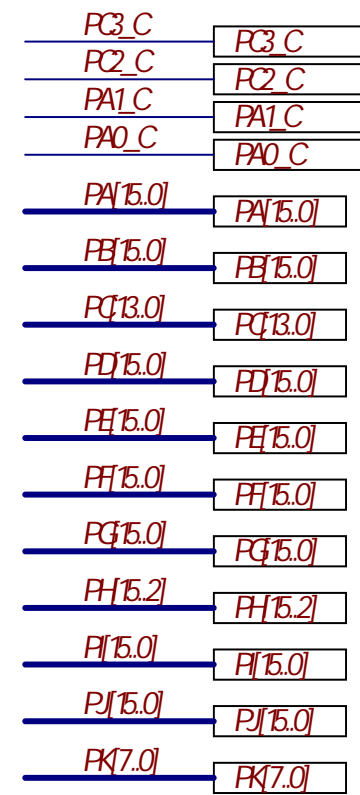
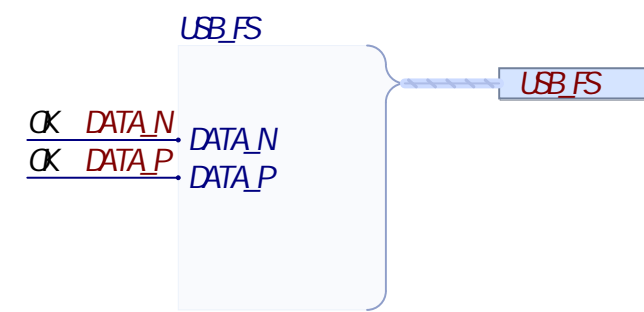
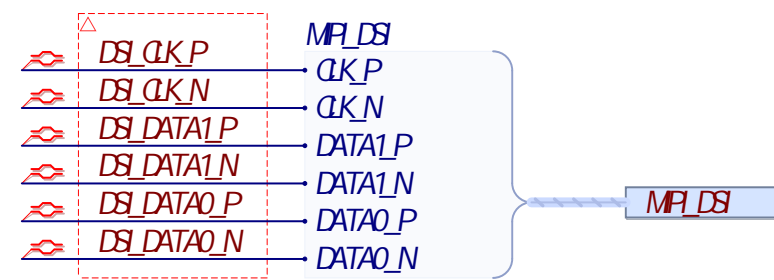
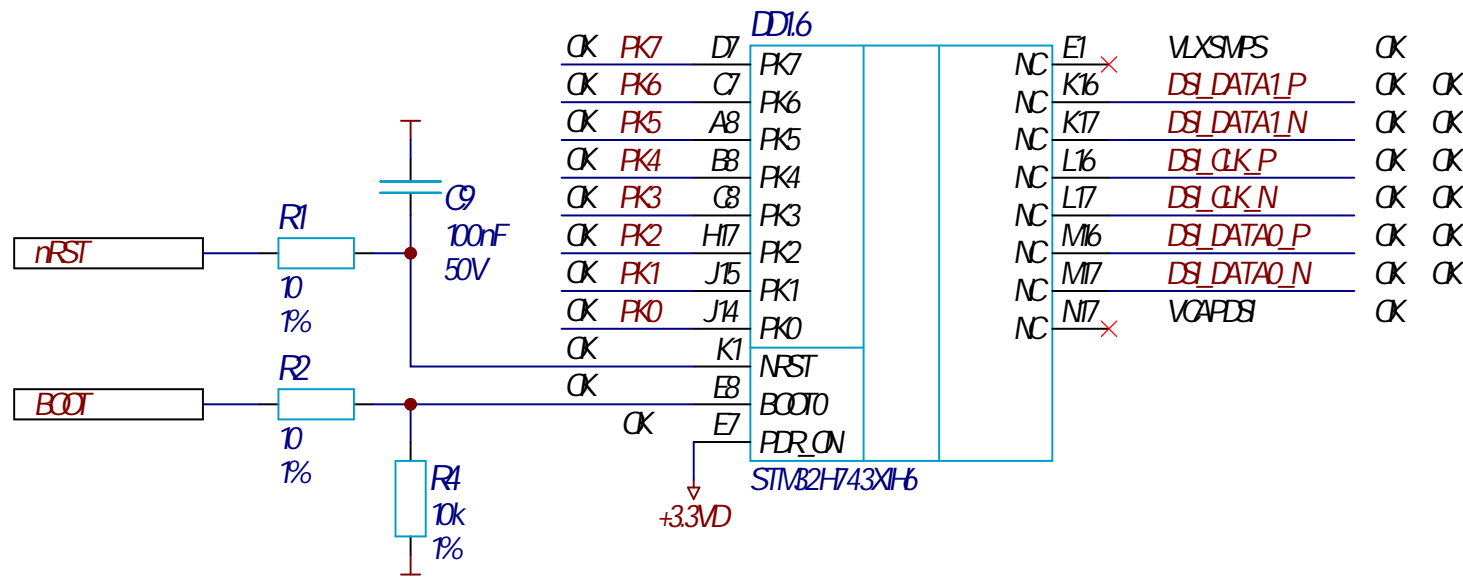
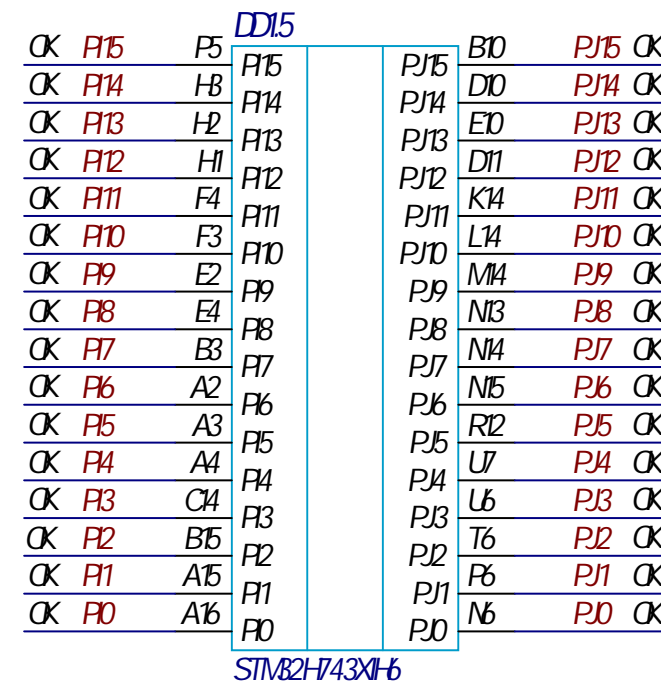
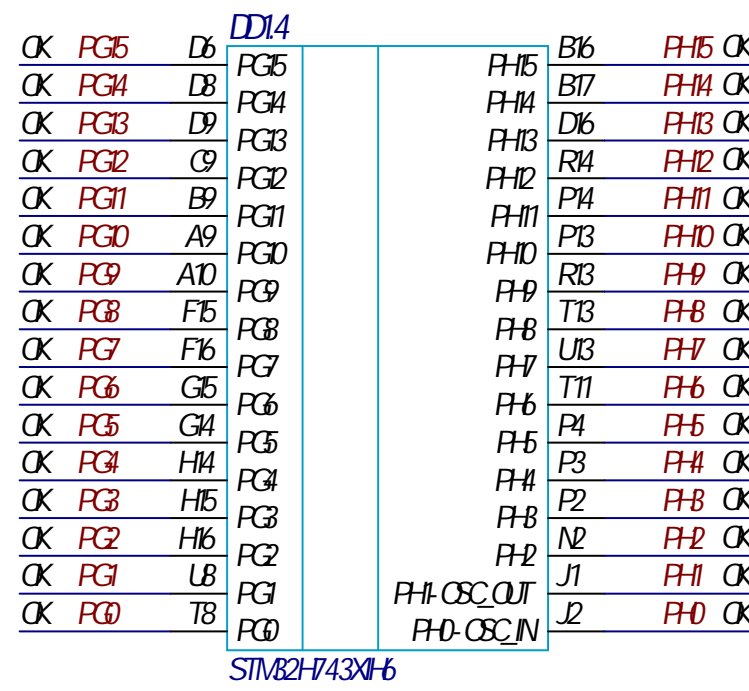
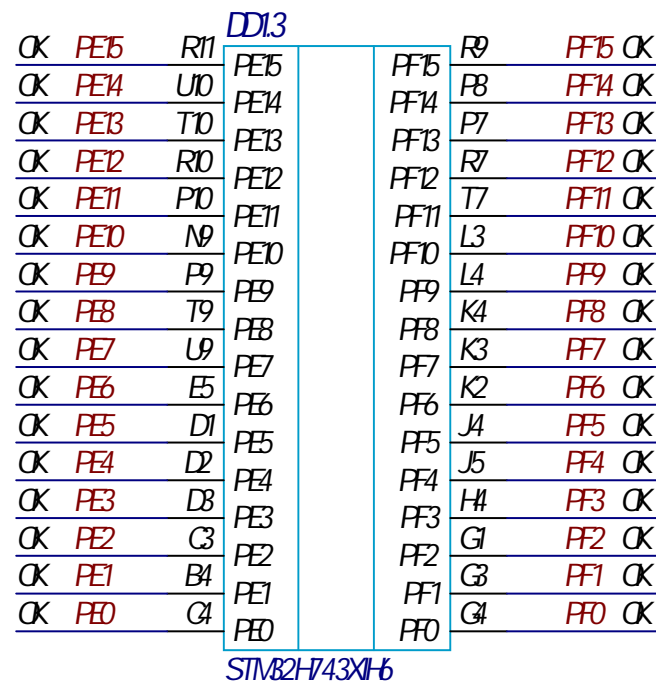
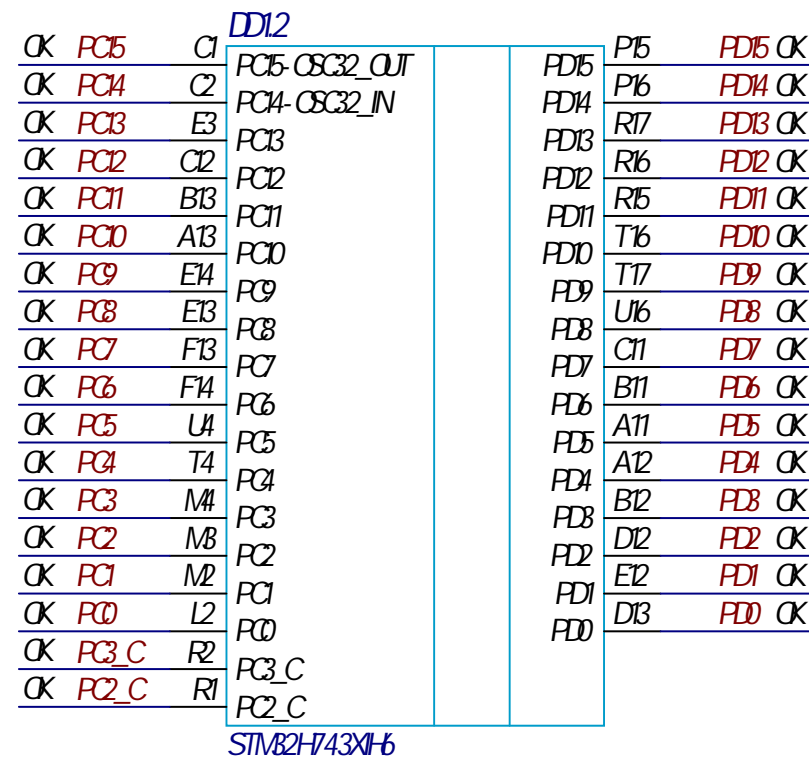
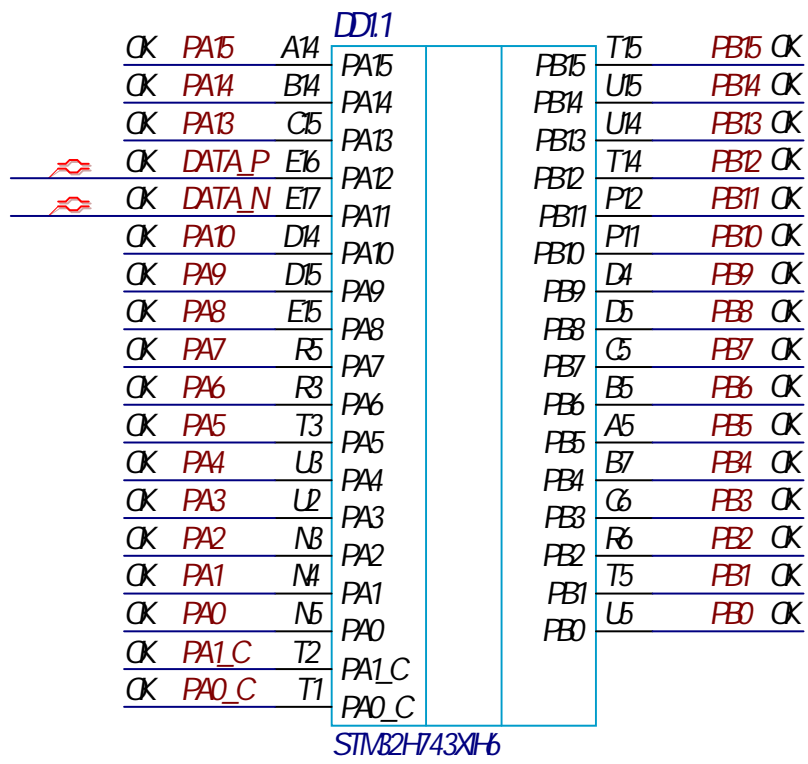


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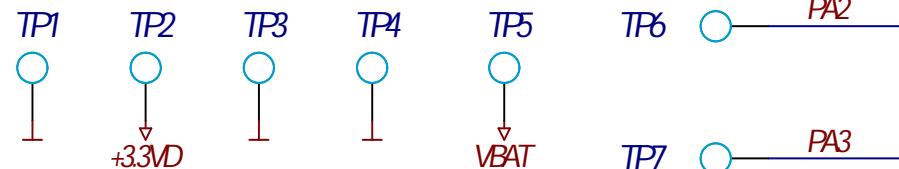
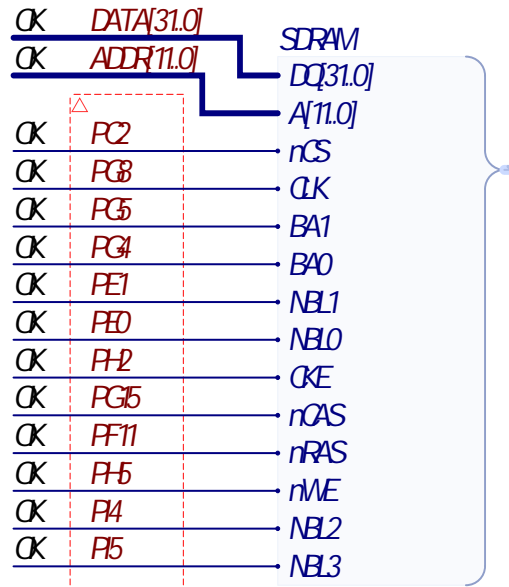
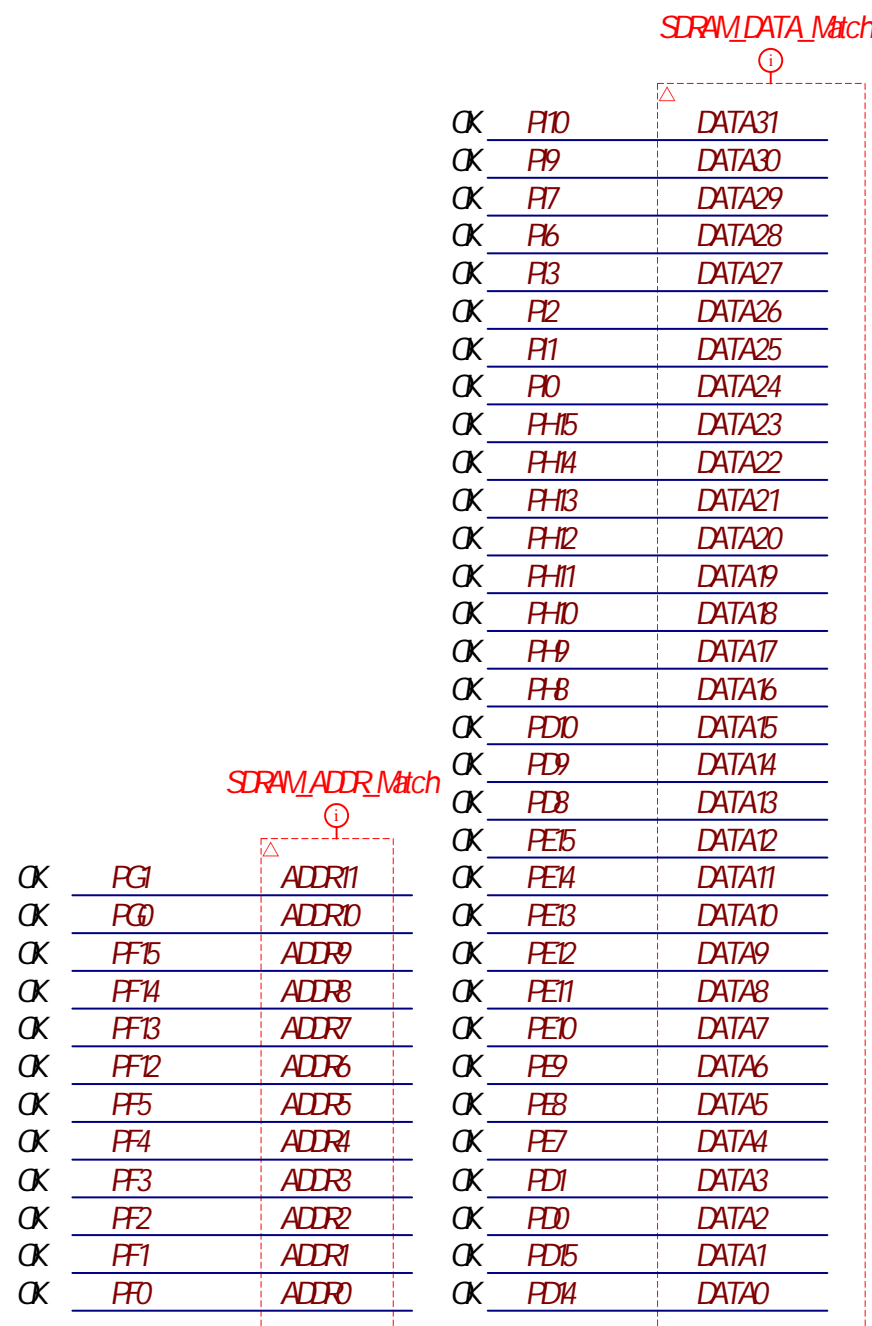
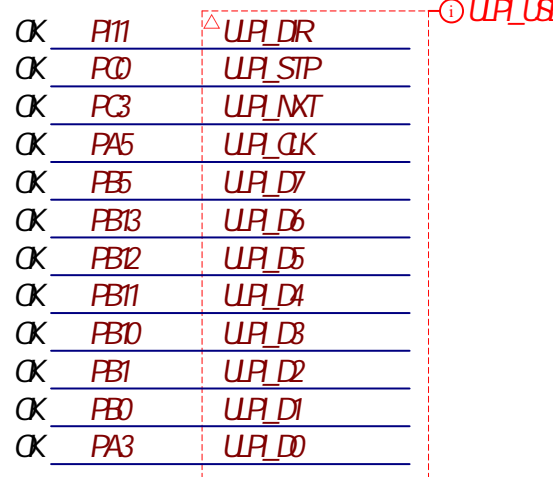
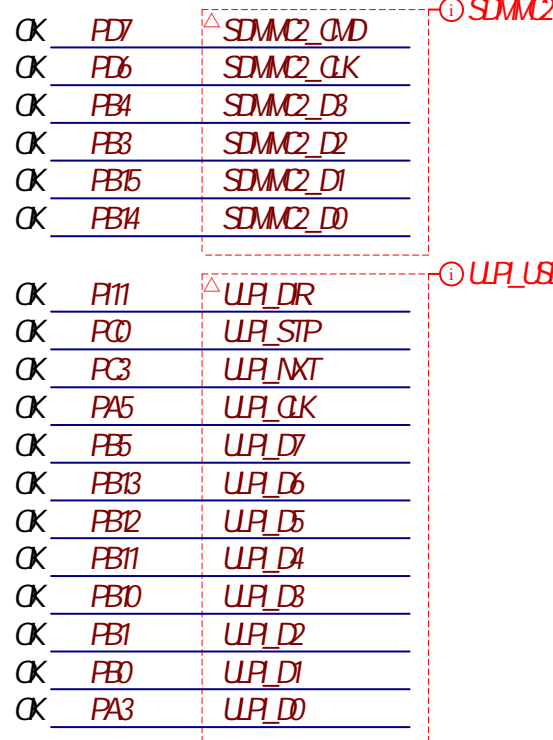
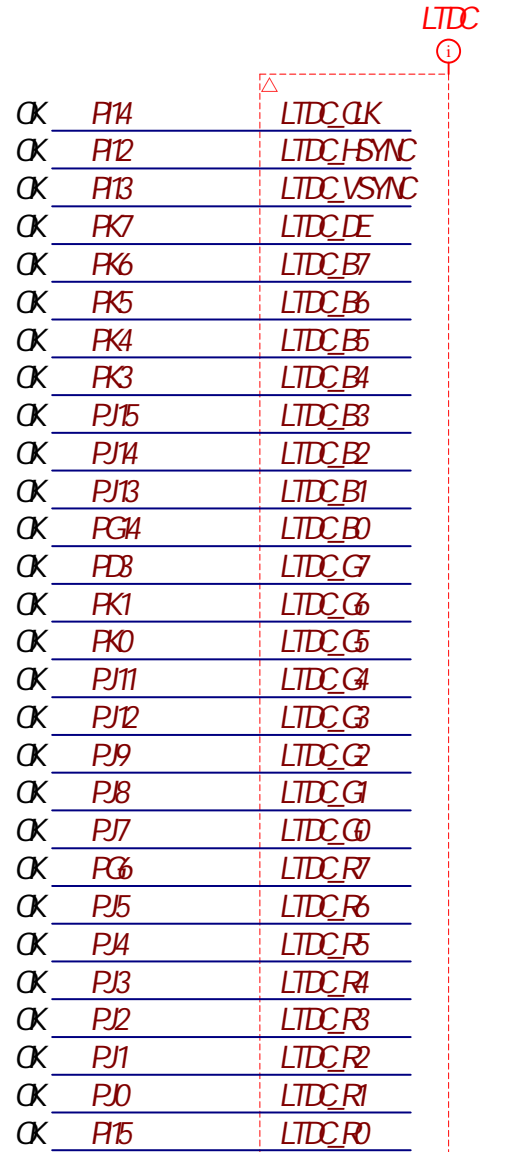
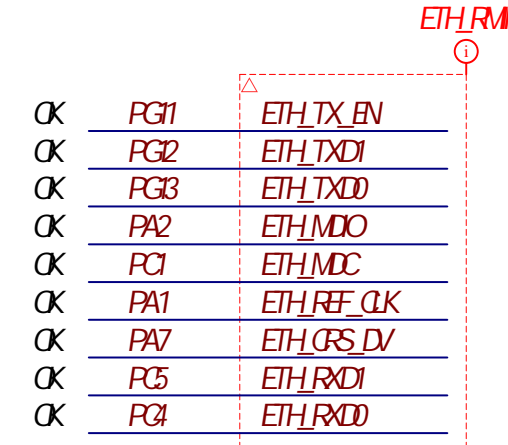
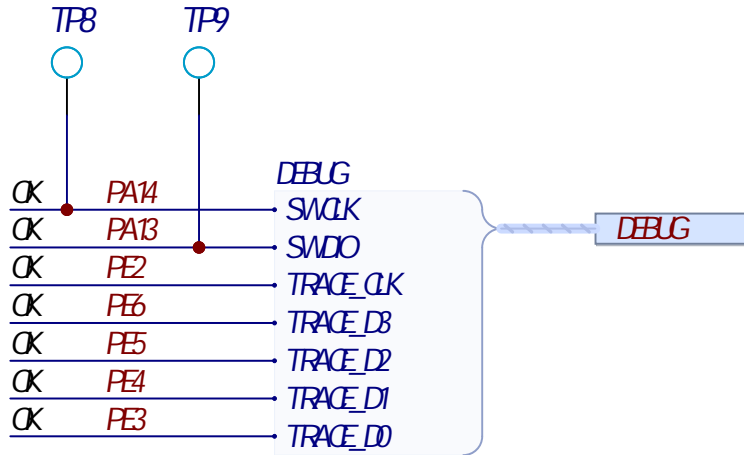
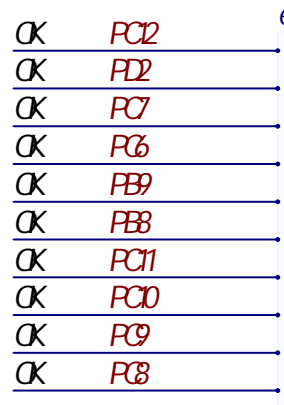
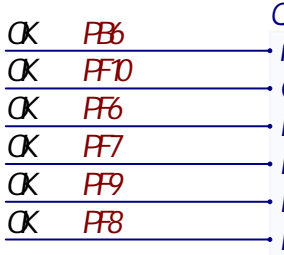
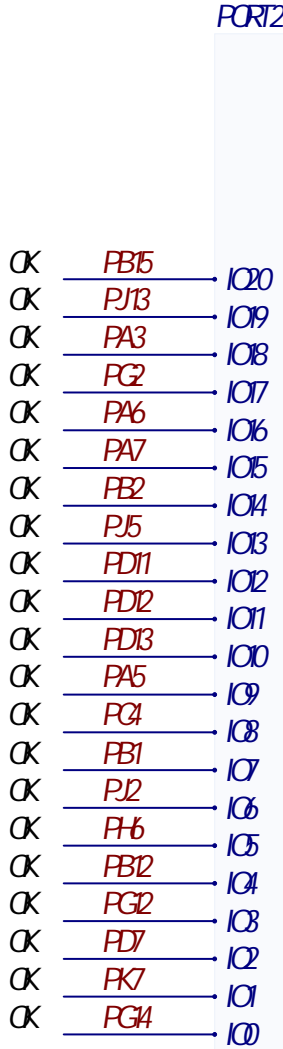
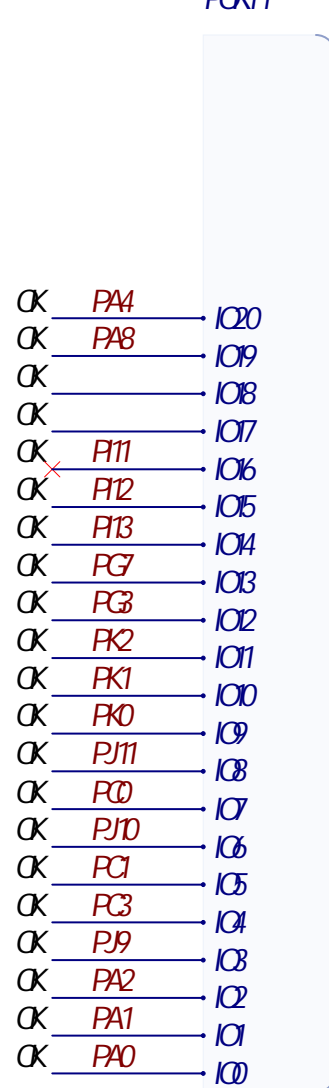
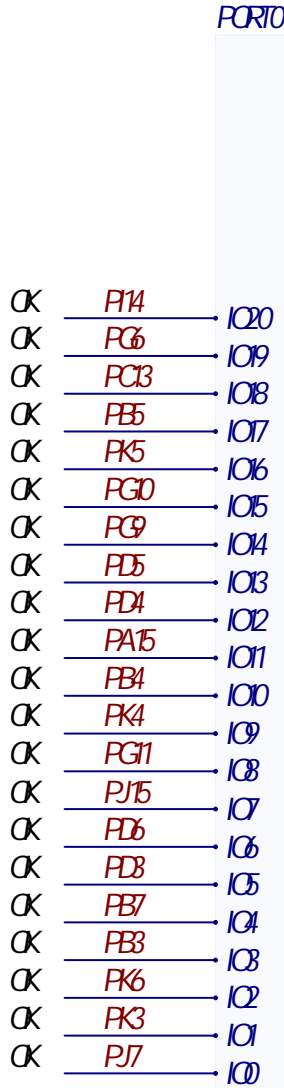
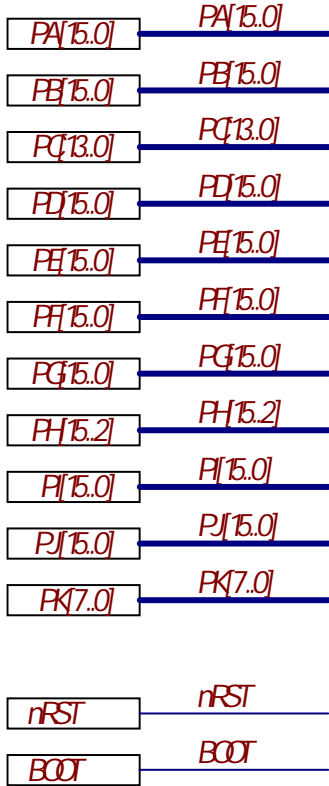
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bga240_str82h743xih6- V1	Лит.		Масштаб
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	Лист 1		Листов 7
Схема электрическая принципиальная	digilabs		

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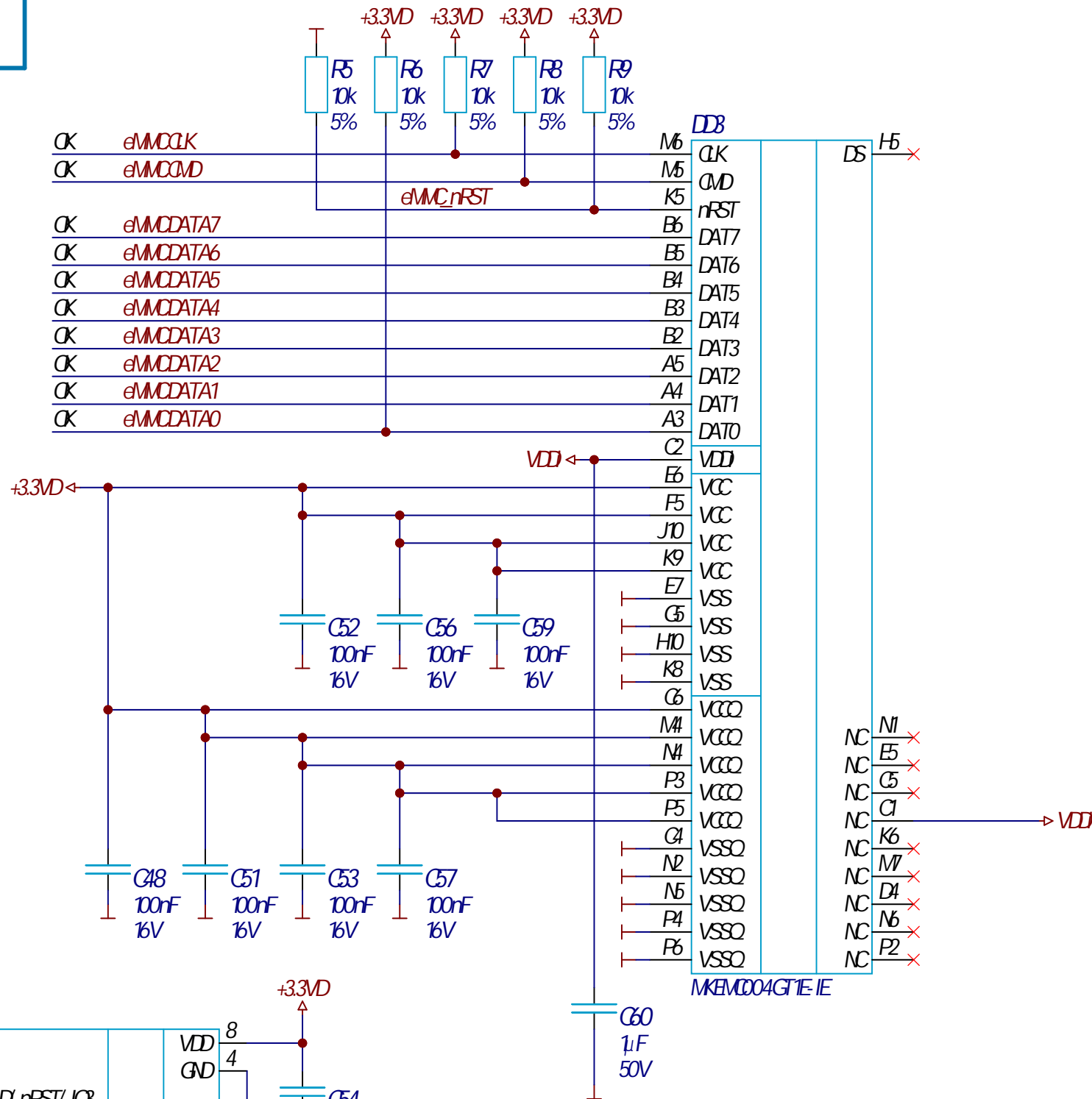


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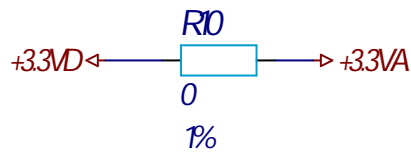
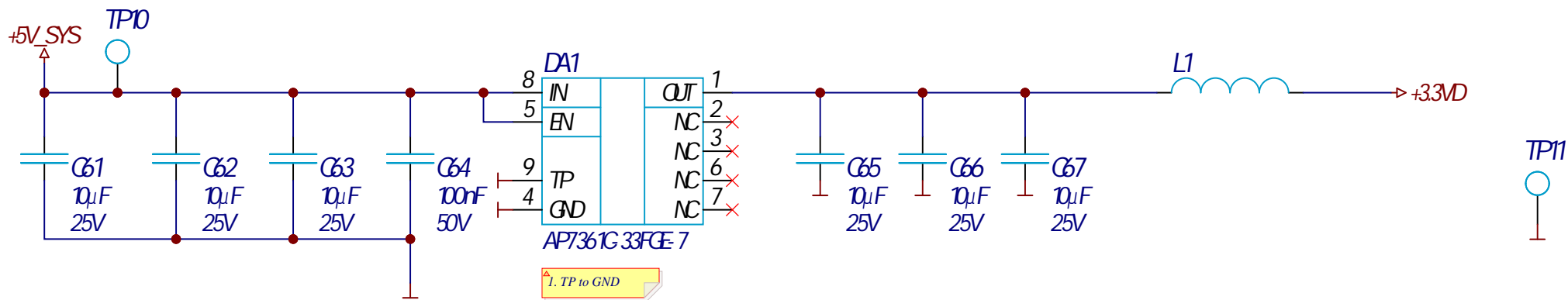
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OK	SDRAMA11	H9	DD4	A11	D081	E2	SDRAMD081	OK
OK	SDRAMA10	G7		A10	D080	D8	SDRAMD080	OK
OK	SDRAMA9	J3		A9	D029	D2	SDRAMD029	OK
OK	SDRAMA8	H2		A8	D028	B1	SDRAMD028	OK
OK	SDRAMA7	H1		A7	D027	C2	SDRAMD027	OK
OK	SDRAMA6	G3		A6	D026	A1	SDRAMD026	OK
OK	SDRAMA5	G2		A5	D025	C3	SDRAMD025	OK
OK	SDRAMA4	G1		A4	D024	A2	SDRAMD024	OK
OK	SDRAMA3	F3		A3	D023	A8	SDRAMD023	OK
OK	SDRAMA2	F7		A2	D022	C7	SDRAMD022	OK
OK	SDRAMA1	G9		A1	D021	A9	SDRAMD021	OK
OK	SDRAMA0	G8		A0	D020	C8	SDRAMD020	OK
				D019	B9	SDRAMD019	OK	
				D018	D8	SDRAMD018	OK	
				D017	D7	SDRAMD017	OK	
				D016	E8	SDRAMD016	OK	
				D015	R2	SDRAMD015	OK	
				D014	N8	SDRAMD014	OK	
				D013	R1	SDRAMD013	OK	
				D012	N2	SDRAMD012	OK	
				D011	P1	SDRAMD011	OK	
				D010	M2	SDRAMD010	OK	
				D009	M8	SDRAMD009	OK	
				D008	L2	SDRAMD008	OK	
				D007	L8	SDRAMD007	OK	
				D006	M7	SDRAMD006	OK	
				D005	M8	SDRAMD005	OK	
				D004	P9	SDRAMD004	OK	
				D003	N8	SDRAMD003	OK	
				D002	R9	SDRAMD002	OK	
				D001	N7	SDRAMD001	OK	
				D000	R8	SDRAMD000	OK	
				NC	E3	✗		
				NC	E7	✗		
				NC	H8	✗		
				NC	H7	✗		
				NC	K3	✗		
				NC	K2	✗		
				NC	B3			
				VSSQ	B8			
				VSSQ	C1			
				VSSQ	D1			
				VSSQ	E9			
				VSSQ	L9			
				VSSQ	M1			
				VSSQ	N1			
				VSSQ	P3			
				VSSQ	P8			
				VSSQ	A3			
				VSS	F1			
				VSS	L3			
				VSS	R3			
				VSS				

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PORT3	PORT3
PORT2	PORT2
PORT1	PORT1
PORT0	PORT0

DEBUG	DEBUG
LAST_FINS	LAST_FINS
MPI_DS	MPI_DS
USB_FS	USB_FS



PORT0.I020		X1
PORT0.I019		X2
PORT0.I018		X3
PORT0.I017		X4
PORT0.I016		X5
PORT0.I015		X6
PORT0.I014		X7
PORT0.I013		X8
PORT0.I012		X9
PORT0.I011		X10
PORT0.I010		X11
PORT0.I09		X12
PORT0.I08		X13
PORT0.I07		X14
PORT0.I06		X15
PORT0.I05		X16
PORT0.I04		X17
PORT0.I03		X18
PORT0.I02		X19
PORT0.I01		X20
PORT0.I00		X21

PORT1.I020		X30
PORT1.I019		X31
USB_FSDATA_P		X32
USB_FSDATA_N		X33
PORT1.I016		X34
PORT1.I015		X35
PORT1.I014		X36
PORT1.I013		X37
PORT1.I012		X38
PORT1.I011		X39
PORT1.I010		X40
PORT1.I09		X41
PORT1.I08		X42
PORT1.I07		X43
PORT1.I06		X44
PORT1.I05		X45
PORT1.I04		X46
PORT1.I03		X47
PORT1.I02		X48
PORT1.I01		X49
PORT1.I00		X50

PORT2.I020		X59
PORT2.I019		X60
PORT2.I018		X61
PORT2.I017		X62
PORT2.I016		X63
PORT2.I015		X64
PORT2.I014		X65
PORT2.I013		X66
PORT2.I012		X67
PORT2.I011		X68
PORT2.I010		X69
PORT2.I09		X70
PORT2.I08		X71
PORT2.I07		X72
PORT2.I06		X73
PORT2.I05		X74
PORT2.I04		X75
PORT2.I03		X76
PORT2.I02		X77
PORT2.I01		X78
PORT2.I00		X79

PORT3.I021		X90
PORT3.I020		X91
PORT3.I019		X92
PORT3.I018		X93
PORT3.I017		X94
PORT3.I016		X95
PORT3.I015		X96
PORT3.I014		X97
PORT3.I013		X98
PORT3.I012		X99
PORT3.I011		X100
PORT3.I010		X101
PORT3.I09		X102
PORT3.I08		X103
PORT3.I07		X104
PORT3.I06		X105
PORT3.I05		X106
PORT3.I04		X107
PORT3.I03		X108
PORT3.I02		X109
PORT3.I01		X110
PORT3.I00		X111

LAST_FINSnRST		X22
LAST_FINSBOOT		X23

MPI_DS.CLK_P		X24
MPI_DS.CLK_N		X25
MPI_DS.DATA1_P		X26
MPI_DS.DATA1_N		X27
MPI_DS.DATA0_P		X28
MPI_DS.DATA0_N		X29

DEBUGSWCLK		X51
DEBUGSWDO		X52
DEBUGTRACE_CLK		X53
DEBUGTRACE_D3		X54
DEBUGTRACE_D2		X55
DEBUGTRACE_D1		X56
DEBUGTRACE_D0		X57

+5V_SYS <		X80
+5V_SYS <		X81
+5V_SYS <		X82

	X83		X112		X122
	X84		X113		X123
	X85		X114		X124
	X86		X115		X125
	X87		X116		X126
	X88		X117		X127
	X89		X118		X128

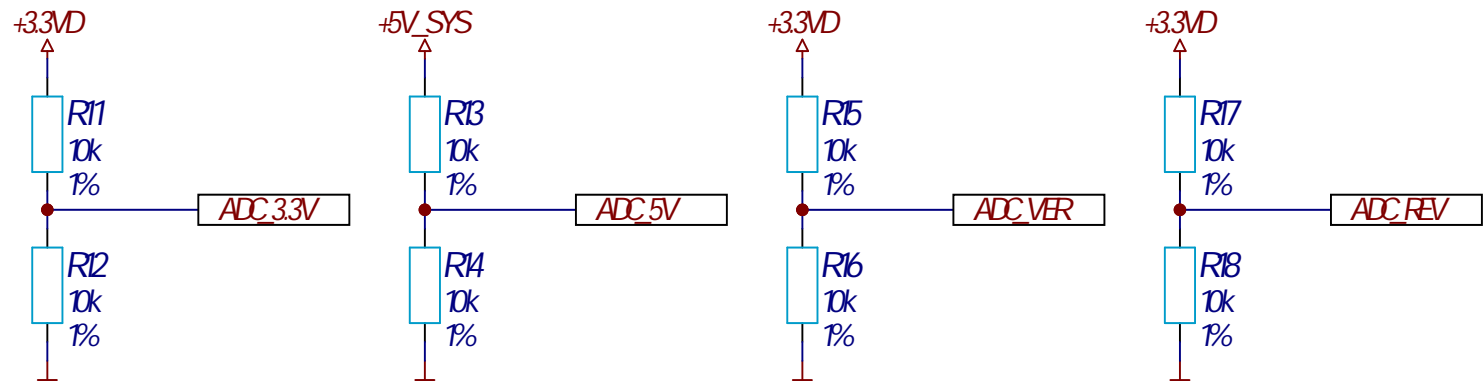
	X119
	X120
	X121

Изм. №	Дата	Взам. инв. №	Инд. № дубл.	Подп. дата

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