

A

B

C

D

A

B

C

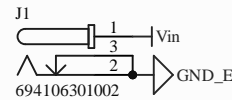
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2

3

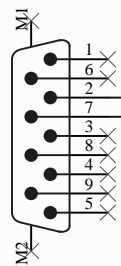
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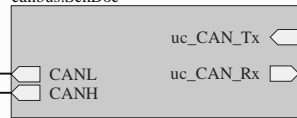
U_Power
Power.SchDoc



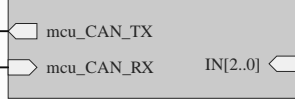
P2
618009231221



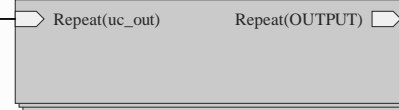
U_canbus
canbus.SchDoc



U_MCU
MCU.SchDoc

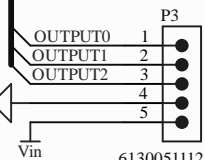


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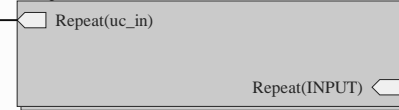
OUT

OUTPUT OUTPUT[2..0]



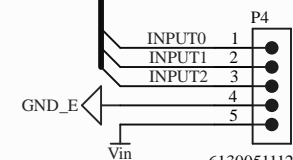
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IN

INPUT INPUT[2..0]



61300511121

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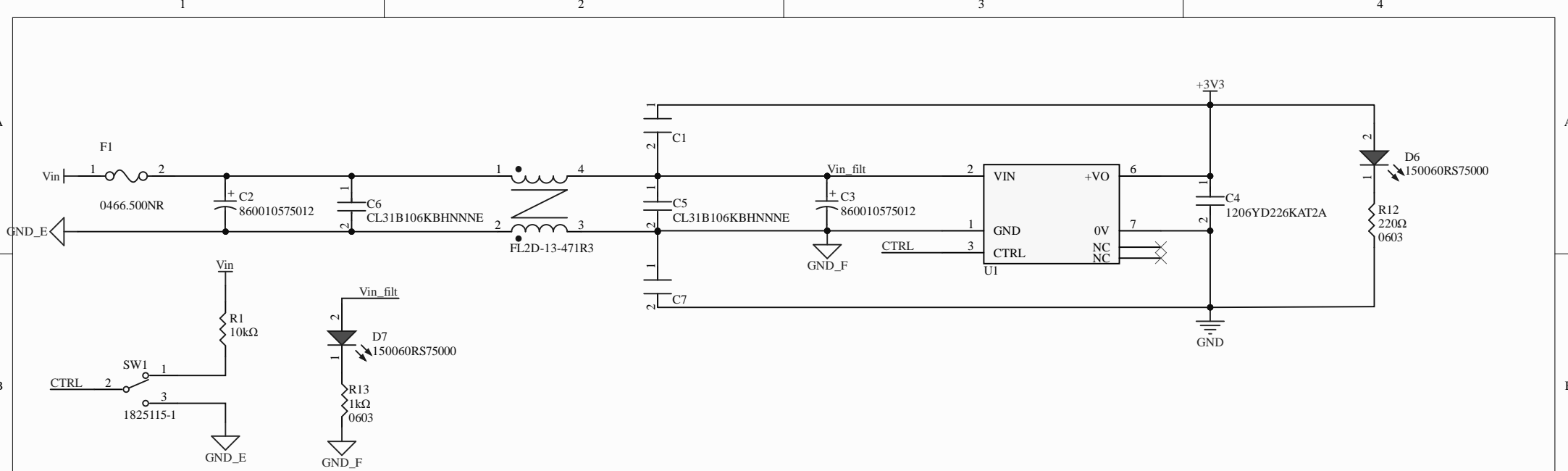
Size: A4 Number: 24.9.1.31 Revision: V0.0

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2. EMC compliance circuit

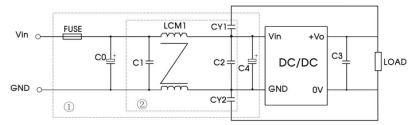


Fig. 3

Notes: We use Part ① in Fig. 3 for Immunity test and part ② for Emissions test. Selecting based on needs.

Parameter description:

Model	Vin: 12VDC	Vin: 24VDC
FUSE	Choose according to actual input current	
C0/C4	330μF/35V	330μF/50V
C1/C2	10μF/50V	
C3	Refer to the Cout in Fig2	
LCM1	470μH, recommended to use MORNSUN's FL2D-13-471R3	
CY1/CY2	1nF/400VAC	

Input Filter	Capacitance Filter			
Hot Plug	Unavailable			
Ctrl *	Module on	Ctrl pin open or pulled high (3.5-12VDC)		
	Module off	Ctrl pin pulled low to GND (0-1.2VDC)		
	Input current when off	-	6	10 mA
Note: *The Ctrl pin voltage is referenced to input GND.				

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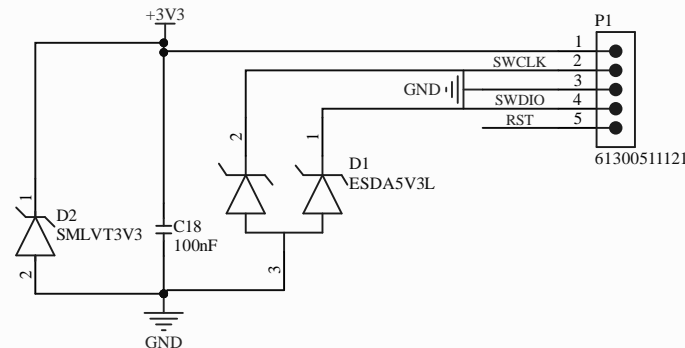
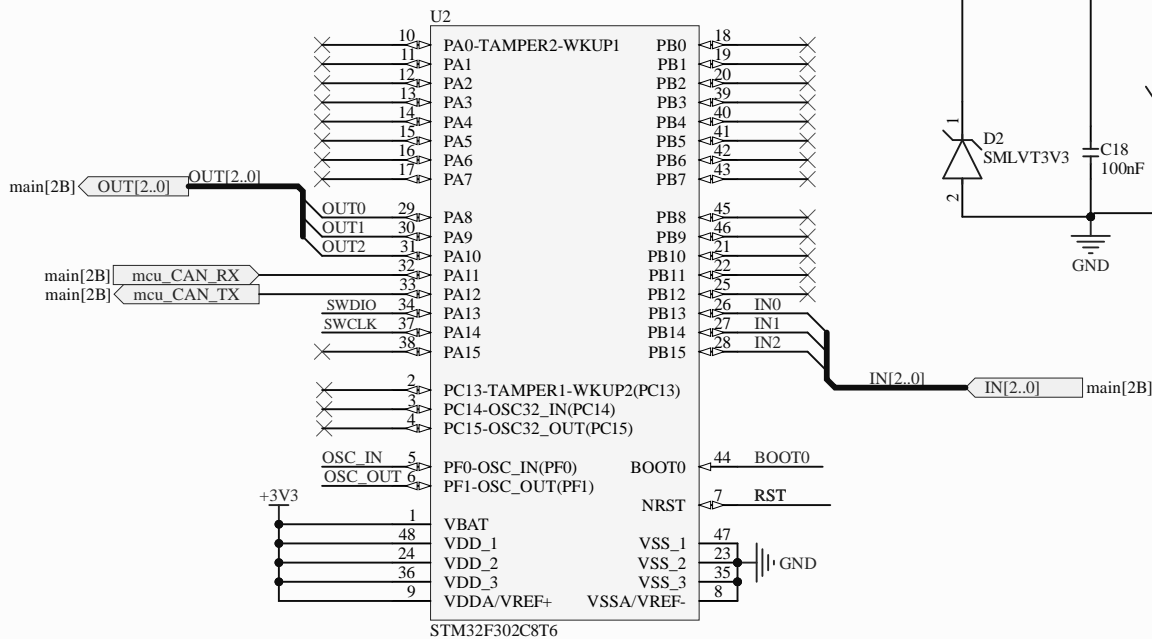
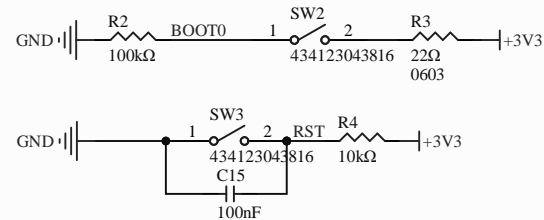
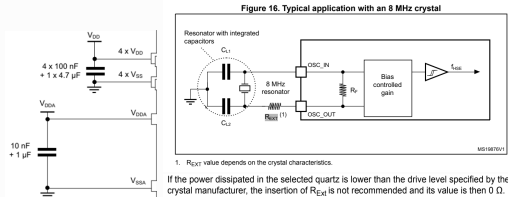
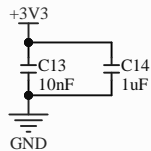
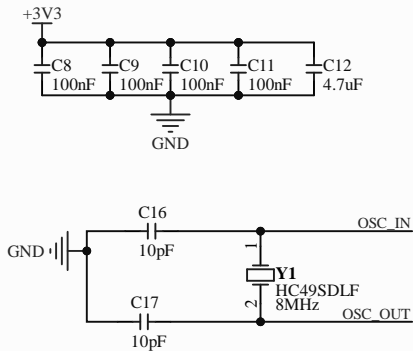
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A

B

C

D



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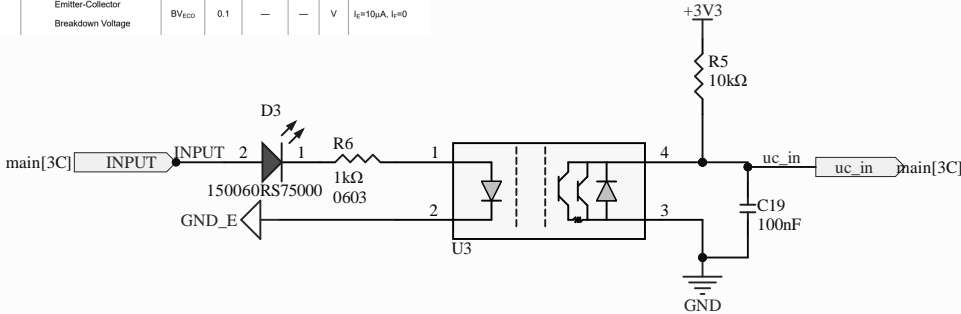
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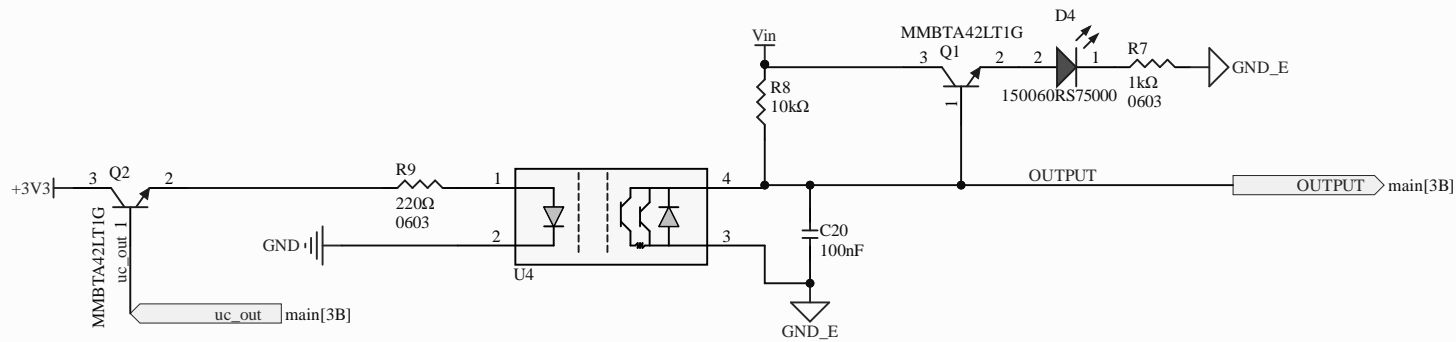
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Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Condition
Input	Forward Voltage	V_f	—	1.2	1.4	V
	Reverse Current	I_R	—	10	μA	$V_R=4V$
	Terminal Capacitance	C_t	—	30	250	pF
Output	Collector Dark Current	I_{CDO}	—	200	nA	$V_{CE}=200V, I_B=0$
	Collector-Emitter Breakdown Voltage	BV_{CEO}	300	—	V	$I_C=0.1mA, I_B=0$
	Emitter-Collector Breakdown Voltage	BV_{ECO}	0.1	—	V	$I_E=10\mu A, I_B=0$



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CAN BUS

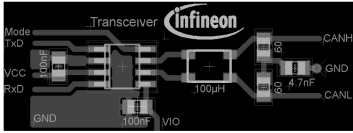
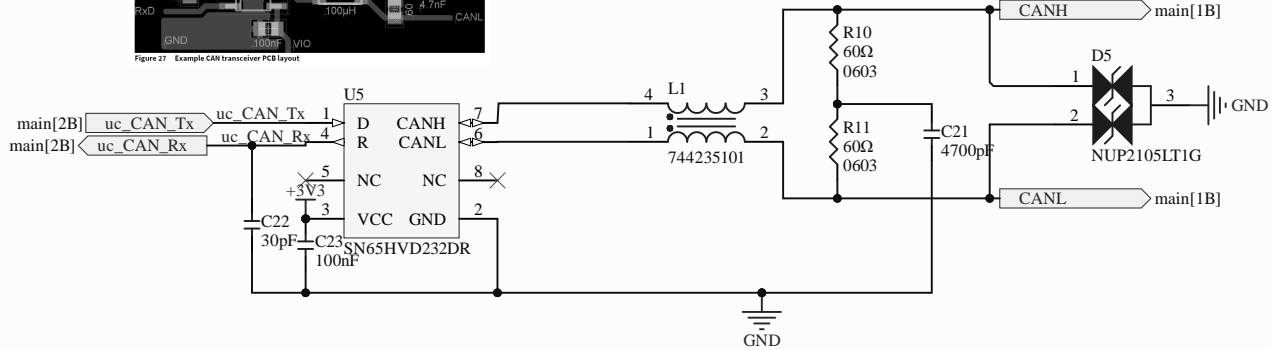


Figure 27 Example CAN transceiver PCB layout



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