CONTACT

Mobile:
+8801763476757

Email:
farzanaece2k15@gmail.com

Website:
http://farzana780.pythonanywhere.com

LinkedIn:
https://www.linkedin.com/in/farzana-bulbul-097b49198/

GitHub:
https://github.com/farzana780

SKILLS

Programming Language:
Python,C/C++, JavaScript, reactJS, MATLAB, PHP.

Libraries: NumPy, Pandas.

Web Framework: Django, Django rest.

Database: MySQL, SQLite, PostgreSQL.

OS:MS Windows, Linux Ubuntu.

TOOLS

IDE: PyCharm, CodeBlocks.

Platform: Python Anywhere.

Software: MS Office, Xilinx.

FARZANA BULBUL

Junior software engineer

I have so much interest in Software development. I always enjoy developing web applications. Utilizing my skills and knowledge efficiently for organizational growth, I want to continue my learning and pursue a highly rewarding career in software development.

EDUCATION

BSc in Electronics & Communication Engineering (ECE) March 2020
Khulna University of Engineering & Technology
Khulna, Bangladesh
CGPA: **3.45** in scale of 4.00

EXPERIENCE

- **Junior Software Engineer**(September 2015-Present):
  I am developing company's current corporate website using django framework. Mainly I am working the backend side.

PERSONAL PROJECTS

- **E-commerce website:**
  I developed a fully functional ecommerce website for a company named Harmony Trade International. In this website, I used HTML5, Bootstrap, JavaScript and jQuery for the frontend and the backend was built using Python Django. Here is the link:
  https://harmonytrades.pythonanywhere.com/
- **Blog:**
  In this project I have used Django rest framework and also used the class base and generic views for user authentication.
- **Text Editor:**
  This is a web app where the text can be edited and the backend is built by using python Django framework.
- **Treasure Hunt:**
  It's another web app where the treasures' image and information can be stored. The backend is designed using Python Django framework.

CERTIFICATION

- Getting Started with Python (Coursera)
- Intro to Machine Learning (Kaggle)
- Python (Kaggle)

UNDERGRADUATE THESIS

Title: Brain Abnormality Detection from MRI Image Based On FPGA.

Description: "I proposed a model where MRI images were used as input of an FPGA to detect the brain abnormality. I used Verilog to create the model and was able to successfully detect the abnormal locations on the MRI image."